

J2

5	GND
4	NRST
3	SWCLK
2	SWDIO
1	3V3

ST-Link headers

J1

5	5V
4	GND
3	3V3
2	UART_RX
1	UART_TX

Prgm headers

J3

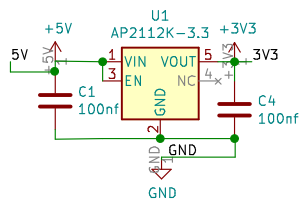
PA0	1
PA1	2
PA2	3
PA3	4
PA4	5
PA5	6
PA6	7
PA7	8
PA8	9
PA11	10
PA12	11
PA15	12

J4

12	PB0
11	PB1
10	PB2
9	PB3
8	PB4
7	PB5
6	PB6
5	PB7
4	PB8
3	PB9
2	PE4
1	GND

Extra Connectors

Power



Sheet: /		File: stm32wb_Dev_v1.sch
Title:		
Size: A4	Date:	Rev:
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