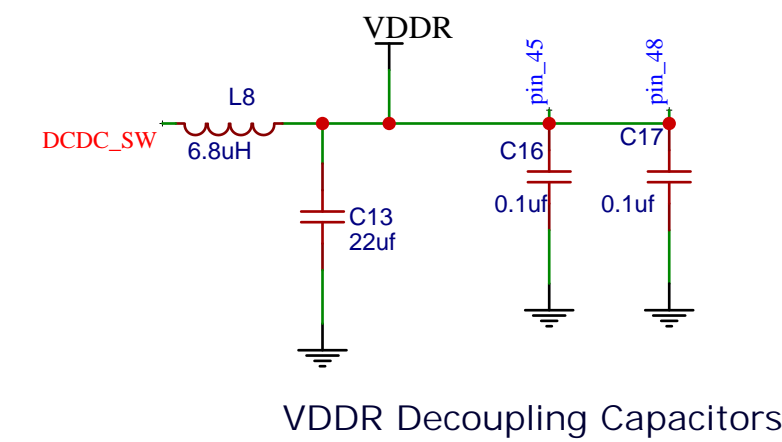
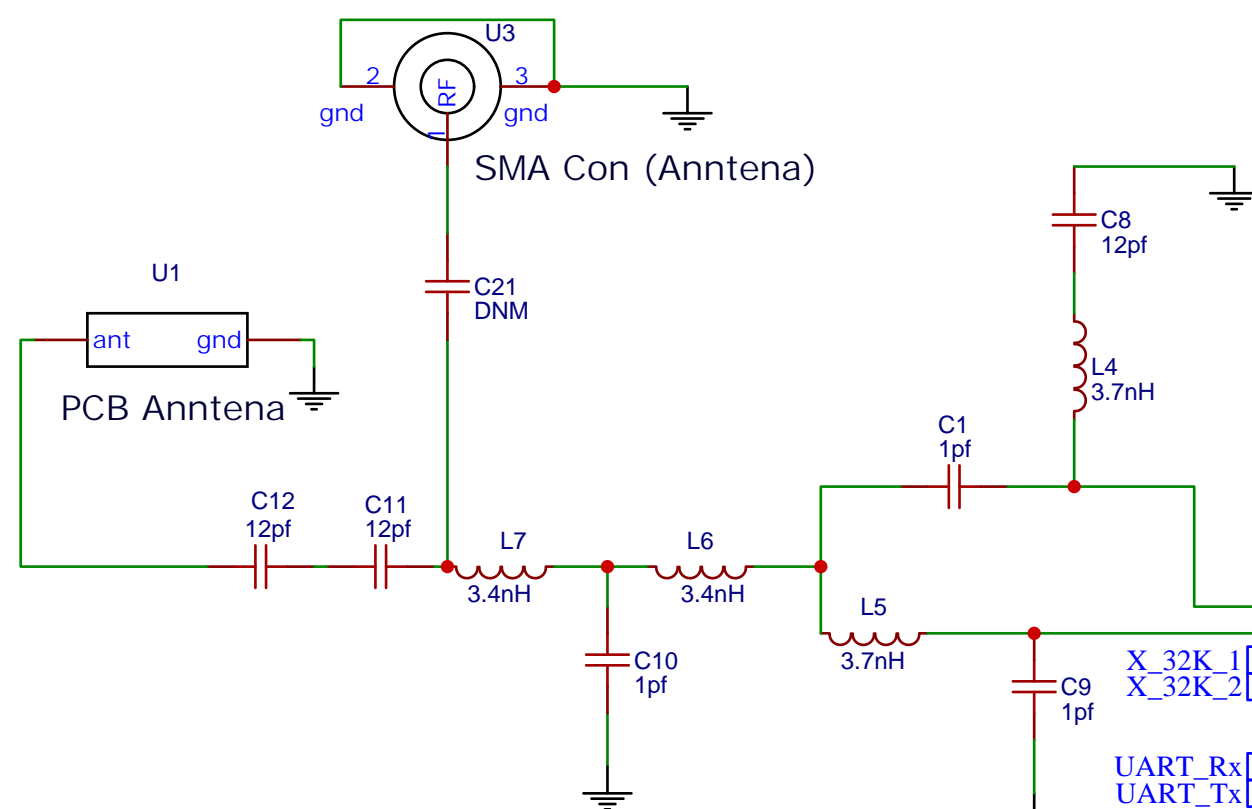
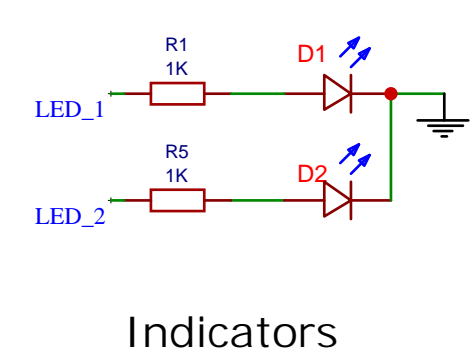
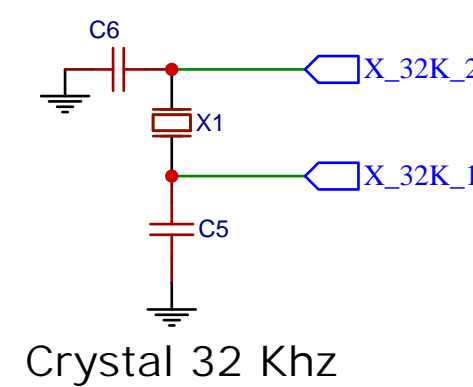
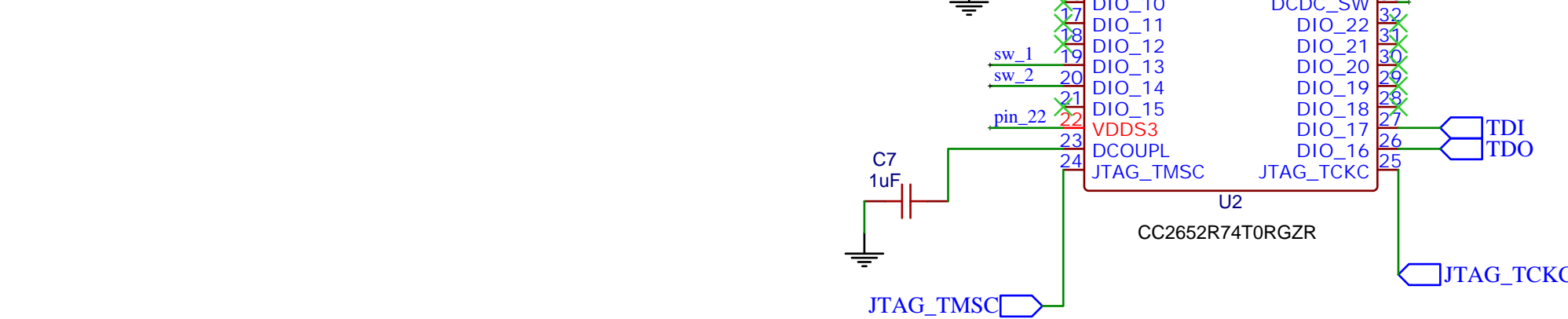
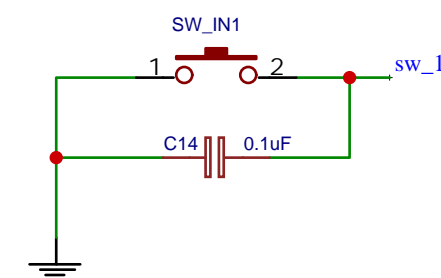
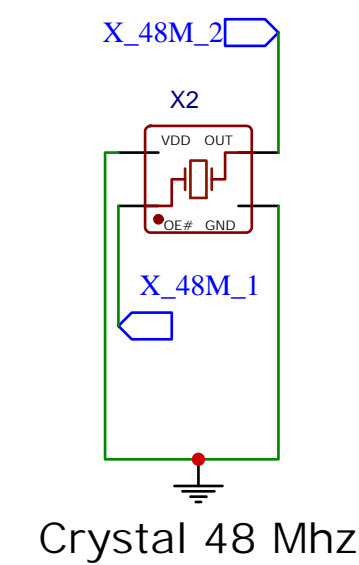
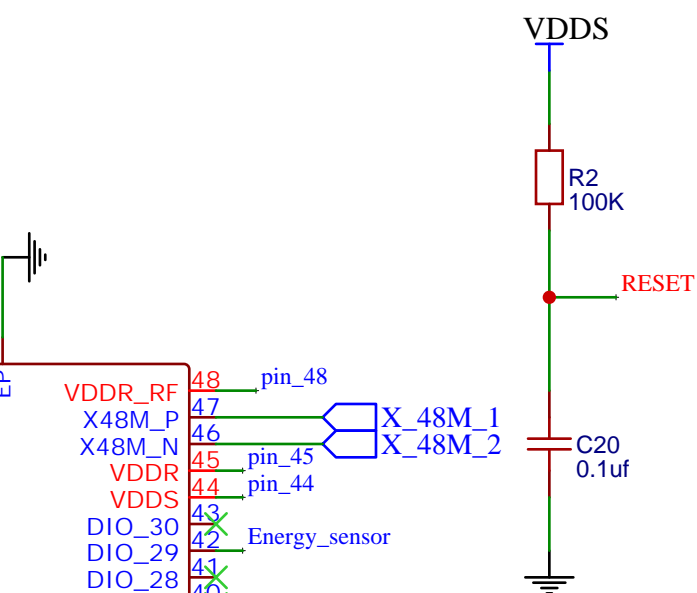


Input_frm_Backend



VDDR Decoupling Capacitors



Switch_input

