

Registro UBRR para la velocidad de comunicación UART

$F - CPU = 16000000Hz$

$Baud = 9600$

$UBRR = \frac{16000000}{16 \times 9600} - 1 = 103.166$

PWM (Timer 1 - 16 bits)

$F - PWM = 50Hz(20ms)$

$Prescaler = 8$

$\frac{\left(\frac{16000000}{8}\right)}{50} = 40000ciclos$

$ICR1 = \frac{\left(\frac{16000000}{8}\right)}{50} - 1 = 39000$

$1ms(0^\circ) = \frac{1ms}{\left(\frac{20ms}{40000}\right)} = 2000$

$1.5ms(90^\circ) = \frac{1.5ms}{\left(\frac{20ms}{40000}\right)} = 3000$

$2ms(180^\circ) = \frac{2ms}{\left(\frac{20ms}{40000}\right)} = 4000$

PWM (Timer 0 y Timer 2 - 8 bits)

$F - PWM = 50Hz(20ms)$

$Prescaler = 256$

$F = \frac{16000000}{256} = 62500Hz$

$\frac{256}{62500} = 4.096ms$





