

Name:Sachin Mahadev Jadhav
Address: At Itti, Post-Poharegaon, Tal: Renapur,
Dist: Latur, Pin - 413512.
Contact NO:9850823263
E-mail: jadhavsa996@gmail.com



1 Career Objective:

Utilization of knowledge and Technical skills along with design abilities to promote growth and development of the organization and self by implementing my innovative ideas, skills and creativity.

2 Academic Details:

Sr No:	Class	Year of Passing	institute	Grades
1.	TY B.Tech	2017-18	WCE,Sangli	9.3
2.	SY B.Tech	2016-17		9.12
3.	FY B.Tech	2015-16		8.78
4.	HSC	2014-15	Dayanand Science college,Latur	91.38
5.	SSC	2012-13	Renuka Vidyalay Bhokaramba	91.45

3 Projects Undertaken:

1. Biomorphic Hyper Redundant Snake Robot.
 - Under E-Yantra 2017 robotic competition organized by IITB.
-Using joystick communication and 3D designing of snake and Servo actuator.
2. Line following based multipurpose system for hospitals.
 - India innovation challenge design contest by Texas Instruments and IIM Bangalore.
 - Idea is to make clone for doing multiple tasks such as sweeping,cleaning,dustbin collection,medicine delivery
3. Pocket DSO
 - Third year sem 2 miniproject
 - Cost and Power efficient portable DSO using GLCD with MSP430.

4. Anti-pilferage and anti-adulteration system for fuel road tankers.
 - Smart India Hackthon 2018
 - Solution to the pilferage and adulteration of fuel tanks en-route from terminals to retailer by continuous monitoring of location ,level, pressure and temperature parameters with cloud connectivity also ensuring emergency management.
5. Digital Trekking Watch.
 - TY BTech sem 1 Mini project
 - OLED based compass and location tracking using magnetometer and GPS.
6. Modeling 3D terrain in blender
 - EYantra 2016 robotic championship by IITB
 - 3D modelling of a terrain in Blender using X-bee comm. with Firebird V.

4 Training and Workshop:

1. Successfully completed the online course on Hardware Modelling Using Verilog conducted by **IIT KHARAGPUR** under NPTEL.
2. Successfully completed workshop on ”**Analog VLSI Design with Emphasis on OP-Amp Design**”.

5 Technical skills:

1. **Programing Skills:**
 - C, Basic C++ and Python.
 - VHDL(Basic), VERILOG, Embedded C.
2. **Microcontrollers Known:**
 - LPC2148 (ARM7),(low power microcontroller) MSP430G2553, MSP432P401R , P89V51RD2(8051), ATMEGA328P, ATmega2560.
3. **3D Designing Software:** Blender 2.7, Autodesk Fusion 360.
4. **Simulation Software:**Proteus, Multisim 13,PSIM,VREP.
5. **Firmware Orientation:**ATMEL 7.0, vision 4 (keil), Code Compose Studio, Xilinx ISE 14.2.
6. **PCB Designing Software:** Eagle

6 EXTRA-CURRICULAR ACTIVITIES:

1. Coordinator of Cybermind committee in National level Technical Symposium **VISION 2K18** organized by WCE, Sangli.
2. Currently working as Technical Director of **ELESA**.(ELectronics Engineering Student Association).
3. Also worked as Joint(2016-17) and Assistant(2015-16) Technical director of **ELESA**.

7 CO-CURRICULAR ACTIVITIES:

1. Secured 4th rank with consolation prize in **E-yantra 2017** National robotics competition organized by **IIT Bombay**.
2. Currently shortlisted for second stage of **Smart India Hackathon 2018**.
3. Shortlisted in IICDC 2017 Quarterfinals organized by **Texas instruments and IIM banglore** .
4. Semifinalist in E-Yantra 2016.

8 SOFT SKILLS:

1. Ability to work under pressure.
2. flexibility, fast learning.
3. Good teamwork and Time management.

9 Personal Details:

- Father's Name: Mahadev Jadhav
- Mother's Name: Sunita Mahadev Jadhav
- Sex:Male
- Date of Birth: 22 September 1997
- Nationality: Indian
- Marital Status: No

10 Declartion:

- I hereby declare that the above written particulars are true to the best of my knowledge and belief.

11 Date: *19 april 2018*