

SACHIN BASAVANI SHIVASHANKARA

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EDUCATION

Master of Science | Computer Engineering | Syracuse University, NY, USA | GPA: 3.67 [Dec 2018]

- Object Oriented Design | Software Modelling and Analysis | Advanced Data Structures and Algorithms | Internet Programming | Data, Knowledge and Media Mining | Design patterns | Advanced Computer Architecture

Bachelor of Engineering | Electrical and Electronics Engineering | UVCE, Bangalore University, India | GPA: 3.74 [June 2014]

- Analog Electronics Circuits | Computer Architecture | Microprocessors and Microcontrollers | Electrical and Electronics Measurement | Power Electronics | Transmission and Distribution | Power Systems

TECHNICAL SKILLS AND INTERESTS

Programming Languages: C++, C#, Python, SQL, HTML, CSS, JavaScript, R, Java, C, Assembly level programming

Tools and Technologies: Visual Studio, WCF, WPF, ASP.NET MVC, RStudio, PSPICE, LTSPICE, SmarTest (Advantest), Windows, Linux, UNIX

TECHNICAL PROJECTS

Remote Build Server | C# | Automated Testing | Continuous Integration | WCF | WPF [Dec 2017]

- Implements Client, Repository, Build Server and Test Harness to create a federated system that supports automated building and testing of libraries (built from source code files) from a test request

Dependency based Code Publisher | C++ [Apr 2017]

- Publishes source code files (from a repository) as web pages with embedded child links. Each link refers to a code file that the displayed code file depends on

Type based Dependency Analysis | C++ [Mar 2017]

- Extracts lexical content from source code files and performs type and dependency analysis
- For each file in a specified file collection, finds, all other files on which they depend

UniCart | Web Application | Java | HTML | CSS | JavaScript | MySQL | PHP [Apr 2018]

- A common marketplace for university students to buy, sell or rent any used items. Provides facilities for seller to put items for sale, for other users to bid on an item or rent or buy

Human Resource Analytics | R | Feature Engineering | Apriori Algorithm | Decision Trees | Classifiers [Apr 2017]

- Evaluated the attributes behind the attrition rate in a company using a Human Resource dataset and provided corrective strategies

Product Comparison | Social Media Mining | Python | Twitter | Natural Language Processing | Sentiment Analysis [Apr 2018]

- A tool which provides comparison of attributes between two companies (ex. Samsung and Apple) based on the data mined from the tweets addressed to them. Segregates the attributes into positive, negative and neutral based on Sentiment Analysis

WORK EXPERIENCE

Test Engineer (Post Silicon Validation) | Tessolve Semiconductors Pvt. Ltd | Bangalore, India [Oct 2014 – May 2016]

- Worked on Silicon debug and bring-up at Wafer Sort (WS) and Final Test (FT) for multicore SOC processors (Mobile and Modem processors of Qualcomm) on ADVANTEST Verigy93K SOC tester platform using SmarTest tool
- Worked on test program development, pattern/vectors validation, corners testing and data analysis, yield analysis and test-time reduction
- Possess knowledge on VLSI testing and debug

Accomplishments:

- Reduced high yield-loss issue for the memory block pattern (SSC-Functional block) on Wafer Sort after working closely with the Design-For-Testability (DFT) team (Yield loss was brought down to 12% from 80%)
- Greatly reduced the test-time in the production flow (for a Modem chip) by slicing off the test-setup section of similar structural patterns (by manually hacking into the pattern and its timing file) and combining all of them into one single unit with a single test setup section

ACHIEVEMENTS AND OTHER ACTIVITIES

- Awarded 5th rank by Bangalore University in 'Electrical and Electronics Engineering' department (BE)
- Was 2nd topper in the Pre-University college
- Was the President of Electrical club of IEEE UVCE during the final year of Bachelor's degree. Organized several technical events (quizzes, seminars etc.) during my tenure as the president