




**DAYANANDA SAGAR COLLEGE OF ENGINEERING**  
Shavige Malleshwara Hills, Kumaraswamy Layout, Bangalore - 560078

Branch : **Electronics & Communication Engg**

Section : **6th Sem 'B' Section**

Batch:	2016	<b>PROCTORIAL FORM</b>
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**Student Details**

Name of the Student	SHAISTA FARHEEN		
Date of Birth	07-09-1997		
Branch	Electronics & Communication Engg		
USN	1DS16EC120		
Postal Address for communication	201,, JAYANAGAR BYRASANDRA, BDA LAYOUT, 8TH MAIN, 1ST A CROSS,, BANGALORE, BANGALORE URBAN, KARNATAKA - 560011		
Email ID	shaistafarheen7997@gmail.com		
Mobile Number	9980952992		
Landline Number	26631382	Signature	

**Academic Information :**

	Marks%	Year		
SSLC Percentage	94.5	2014	CET Rank	0
Pre-University Percentage	84.4	2016	Comed-K Rank	5988
Diploma Percentage			Management	No
Lateral Entry	No		NRI	NO

**Personnel Details**

Father's Name	SAJID MOHAMMAD	Mother's Name	SURAIYA TARANNUM
Academic Qualification		Academic Qualification	
Occupation	PROGRAMMER	Occupation	
Address	ssuraiya@gmail.com	Address	
Email ID	ssuraiya@gmail.com	Email ID	ssuraiya@gmail.com
Mobile Number	9901929992	Mobile Number	9901929992
Landline Number		Landline Number	

**Local Guardian Details:**

Name	SURAIYA TARANNUM
Occupation	
Contact Details	9901929992
Relationship	

**Reference of Important Person:**

Name	SURAIYA TARANNUM
Occupation	
Contact Details	9901929992

**Proctor Details:**

Proctor's Name	KUMAR P
Department	Electronics & Communication Engineering (EC)
Proctor's Contact No.	-8050565568
Email ID	HPKUMAR2015@GMAIL.COM

**Signature:**

Dean (Student Welfare)		Prof. and HOD	
Name		Name	
Email ID		Email ID	



# DAYANANDA SAGAR COLLEGE OF ENGINEERING

Shavige Malleshwara Hills, Kumaraswamy Layout, Bangalore - 560078

Time Table									
Days	09:00-10:00	10:00-11:00	11:00-11:15	11:15-12:15	12:15-13:15	13:15-14:00	14:00-15:00	15:00-16:00	16:00-17:00
Monday	ELE1	EC63		EC62	EC64		EC61		
Tuesday	LAB	LAB	LAB	LAB	ELE1		EC64		
Wednesday	ELE2	EC63		EC61	EC62		LAB	LAB	LAB
Thursday	EC64	EC61		EC62	ELE1		ELE2		
Friday	LAB	LAB	LAB	LAB	ELE2		EC63		
Saturday	EC62	EC61		EC63					

Legends:-

Subject Code	Subject Name	Faculty Name
EC61	Digital Communication	Dr. K L SUDHA
EC62	Embedded System Design	SANTHOSH KUMARA R
EC63	Analog VLSI Design	YAMUNA B J
EC64	Information Theory & Coding	DEEPA NP
EC651	SATELLITE COMMUNICATION	SHASHI RAJ K
EC653	DIGITAL IMAGE PROCESSING	Dr. THENMOZHI S
EC654	OPERATING SYSTEM	Dr. Kiran Gupta
EC661	DIGITAL SYSTEM DESIGN USING VERILOG	DR JAMUNA S
EC663	ADVANCED DIGITAL SWITCHING SYSTEMS	CHANDRA SEKHARA N
ECL67	Advanced Communication Lab	VIDYASHREE K N
ECL67	Advanced Communication Lab	MAHESH KUMAR N
ECL67	Advanced Communication Lab	KAVITA GUDDAD
ECL67	Advanced Communication Lab	VIBHA T G
ECL67	Advanced Communication Lab	TRUPTI TAGARE
ECL67	Advanced Communication Lab	
ECL68	VLSI Lab	
ECL68	VLSI Lab	Nanditha M S
ECL68	VLSI Lab	KUMAR P
ECL68	VLSI Lab	KRUTTHIKA H K
ECL68	VLSI Lab	MADHURA.R
ECL68	VLSI Lab	Dr. VIMALA P.

Attendance				
Subject Code	Subject Name	Class Taken	Attended Class	Attendance(%)
EC61	Digital Communication	5	5	100
EC62	Embedded System Design	1	1	100
EC63	Analog VLSI Design	1	1	100
EC64	Information Theory & Coding	2	2	100
EC651	SATELLITE COMMUNICATION	0	0	0
EC653	DIGITAL IMAGE PROCESSING	0	0	0
EC654	OPERATING SYSTEM	0	0	0
EC661	DIGITAL SYSTEM DESIGN USING VERILOG	0	0	0
EC663	ADVANCED DIGITAL SWITCHING SYSTEMS	1	1	100
ECL67	Advanced Communication Lab	1	1	100
ECL68	VLSI Lab	1	1	100

Internal Marks					
Subject Code	Subject Name	1st Internal	2nd Internal	3rd Internal	Avg. (Best Two)