Final Marks: 83.0/100

## **Evaluation Summary**

Question: Q1a Answer Given: 1011011101? 1101100111? 10011010? 1011011101? 1101100111? 10011010? Marks Awarded: 2 / 6 Feedback: ?? Mostly incorrect Question: Q1b Answer Given: 3502 ? 011101000010 1006 ? 001000000110 2234 ? 010010011100 4321 ? 100011010001 6753 ? 110111101011 3502 ? 1D12 1006 ? 206 2234 ? 24E 4321 ? 1A1 6753 ? 37B

Marks Awarded: 5 / 5

Feedback:	?	Fully correct	

Question: Q1c

Answer Given: C74 ? 110001110100

53F ? 010100111111

B132 ? 1011000100110010

1AFE ? 0001101011111110

C74?14364

53F ? 12377

B132?130314

1AFE ? 15376

Marks Awarded: 8 / 8

Feedback: ? Fully correct

Question: Q1d

Answer Given: 155 ? 10011011

3336 ? 110100001000

759 ? 101111011

155 ? 233

3336 ? 6500

759 ? 1367

Task 02: Logic Expressions and Control System

INSTRUCTIONS (Important):

- Write Boolean expressions clearly using symbols like ·, +, ~ and parentheses.
- For Q2a\_tt1 to Q2a\_tt3, give only the 8-bit output values. DO NOT write full truth tables.

0000011
- For Q2b_tt, use the format: 4-bit input ? output.
Example:
0000 ? 0
0001 ? 1
0010 ? 1
(continue up to 1111)
- For Boolean Equation (Q2b_eqn), write in proper logical form using + and variables.
Example:
X = A + B + C + D
- For Circuit Diagram (Q2b_circuit), describe the logic in simple sentence.
Example:
Connect A, B, C, and D to a 4-input OR gate to get output X.
Marks Awarded: 6 / 6
Feedback: ? Fully correct
Question: Q2a_expr1
Answer Given: $(A \cdot B \cdot C) + (A \cdot (\sim B + \sim C))$
Marks Awarded: 4 / 4
Feedback: ? Expression is correct
Question: Q2a_tt1
Answer Given: 00000011

Marks Awarded: 1 / 1

Example: Q2a\_tt1

Feedback: ? Truth table is fully correct Question: Q2a\_expr2 Answer Given:  $(P \cdot (\sim Q + R)) + Q$ Marks Awarded: 4 / 4 Feedback: ? Expression is correct Question: Q2a\_tt2 Answer Given: 00011111 Marks Awarded: 1 / 1 Feedback: ? Truth table is fully correct Question: Q2a\_expr3 Answer Given:  $(\sim(P\cdot Q) + (P\cdot R)) \cdot \sim R$ Marks Awarded: 4 / 4 Feedback: ? Expression is correct Question: Q2a\_tt3 Answer Given: 10110100

Marks Awarded: 1 / 1

Feedback: ? Truth table is fully correct

0001 ? 1
0010 ? 1
0011 ? 1
0100 ? 1
0101 ? 1
0110 ? 1
0111 ? 1
1000 ? 1
1001 ? 1
1010 ? 1
1011 ? 1
1100 ? 1
1101 ? 1
1110 ? 1
1111 ? 1
b) Boolean Equation:
Marks Awarded: 4 / 4
Feedback: ? Logic map fully correct
Question: Q2b_eqn
Answer Given: $X = A + B + C + D$
c) Circuit Diagram:
Marks Awarded: 1 / 2

Feedback: ? Partially matched Boolean equation

Question: Q2b\_tt

Answer Given: 0000 ? 0

Question: Q2b_circuit
Answer Given: Connect A, B, C, D to a 4-input OR gate to get X as the output.
Task 03: Hotel IT Configuration
Marks Awarded: 2 / 4
Feedback: ? Some elements missing in circuit explanation
Question: Q3a
Answer Given: xeon epyc ecc ssd raid pos ups firewall
Marks Awarded: 15 / 15
Feedback: ? Keywords sufficient for diploma level
Question: Q3b
Answer Given: windows ubuntu security erp domain integration
Task 04: Microprocessor
Marks Awarded: 4 / 10
Feedback: ?? Few relevant terms present
Question: Q4a

Answer Given: alu control registers cache clock bus decoder pc

Marks Awarded: 8 / 8

Feedback: ? Keywords sufficient for diploma level

Question: Q4b

Answer Given: executes instructions, performs calculations, controls data, coordinates ops

Marks Awarded: 3 / 7

Feedback: ?? Few relevant terms present

-----

Question: Q4c

Answer Given: ALU fast, cache reduces latency, registers quick access, clock speed, pipelining

1011011101 ? 1355

1101100111 ? 1567

10011010 ? 232

1011011101?2DD

1101100111?1B3

10011010 ? 9A

3502 ? 011101000010

1006 ? 001000000110

2234 ? 010010011100

4321 ? 100011010001

6753 ? 110111101011

3502 ? 1D12

1006 ? 206

2234 ? 24E

4321 ? 1A1

6753 ? 37B

C74 ? 110001110100

53F ? 010100111111

B132 ? 1011000100110010

1AFE ? 0001101011111110

C74 ? 14364

53F ? 12377

B132?130314

1AFE ? 15376

155 ? 10011011

3336 ? 110100001000

759 ? 101111011

155 ? 233

3336 ? 6500

759 ? 1367

 $(A \cdot B \cdot C) + (A \cdot (\sim B + \sim C))$ 

00000011

 $(P \cdot (\sim Q + R)) + Q$ 

00011111

 $(\sim (P \cdot Q) + (P \cdot R)) \cdot \sim R$ 

10110100

0000?0

0001?1

0010 ? 1

0011?1

0100 ? 1

0101?1

0110?1

0111?1

1000?1

1001 ? 1
1010 ? 1
1011 ? 1
1100 ? 1
1101 ? 1
1110 ? 1
1111 ? 1
X = A + B + C + D
Connect A, B, C, D to a 4-input OR gate to get X as the output.
Xeon processor, 64GB RAM, ECC memory, SSD, RAID setup, POS system, Scanner,
Printer
Windows Server for the main server and Ubuntu/Linux Mint for functional
computers
ALU, Control Unit, Registers, Cache, Clock, System Bus, Instruction Decoder,
Program Counter
Executes instructions, performs calculations, controls data flow, and handles logic
operations
Cache and registers for speed, clock for cycles, pipelining and multicore for parallel
tasks
Marks Awarded: 10 / 10
Feedback: ? Keywords sufficient for diploma level