How to increase the performance of a system?

Alternative 1:- horeen hadware.

1 adder -> Sadder

Alternation 2:- Pipelining

Pipelining: -

A mechanism of "Overlapped execution" of some input sets by partitioning the computations into "k" stages.

Ideal speedup is "k".

Uses:-

- 1 Instruction execution.

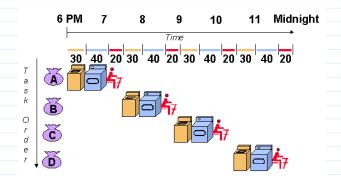
 multiple instructions executed in some sequence.
- D Arithmetic computation.
 multiple operation on some data sets.
- (3) Memory access
 multiple accesses to consensive memory locations.

Laundry Analogy of pipelining:-

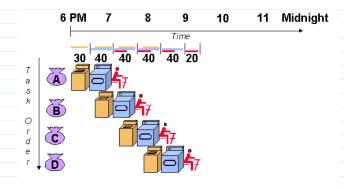
Wash Clothes

1 Wash @ Dry & Fold

(30 min) (40min) (20 min)



PIPELINING approach :-



Let Tw, TD, TF be the time required for washing, drying and folding, respectively.

Non-pipelined betup:

=) For N bundles of dothes

Total time $T_{NP} = (T_W + T_D + T_F) \cdot N$ for $T_W = T_S = T_F$

Pipelined setup: -

(1)	Tw	TD	TF				-	•
2		TW	TD	TF			_	•
3			Tw	To	TE		-	٠.
9				Tw	To	TE	_	
1	-	-1	- 1	1	-	1	•	
1	((1					

Bundle 1 - 3.Tw Bundle 2 - 4.Tw Bundle 3 - 5.Tw

bundle N - (N+2). Tw

Speedup (S), of N-stage pipeline over non-pipelined setup.

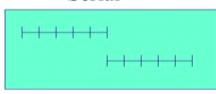
$$S = \frac{T_{NP}}{T_{p}} = \frac{3N.Tw}{(N+2)Tw}$$

Clausification of processors based on various pipeline parameters:

@ Serial:-

The next operation can start only after the previous operation finishes.

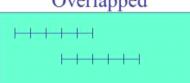
Serial



6 Overlapped:

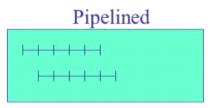
There is some overlap between successive operations.

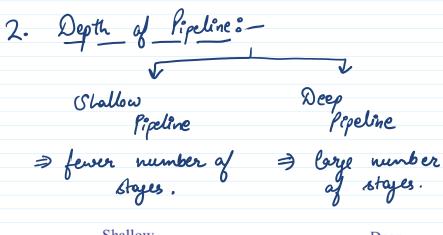
Overlapped

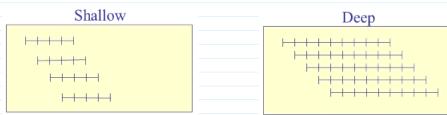


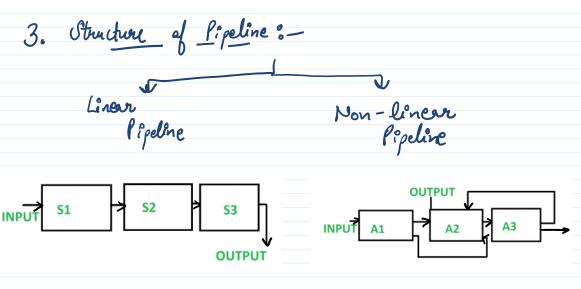
@ Pipelined :-

Fine-grain overlag between successive operation.









The stayes are executed one by one in a sequence.

Stages may not execute in a linear sequence/ A particular stage may execute more than once

Reservation Table: -

A data structure that regressite the pipeline.

		1	2	3	4	-	time	steps
	S,							,
	52							
	S_3							
	Sy							
pipeline)							
stayus	V							
7014905								

a space-time diagram af the pipeline that shows precedence relationships among pipeline stayes.