

1. (a)

2. Three dependencies

$$I_1 \leftrightarrow I_2$$

$$I_2 \leftrightarrow I_4$$

$$I_3 \leftrightarrow I_4$$

$$\text{frequency} = 2 \text{ GHz}$$

$$t = \frac{1}{2 \text{ GHz}} = 0.5 \text{ ns}$$

	1	2	3	4	5	6	7	8	9	10	11
	IF	ID	EX	MEM	WB						
		IF	ID	EX	MEM	WB					
			IF	ID	EX	EX	EX	MEM	WB		
				IF	X	X	ID	EX	EX	MEM	WB

\Rightarrow 11 cycles

$$\Rightarrow \text{Execution Time} = 11 \times 0.5 = 5.5 \text{ ns}$$

\Rightarrow (c) 3, 5.5 ns

3. (c)

$$4. \text{ Transfer rate} = 10 \text{ MBps}$$

$$\text{Amount of data} = 20 \text{ kbytes}$$

$$\begin{aligned}
 &= 20 \times 2^{10} \\
 \text{Total time} &= \frac{20 \times 2^{10}}{10 \times 2^{20}} \\
 &= 2 \times 2^{-10} \text{ s} \approx 2 \times 10^{-3} = 2 \text{ msec}
 \end{aligned}$$

$$\text{Processor speed} = 600 \text{ MHz}$$

$$= 600 \times 10^6 \text{ cycles/sec}$$

$$\begin{aligned}
 \text{Cycles required by CPU} &= 300 + 900 \\
 &= 1200
 \end{aligned}$$

$$\begin{aligned}
 \Rightarrow \text{Time} &= \frac{1200}{600 \times 10^6} = 2 \times 10^{-6} \\
 &= 0.002 \text{ msec}
 \end{aligned}$$

$$\begin{aligned}
 \% \text{ age of processor time consumed} &= \frac{0.002}{2 \times 1000} \times 100 \\
 &= 0.1\%
 \end{aligned}$$

\Rightarrow (D)

5. (B) Both B and C are true only.