# CSE 431/531: Algorithm Analysis and Design (Fall 2024) Greedy Algorithms

Lecturer: Kelin Luo

Department of Computer Science and Engineering University at Buffalo

#### Outline

- Interval Scheduling
  - Interval Partitioning

- Offline Caching
  - Heap: Concrete Data Structure for Priority Queue

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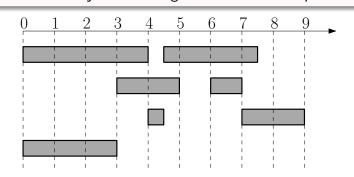
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  - Heap: Concrete Data Structure for Priority Queue

**Input:** n jobs, job i with start time  $s_i$  and finish time  $f_i$ 

i and j are compatible if  $\left[s_{i},f_{i}\right)$  and  $\left[s_{j},f_{j}\right)$  are disjoint

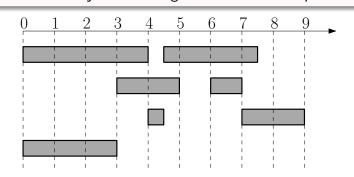
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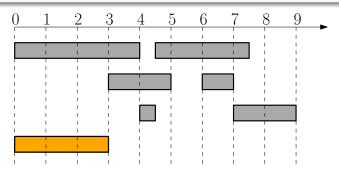
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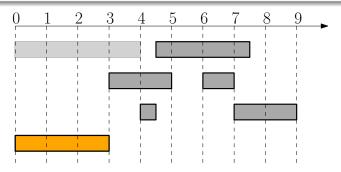
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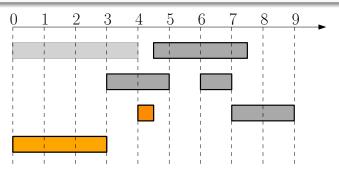
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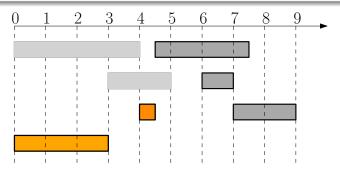
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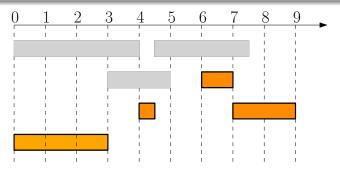
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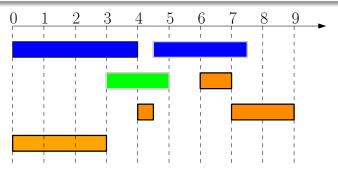
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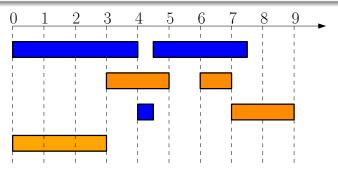
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**Lemma** It is safe to schedule the job j with the earliest starting time to a feasible machine: There exists an optimum solution where job j with the earliest starting time is scheduled first on a machine that is compatible with all jobs in that machine if applicable; otherwise, it can be scheduled by opening a new machine.

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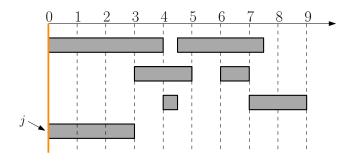
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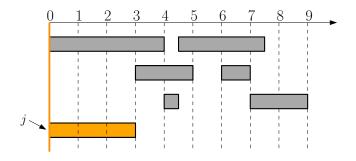
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- Otherwise, replace all the jobs scheduled to the machine i in S with j and its subsequent jobs to obtain another optimum schedule S'.

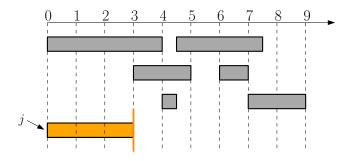
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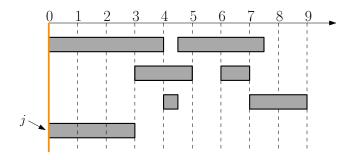


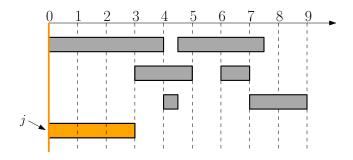
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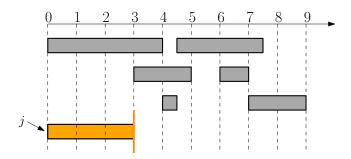


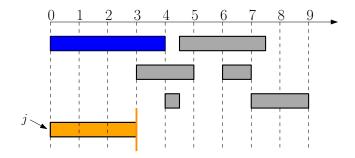
#### Partition(s, f, n)

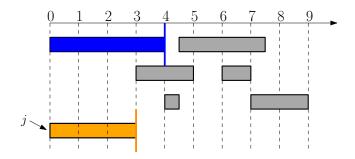
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- 5: Otherwise, schedule j to machine |S|+1,  $S \leftarrow S \cup \{|S|+1\}$  and  $t_{|S|}=f_{j}$
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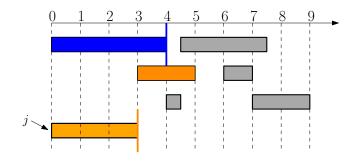


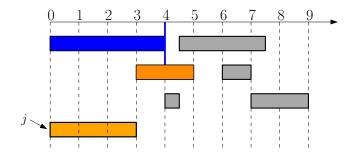


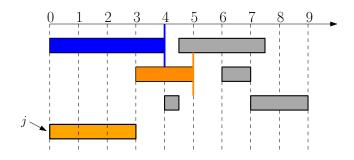


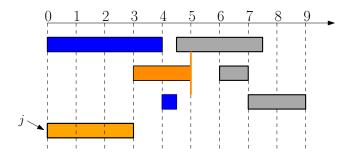


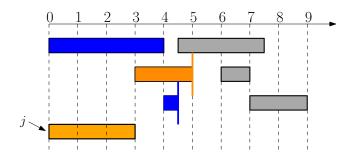


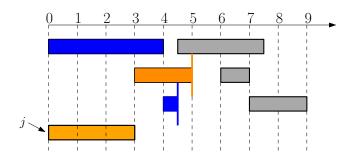


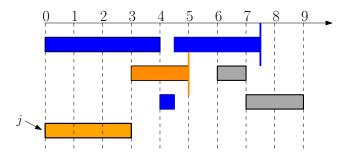


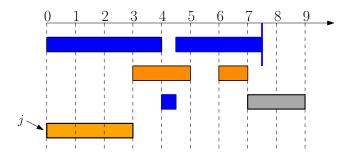


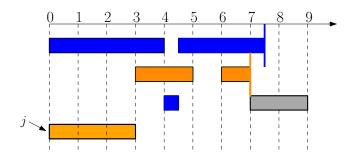


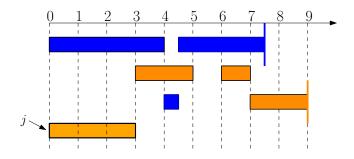












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**Obs.** Greedy algorithm never schedules two incompatible jobs in the same machine.

**Theorem** Greedy algorithm is optimal.

#### Proof.

 $\bullet$  Let d be the number of machines that greedy algorithm used.

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- ullet By the Observation in the previous slide, an optimal solution  $\geq d$ . Thus the greedy algorithm is optimal.

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- ullet Clever implementation:  $O(n\lg n)$  time with Priority Queue.

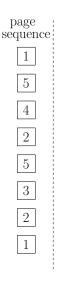
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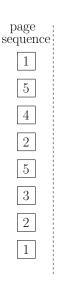
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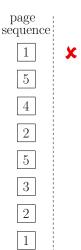


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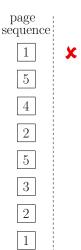
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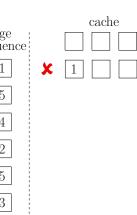
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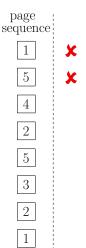
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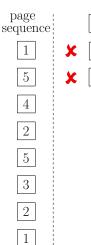


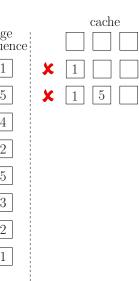
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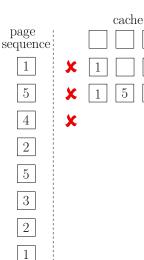


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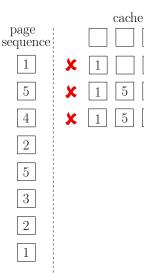




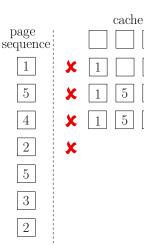
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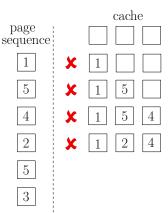
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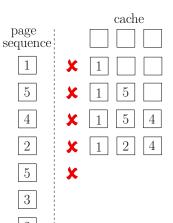
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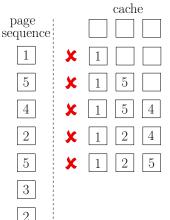
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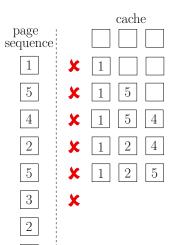
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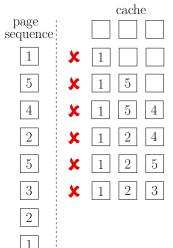
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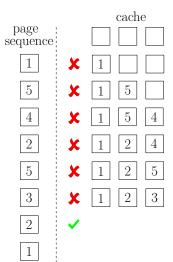
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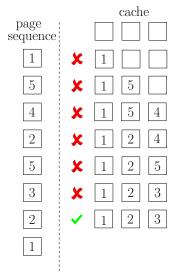
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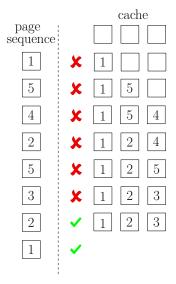
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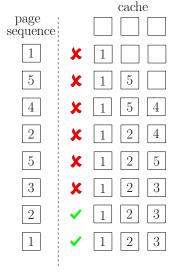
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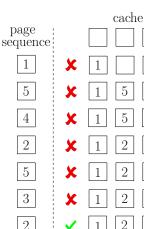


page

sequence

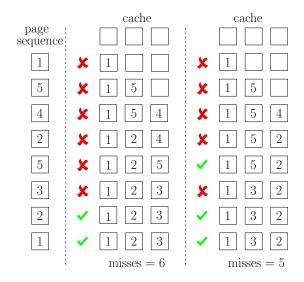
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- Cache hit happens if requested page already in cache.
- Goal: minimize the number of cache misses.



misses = 6

#### A Better Solution for Example



#### Offline Caching Problem

**Input:** k: the size of cache

n: number of pages We use [n] for  $\{1, 2, 3, \dots, n\}$ .

 $\rho_1, \rho_2, \rho_3, \cdots, \rho_T \in [n]$ : sequence of requests

**Output:**  $i_1, i_2, i_3, \dots, i_T \in \{\text{hit}, \text{empty}\} \cup [n]$ : indices of pages to

evict ("hit" means evicting no page, "empty" means

evicting empty page)