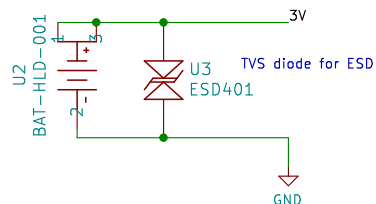
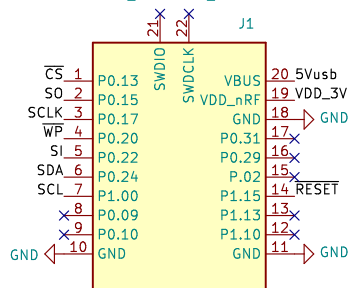


20mm diam. coin cell holder

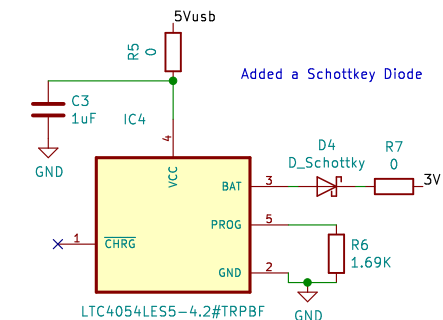


CR2032/CR2025 Coin Cell holder and ESD/Surge protection

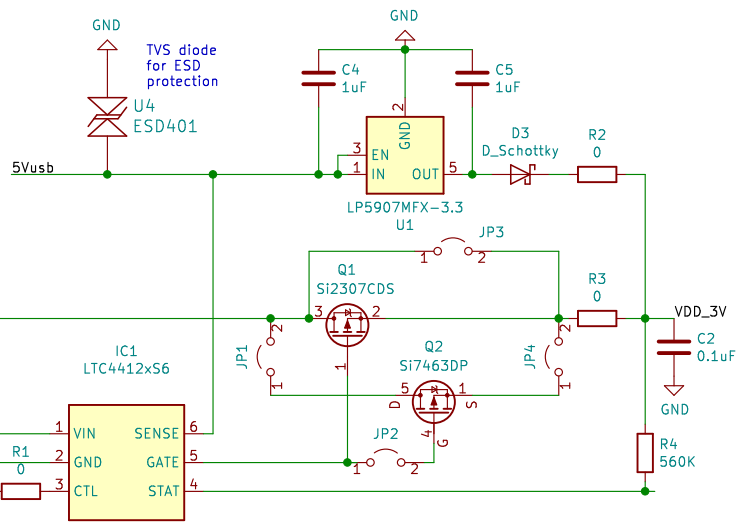
Nordic_nRF52840_breakout



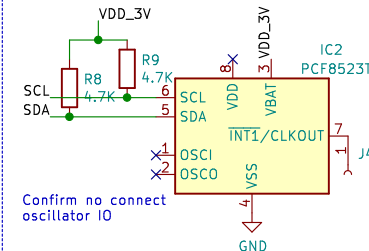
CS - P0.17
CLK - P0.19
SI - P0.20
SO - P0.21
WP - P0.22
RESET (SI03 - pin7) - P0.23
Pinouts from development board schematic
P1.15 is a low frequency GPIO (<10kHz)
GPIO not marked for any other function or low frequency include P0.13, 15, 17, 20, 22, 24, P1.00



Coin cell charger circuit

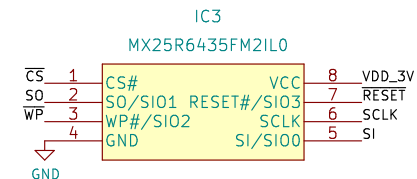


Power source selection logic



Confirm no connect oscillator IO

Real time clock module with i2c interface



NB: CS WP RESET use inverted logic
SPI interface flash memory with SPI



Sheet: /
File: ble_mvp_1A-rev0.sch

Title:

Size: A4
KiCad E.D.A. kicad (5.1.5)-3

Date:

Rev:

Id: 1/1