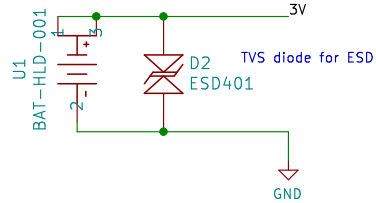


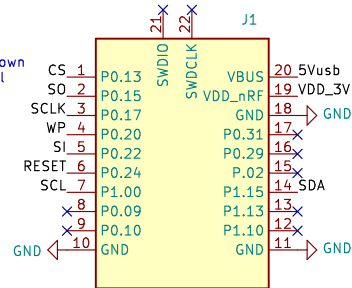
20mm diam. coin cell holder



CR2032/CR2025 Coin Cell holder  
and ESD/Surge protection

Nordic\_nRF52840\_breakout

Confirmed no pull-up/down  
resistors for SPI protocol  
Add 100nF cap between  
GND and VCC

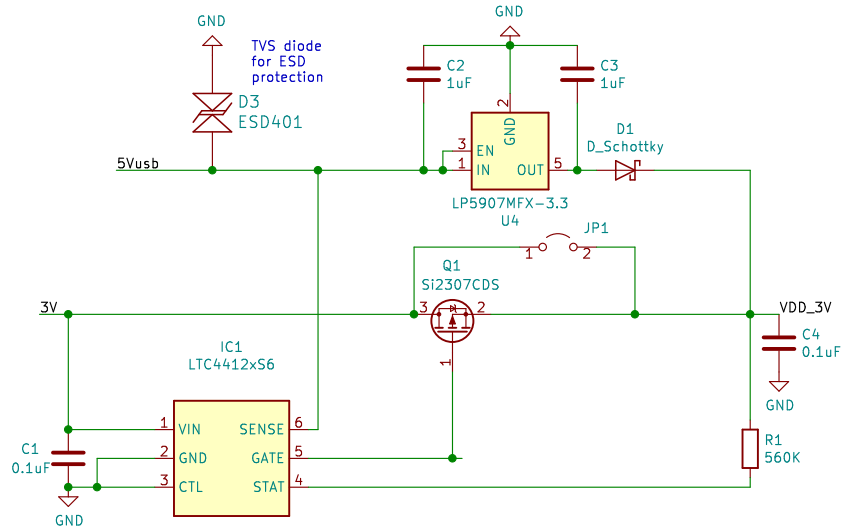


CS - P0.17  
CLK - P0.19  
SI - P0.20  
SO - P0.21  
WP - P0.22  
SIO3/HOLD/RESET(pin7) - P0.23  
Pinouts from development board schematic

P1.15 is recommended for low frequency GPIO (<10kHz) due to  
proximity to the Antenna. Only used for pinging the RTC on startup.  
GPIO not marked for any other function or low frequency include P0.13, 15, 17, 20, 22, 24, P1.00

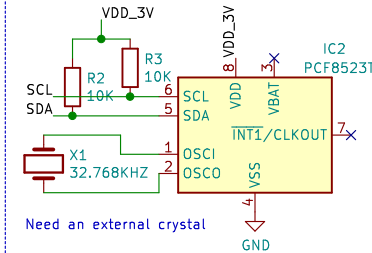
Through hole pin header for Nordic Dangle

NON RECHARGEABLE VERSION



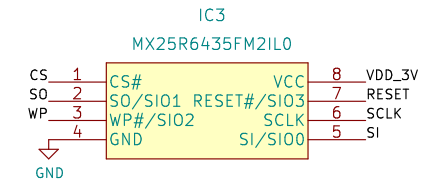
Power source selection logic

VDD powers chip. VBat is for time  
keeping backup with switching logic.



Need an external crystal

Real time clock module with i2c interface



SPI interface flash memory with SPI

Sheet: /  
File: ble\_mvp\_1A-rev2.sch

**Title:**

Size: A4  
KiCad E.D.A. kicad (5.1.5)-3

Date:

**Rev:**

Id: 1/1