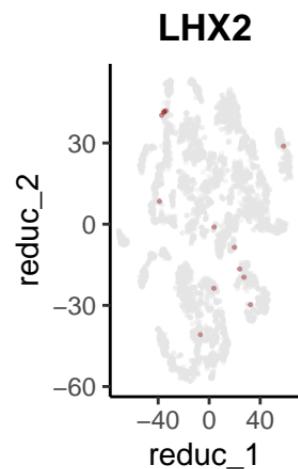
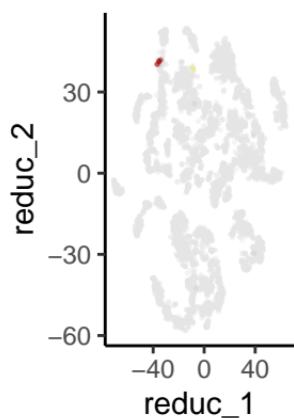
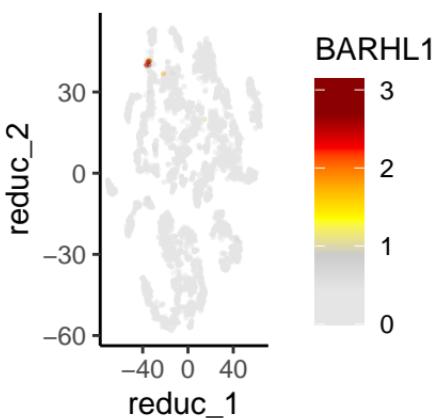
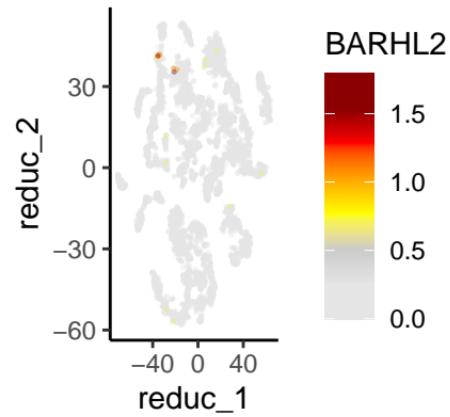
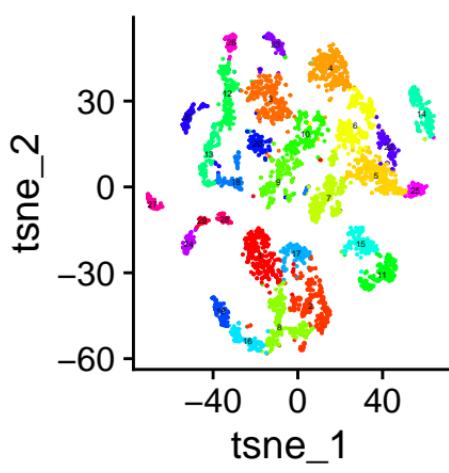
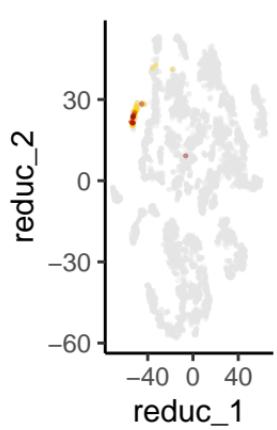
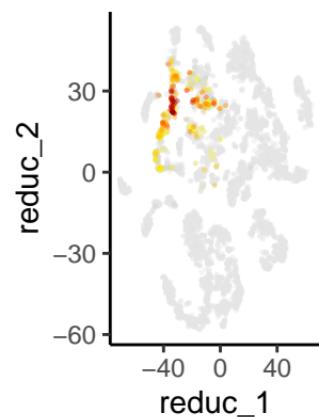
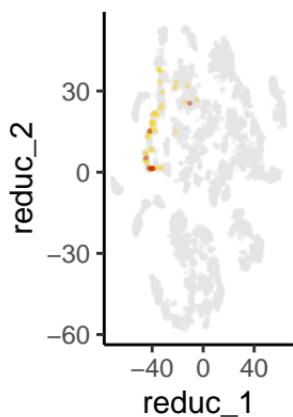
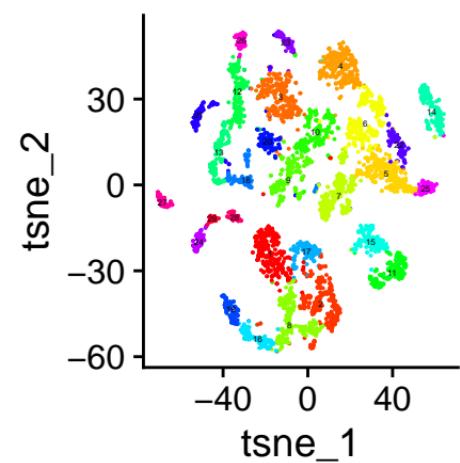
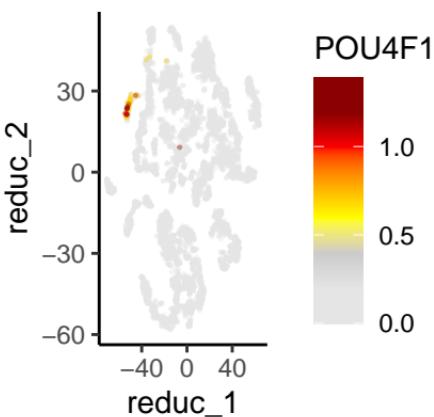


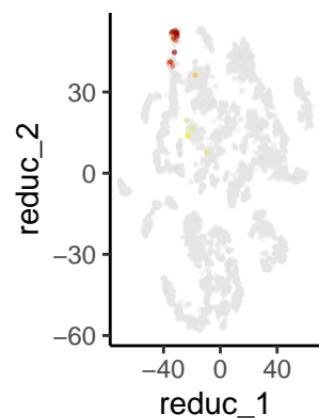
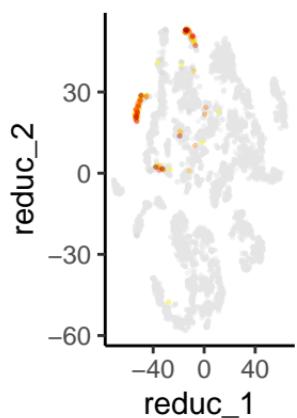
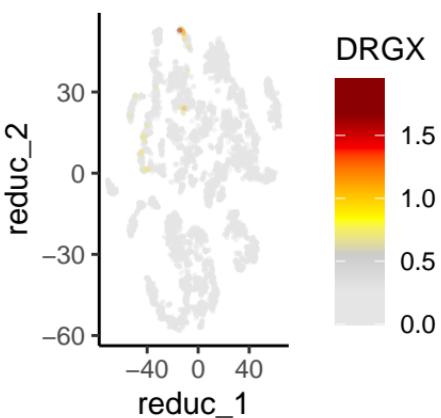
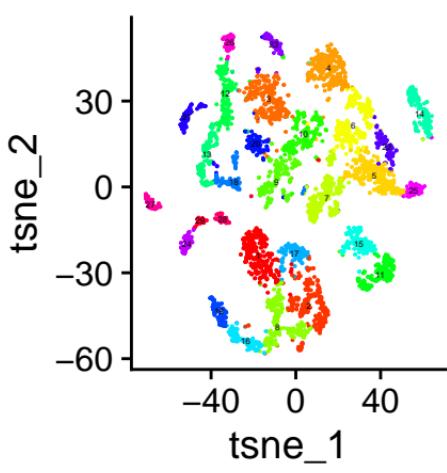
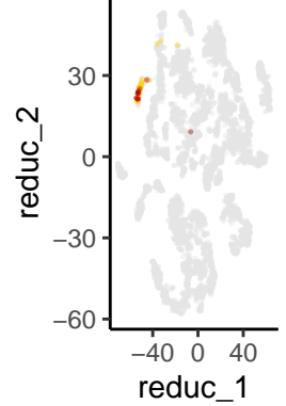
dl1

**LHX9****BARHL1****BARHL2****POU4F1**

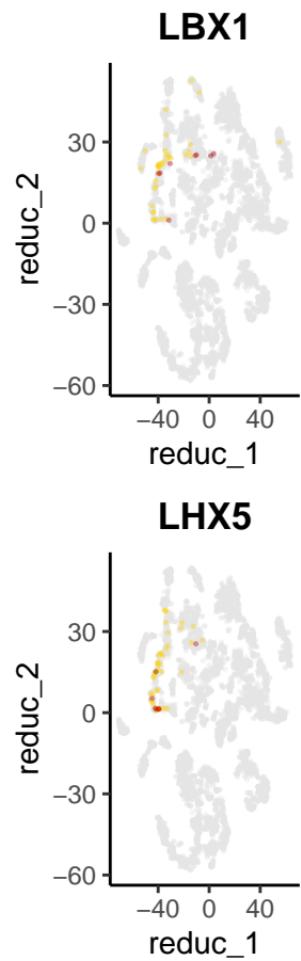
dl2

LHX1**LHX5****POU4F1**

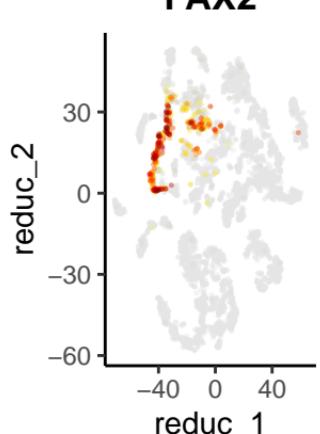
dl3

ISL1**TLX3****DRGX****POU4F1**

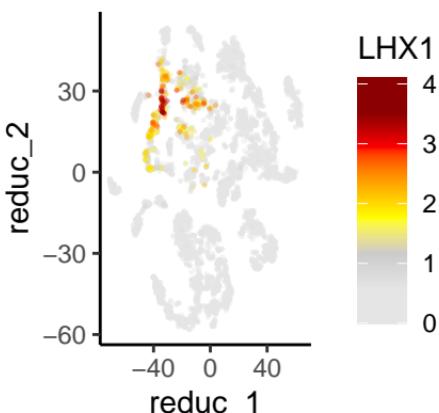
dl4



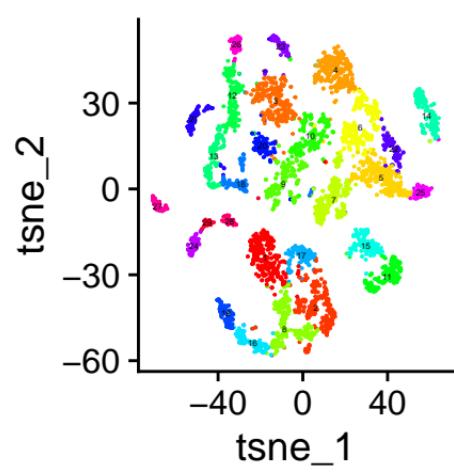
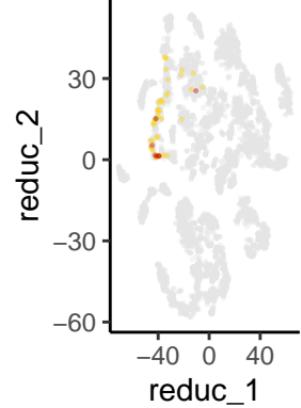
PAX2



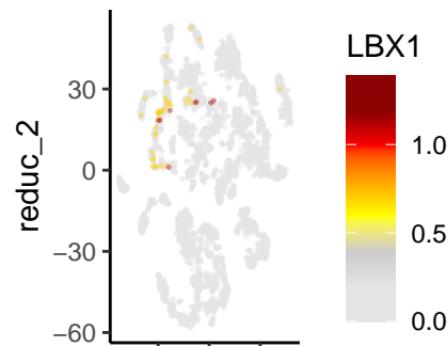
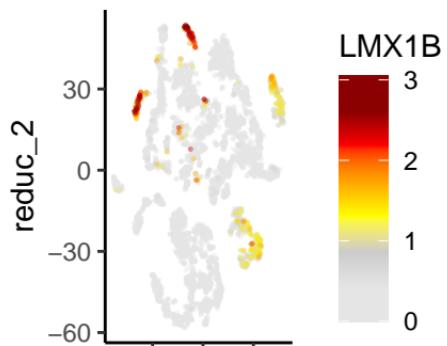
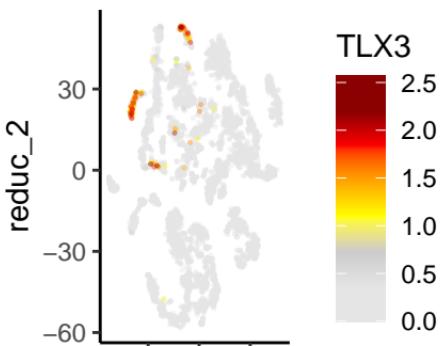
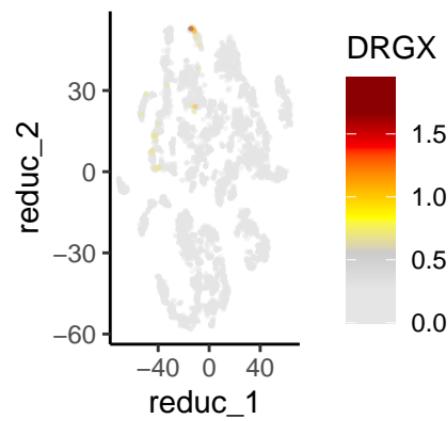
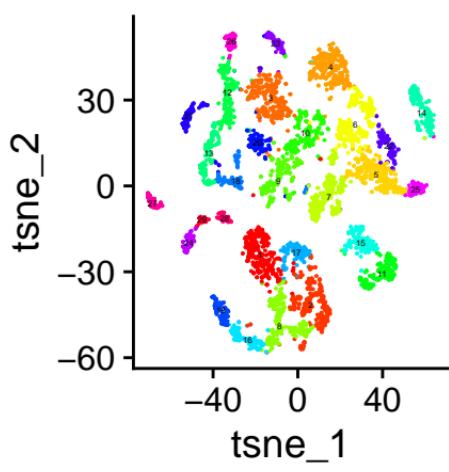
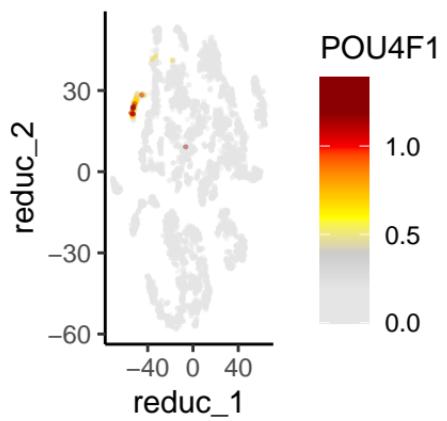
LHX1



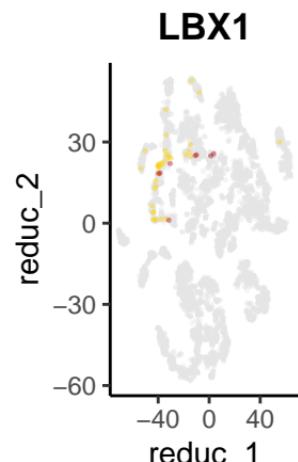
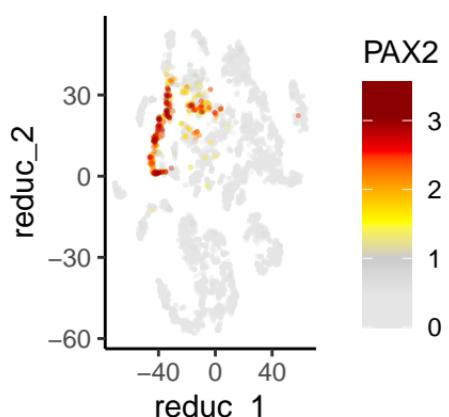
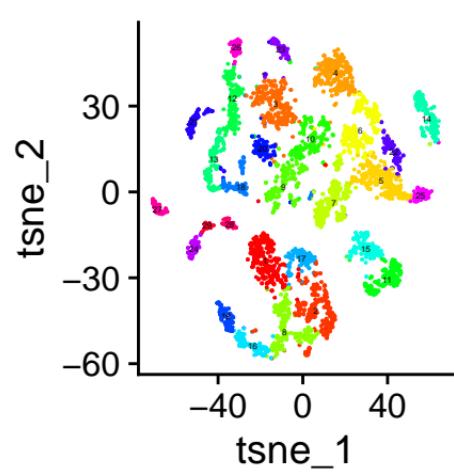
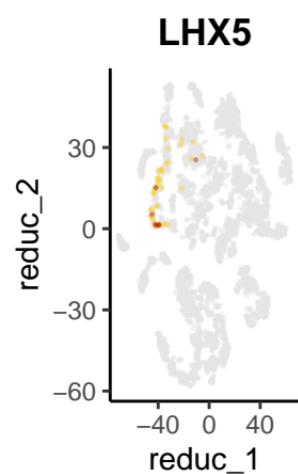
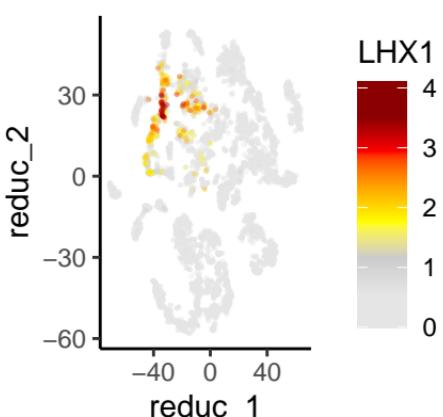
LHX5



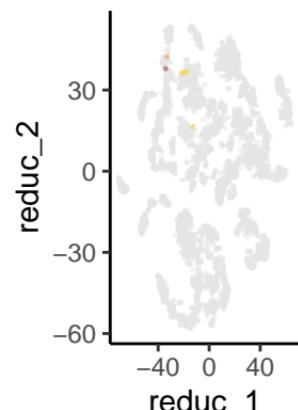
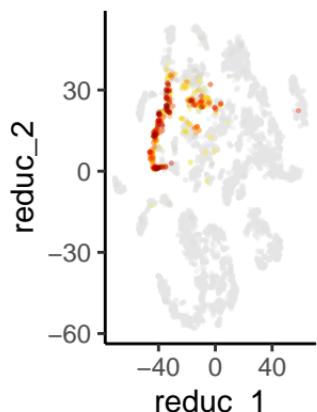
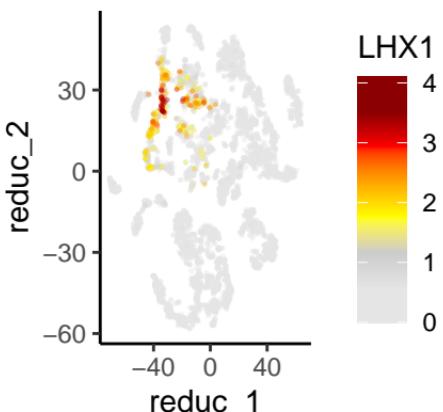
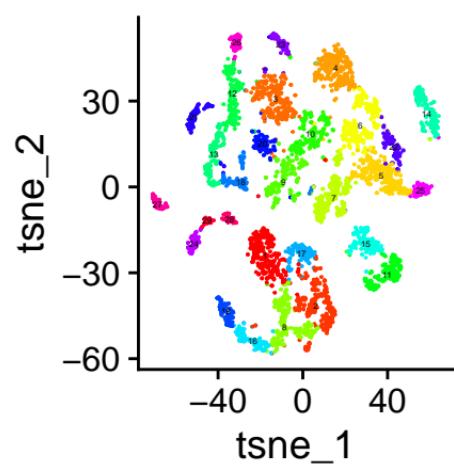
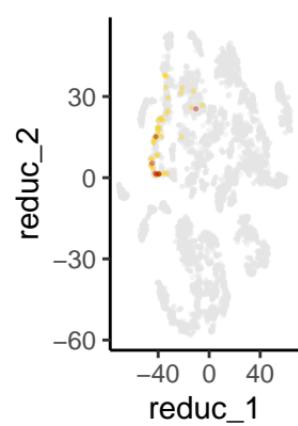
dl5

LBX1**LMX1B****TLX3****DRGX****POU4F1**

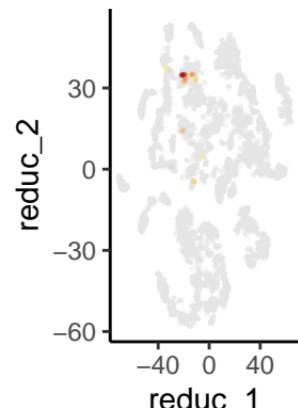
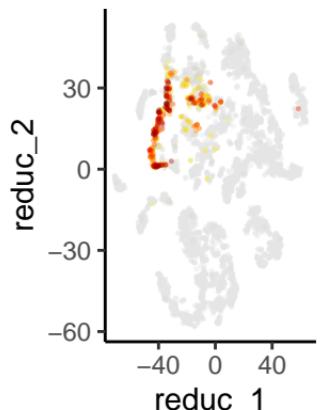
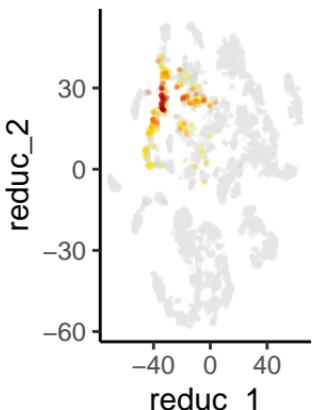
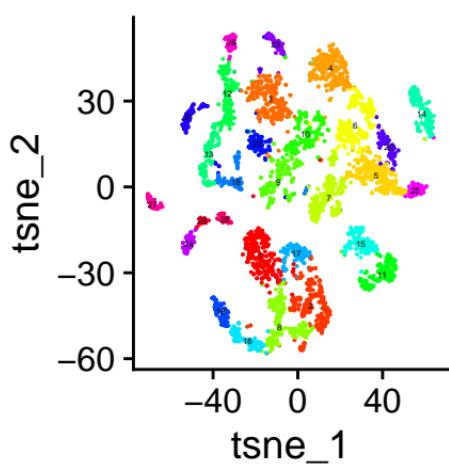
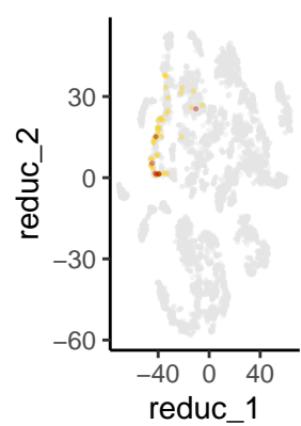
dl6

**PAX2****LHX1**

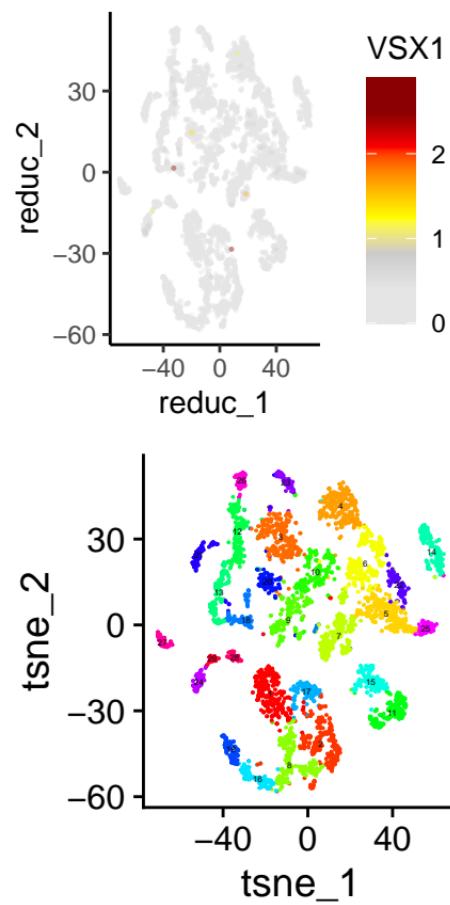
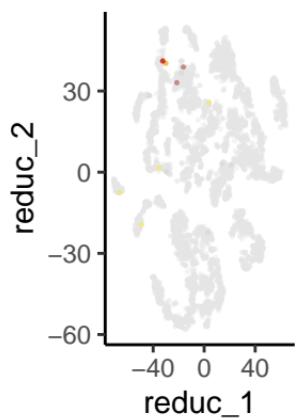
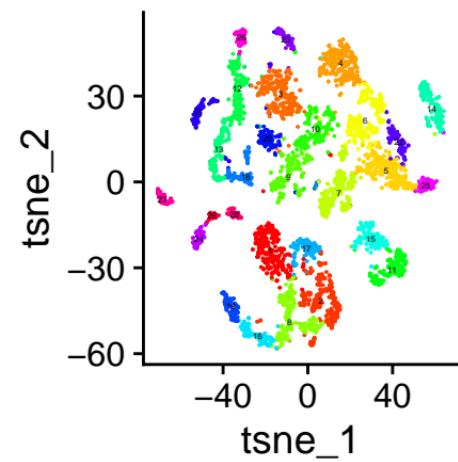
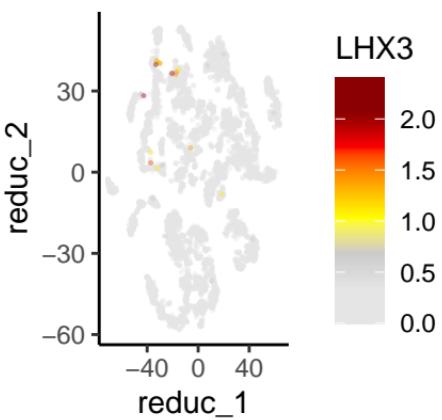
V0

EVX1**PAX2****LHX1****LHX5**

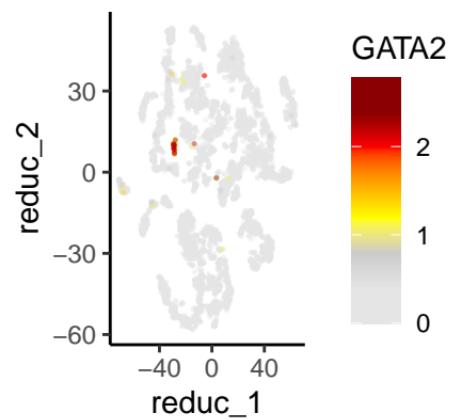
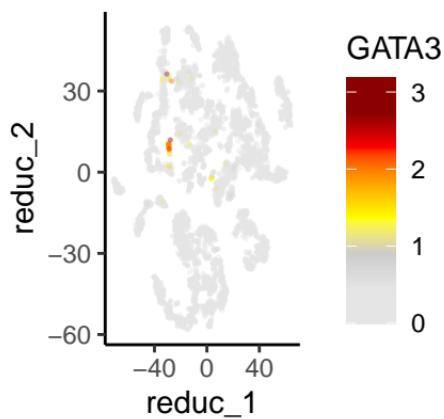
V1

EN1**PAX2****LHX1****LHX5**

V2a

VSX1**SOX14****LHX3**

V2b

GATA2**GATA3****TAL1**