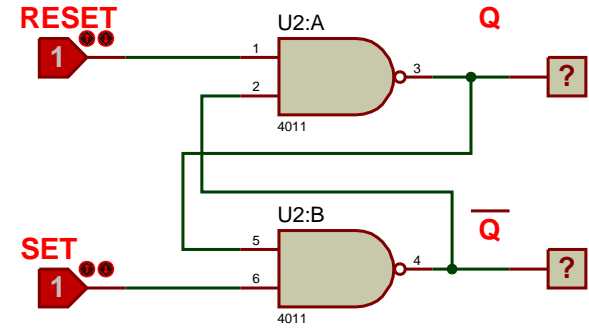


S - R FL P FLOP DEVRELER

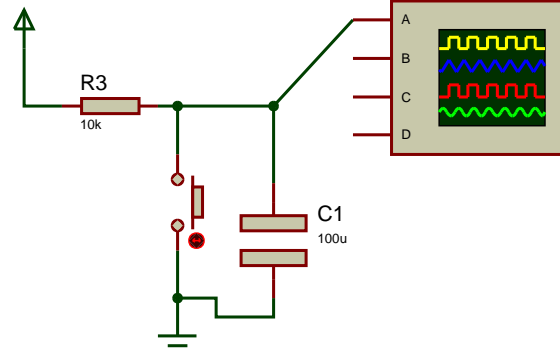
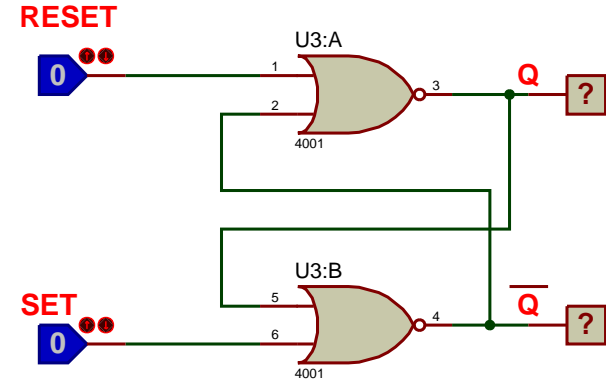
LOW seviyesine alarak seçilir. NAND oldu u için.



A	B	Q
0	0	1
0	1	1
1	0	1
1	1	0

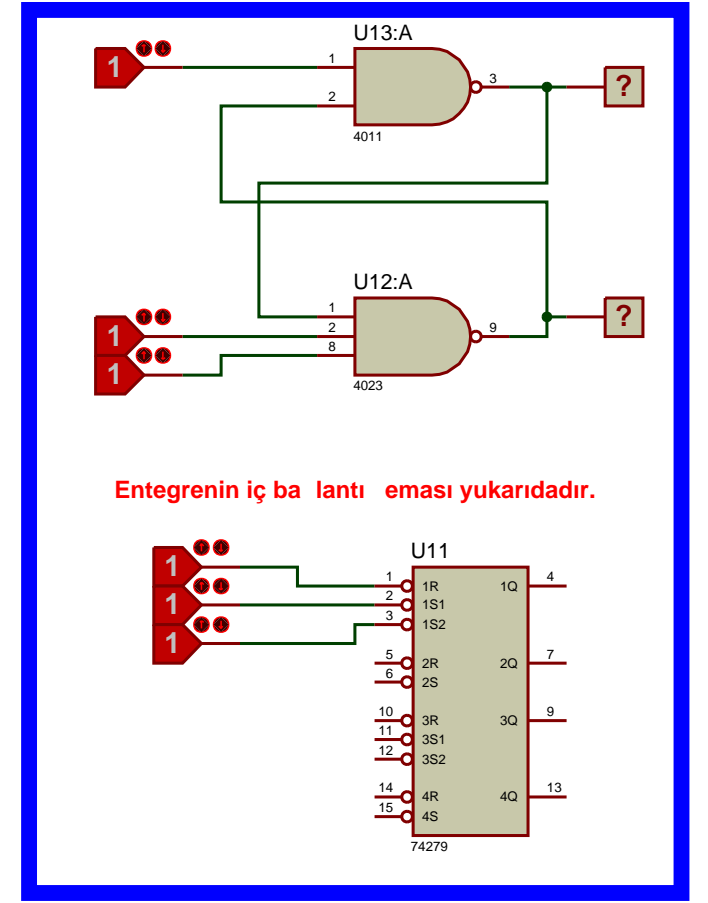
A	B	Q
0	0	1
0	1	0
1	0	0
1	1	0

HIGH seviyesine alarak seçilir. NOR oldu u için.

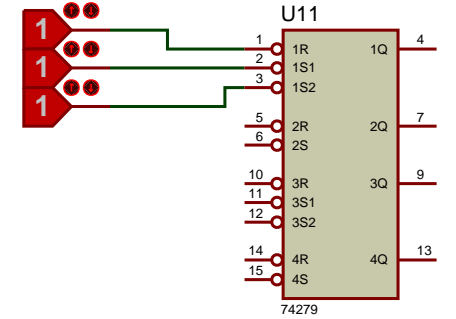


Çift tetikleme önleme

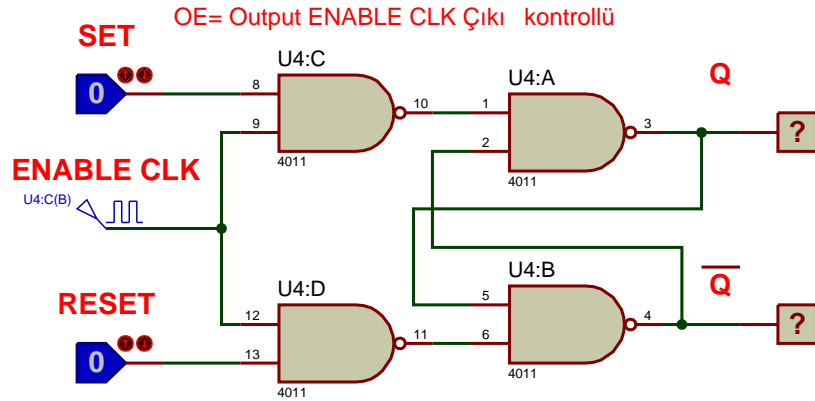
Sno	S	R	Q	Q'	State
1	1	0	1	0	Q is set to 1
2	1	1	1	0	No change
3	0	1	0	1	Q' is set to 1
4	1	1	0	1	No change
5	0	0	1	1	Invalid



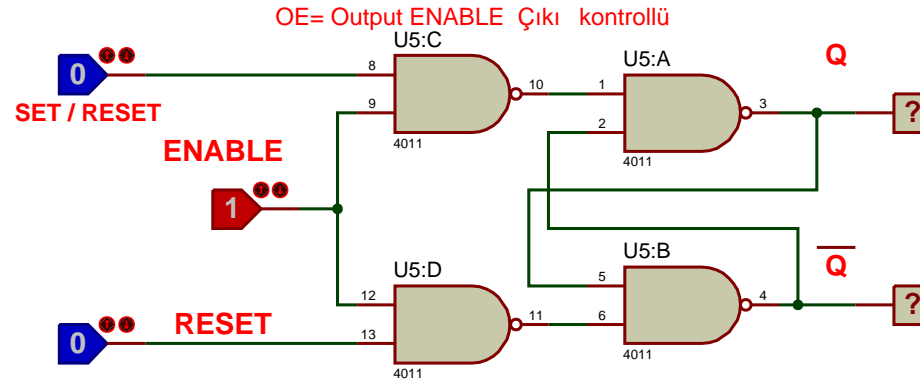
Entegrenin iç ba lantı eması yukarıdadır.



OE= Output ENABLE CLK Çıktı kontrollü



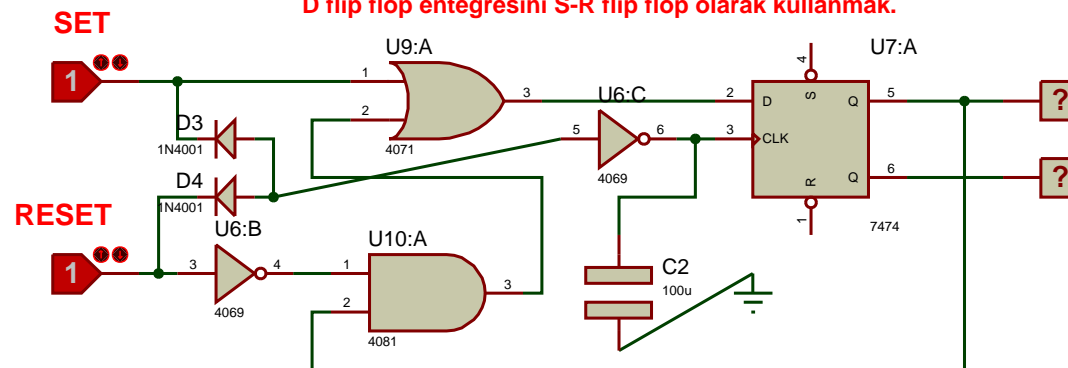
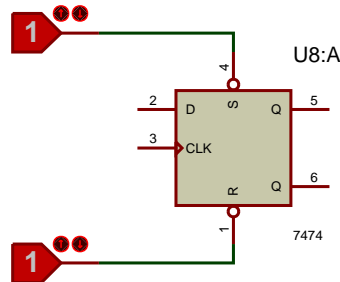
OE= Output ENABLE Çıktı kontrollü



Yukarıdakinin Benzeri D F/ F ile a ıdaki gibi yapılabilir.

D flip flop entegresini S-R flip flop olarak kullanmak.

D Flip Flop S-R giri kontrollü entegre., ACTIVE=LOW



J-K Flip flop entegresini S-R flip flop olarak çalıştırmak

