

lec 2024/08/29

- binary: get more precision over n-ary or smth
- and (&), or (|), not (~), xor (^)
- shifts
 - $x \ll y$
 - throw away extra bits at left
 - fill with 0s on right
 - $x \gg y$
 - throw away extra bits at left
 - logical shift: fill with 0s on left
 - arithmetic shift: replicate sign bit on left
 - *undefined*: shift amtn < 0 or \geq word size
- $-x = \sim x + 1$ in twos complement
 - but if $x = T_{\min}$ (most negative two's complement), you get back T_{\min}
- mix of signed and unsigned in expression eg $==?$ implicit casted and evaled in unsigned.