- 1. Design a counter which goes through the sequence 000 -> 010 -> 100 -> 001 -> 011 -> 111
- 2. Design a 2 bit multiplier extusing (a) combinational circuit, (b) segmental circuit (using a shift register.
- 3. Design a ckt which divides the CP by n. 2I/P
- 4. Design a IXOR gate using minimum number of [NAND gates.
- J. The following Boolean expression BE+ BDE is a simplified vension of the Expression

ABE+BCPE+ 8CDE+ ABDE+ BCDE.

Ane there any don't come conditions? If so, what are they?

- 6. With the use of maps, find the simplest form in sum of products of the function F=fg, where of and g are given by f = wxy + yz + wy = + x yz g= (N+ X+) (X+)+Z) (N+7+Z)
- 7. There are two channels in a communication system. The incoming bit streams are Ao A, A2 A3 -- An and Bo B, B2 B3 - Bn Design a ext such that the olp sequence from the ckt is AoBo AIBi ALAz----AmBn.
- A seven segment LED display is shown below.

 1916 Design a combination of sequential and combinational elaboration of sequential and combination of el_lc and then back to 0.
- Implement Boolean functions using devoders, multiplexers. ROM, PLA etc.
- Obtain an 8x1 multiplexes with a dual 4-line to 1-line multiplexes having seperate enable inputs but common selection lines. Use a block diagram constants
- Design a 4 bit shift register such that when an input x=0, it loads an external input at the next CP and when z=1, it shifts the content of the negister to the night inserting o in the MSB.
- 12. Think of practice | near problem and then try to find a solution using digital circuits.