





octave Project Status (12/06/2013 - 13:54:27)			
Configuration File:	octave.xreport	Parser Errors:	
Module Name:	octave	Implementation State:	Programming File Generated
Target Device:	5v1x110tff1136-1	• Errors:	No Errors
Product Version:	ISE 14.6	• Warnings:	513 Warnings (513 new)
Design Goal:	data unavailable	• Routing Results:	All Signals Completely Routed
Design Strategy:	data unavailable	• Timing Constraints:	All Constraints Met
Environment:	System Settings	• Final Timing Score:	0

Device Utilization Summary 				
Slice Logic Utilization	Used	Available	Utilization	Note(s)
Number of Slice Registers	2,602	69,120	3%	
Number used as Flip Flops	2,602			
Number of Slice LUTs	5,104	69,120	7%	
Number used as logic	1,395	69,120	2%	
Number using O6 output only	1,304			
Number using O5 output only	81			
Number using O5 and O6	10			
Number used as Memory	3,682	17,920	20%	
Number used as Shift Register	3,682			
Number using O6 output only	3,682			
Number used as exclusive route-thru	27			
Number of route-thrus	146			
Number using O6 output only	108			
Number using O5 output only	38			
Number of occupied Slices	1,729	17,280	10%	
Number of LUT Flip Flop pairs used	5,647			
Number with an unused Flip Flop	3,045	5,647	53%	
Number with an unused LUT	543	5,647	9%	
Number of fully used LUT-FF pairs	2,059	5,647	36%	
Number of unique control sets	51			
Number of slice register sites lost to control set restrictions	72	69,120	1%	
Number of bonded IOBs	103	640	16%	
Number of BUFG/BUFGCTRLs	1	32	3%	
Number used as BUFGs	1			
Number of DSP48Es	37	64	57%	
Average Fanout of Non-Clock Nets	2.77			

Performance Summary 			
Final Timing Score:	0 (Setup: 0, Hold: 0)	Pinout Data:	Pinout Report
Routing Results:	All Signals Completely Routed	Clock Data:	Clock Report
Timing Constraints:	All Constraints Met		

Detailed Reports (working directory="/home/cc/cs150/fa013/class/cs150-bn/fa13_team07/hardware/build/octave") 					
Report Name	Status	Generated	Errors	Warnings	Infos
Synthesis Report	Current	Fri Dec 6 13:49:54 2013	0	513 Warnings (513 new)	48 Infos (48 new)
Translation Report	Current	Fri Dec 6 13:50:09 2013	0	0	0
Map Report	Current	Fri Dec 6 13:51:58 2013	0	0	8 Infos (8 new)
Place and Route Report	Current	Fri Dec 6 13:52:58 2013	0	0	3 Infos (3 new)
Power Report					
Post-PAR Static Timing Report	Current	Fri Dec 6 13:53:44 2013	0	0	4 Infos (4 new)
Bitgen Report	Current	Fri Dec 6 13:53:29 2013	0	0	0

Secondary Reports 		
Report Name	Status	Generated
WebTalk Report	Current	Fri Dec 6 13:53:30 2013
WebTalk Log File	Current	Fri Dec 6 13:53:30 2013

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