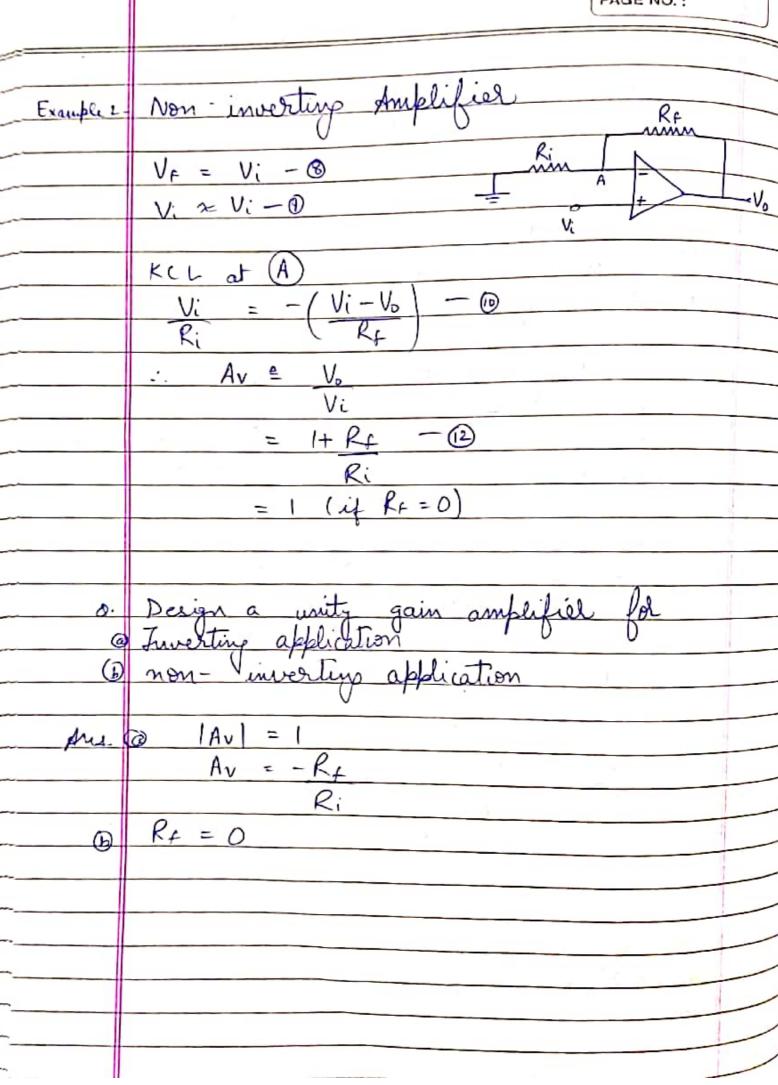
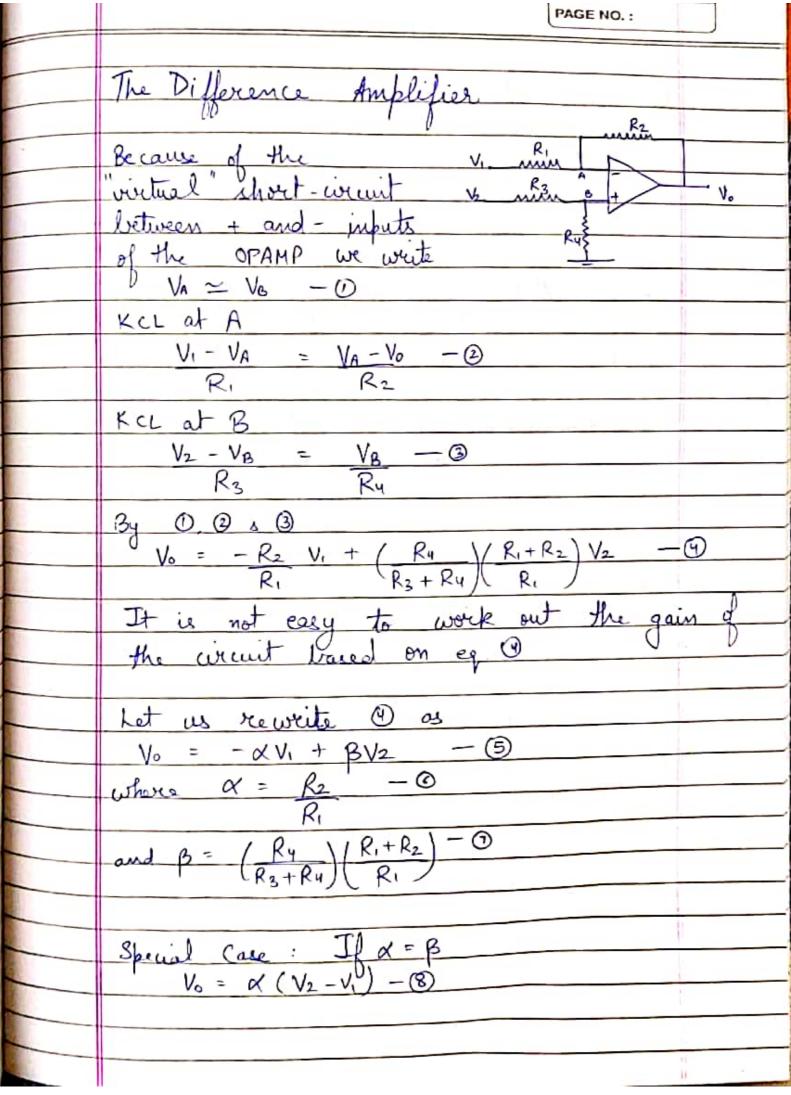
PAGE NO. : Operational Amplifiers The OP-AMP-1) The OPAMP is essentially speaking, a voltage amplifier with very high gain.
2) Essentially, the OPAMP contains a large number of individual amplifier stages enterconnected The OPAMP circuitry contains a of Bibolar Junction Transistors (BJT'S), Field effect transitors (FET's), resistors etc. space and is hence suited to being made available in the Integreted (incut (IC) form 5) The OPAMP Circuitry, for the sake of eace handeling, is contained in a longe multi Characteristics of an Ideal OP-AMP-Infinitely-large open loop gain. Infinitely-large Input Impedance Zero output Impedence The OPAMP is capable of producing operations like addition subtraction multiplication integration differentation etc. For each applications special wientory needs to be designed

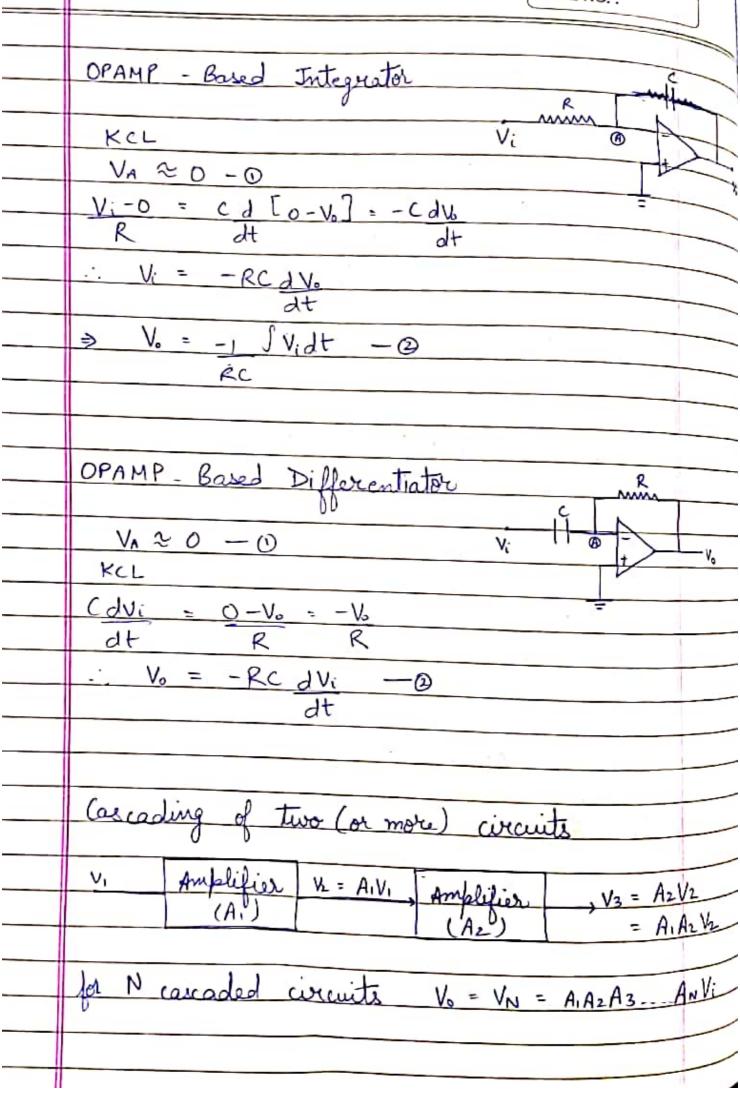
PAGE NO. : I+Vcc Vo output The Symbolnon-investing inferting - Vec A = open loop gain (Between 10 s 106 ey- Under ideal conditions, OPAMP'S input I = 0 Vi min Also because A is large, Va 20 In this case, lince we gave grounded + Terminal V+ = 0 - 3 V- = 0 - 1 Virtual ground concept The actual gain This is an example of inverting amplified As output is negative to there of impu

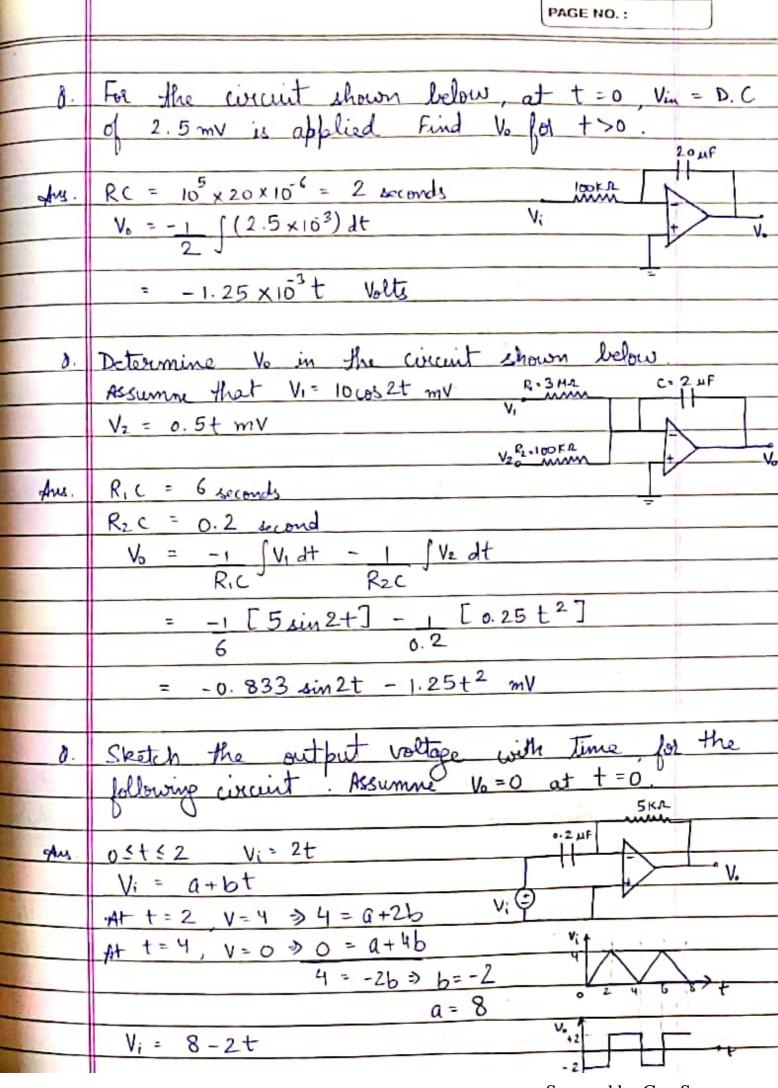




	PAGE NO. :
	of the following two inputs -
	of the Pollowing two inputs -
a	Differential Input - When 4 and 1/2 are
	simultaneously present and have 180 bloom
	Differential Input - When Vi and V2 are simultaneously present and have 180 plans difference between them. Common Mode Input - When Vi and V2 are simultaneously present and have zero phase slift between them.
b.	Common Mode Input - When Vi and V2 are
	simultaneously present and have zero phase
	slift between them.
	The differential input is defined as
	$V_{d} = V_{2}' - V_{1} - \mathfrak{D}$
	and Conamon mode input is defined as
	and Conamon mode input is defined as $V_{cm} = \frac{V_2 + V_1 - 0}{2}$
	$\frac{V_2 = V_d + 2V_{cm} - \overline{0}}{2}$ $V_1 = 2V_{cm} - V_d - \overline{0}$
	2
	Using @ 10 in 6 we get Vo = Ad Vd + Acm Vem
	Vo = Ad Vd + Acm Vem
	D:11 #16:
	Differential Grain Common-Mode Grain
	Where
	$Ad = \frac{1}{2} \left[\frac{R_2}{R_1} + \left(\frac{R_4}{R_3 + R_4} \right) \left(\frac{R_1 + R_2}{R_2} \right) \right] - (9) = \frac{1}{2} (\alpha + \beta)$
	1 10 310 5
	$A_{cm} = \frac{(R_4)(R_1 + R_2)}{(R_3 + R_4)(R_1)} = \frac{R_2}{R_1} = \frac{R_2}{R_1}$

	DATE: / /
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	Common Mode Rejection Ratio (CMRR) CMRR = Ad 1 - ®
	The dB terms, (HRR(dB) = 20 log Ad - 17) Ann
	Probl- Define an OP Amp.
0.	Deign an OP-Amp based Difference Amplifier with the output Voltage 1/6 = 1/2 - 2 VI where V x V2 are the input voltage.
Au	$X = R_2 = 2$ R_1 $R_3 = Z$ $Choose R = 1 \Rightarrow R_3 = Z$
	Summing Amplifier VA = 0 - 0
	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$



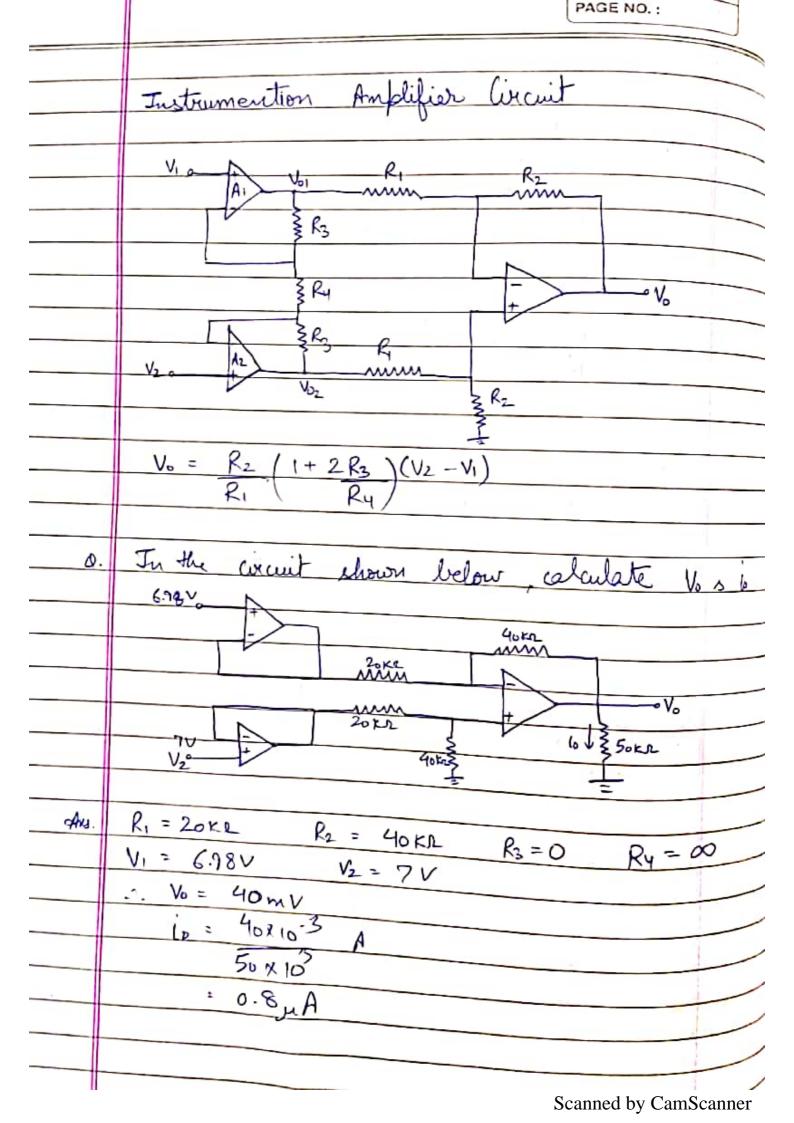


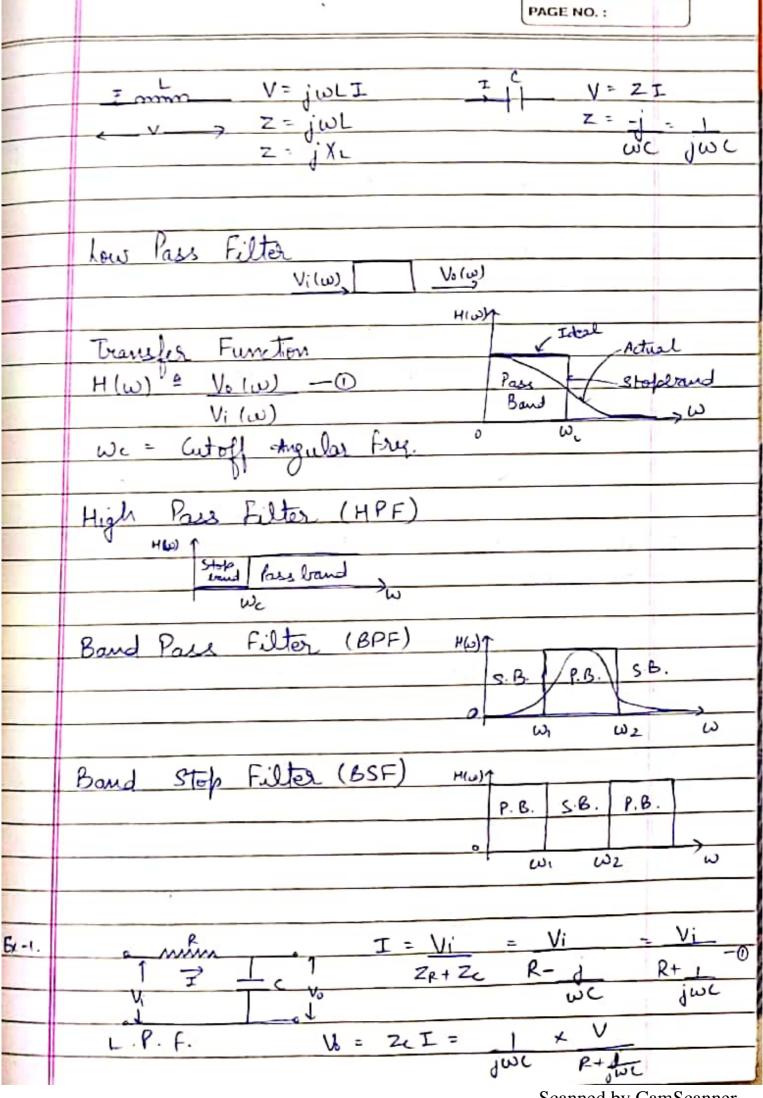
PAGE NO. : $\frac{-5 \times 10^{3} \times 0.2 \times 10^{6} \times -2}{-10^{-3} \times -2}$ 5K lok 4K 15 K Vb = -2V2 31, 1 (30V1+ 20 V2) Voltage Transfer Characteristic (VTC)

DATE: / PAGE NO. : -) Saturation region Follower (also known as Buffer inp but a non-inverting am

	PAGE NO.:
	- A 10.0.
-	Instrumentation Amplifion
	VI -> Buffer 1 - + Difference -> Vo
	V2 -> Buffer 2 -
	Advantages -
1.	High CMRR is possible
2.	Advantages - High CMRR is possible High Tuput Impedence Grand Scrietivity to small inputs
	Detailed Circuit
	V, Pan
	₹R3
	5 Hos num
	R_2 R_3 R_2
	V ₂ 1
	Vo = Ad (Voz - Vo1) + Aan (Voz + Vo1) 0
	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$
	$Ad = \frac{1}{2} \begin{bmatrix} R_2 + R_{11} & R_{1} + R_{2} \\ R_1 & R_{3}' + R_{1}' & R_1 \end{bmatrix} = \frac{R_2}{R_1}$
	$A cm = R'' \cdot R_1 + R_2 - R_2 = 0$ $R''_3 + R''_4 \cdot R_1 \cdot R_1$
	$ \frac{R_{3}' + R_{4}'}{R_{3}' + R_{4}'} \frac{R_{1}}{R_{1}} \frac{R_{2}}{R_{1}} $ $ = V_{0} = R_{2} (V_{02} - V_{01}) - Q $

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-	
	I = V01-V02 -3
	2 R3 + R4
	$V_A = V_1 - \Theta$
	VB = V2 - 5
1	
	$I = \frac{V_A - V_B}{R_4} = \frac{V_1 - V_2}{R_4} - \Theta$
	We should get Vo = R2 (2R3 + Ry)(V2 - V1) - (7) R1 (R4)
	Vo = R2 /2R3+Ry)(V2-V1) - (7)
	RI Ru
	Special Cale-
	het RI = Re = R3 = R and let RI = R6 (variable)
	Then Vo = 2R+ RG (V2-V1)
	Re
	$= [1 + 2R](V_2 - V_1) - 8$
	L Ra J
0.	For the 3.A alterady considered R= R2 = R3=10 KR, V1 = 2.011 V, V2 = 2.017 V and Ry = 50052.
	V1 = 2.011 V , 1/2 = 2.0147 V and Ky = 50052.
	Calculate Vo.
	2 2 2 111
ofu.	Grain = 1+2R = 1+2x10x103 = 41
1	$V_2 - V_1 = 6mV$
	Vo = 41 × 6 × 10-3



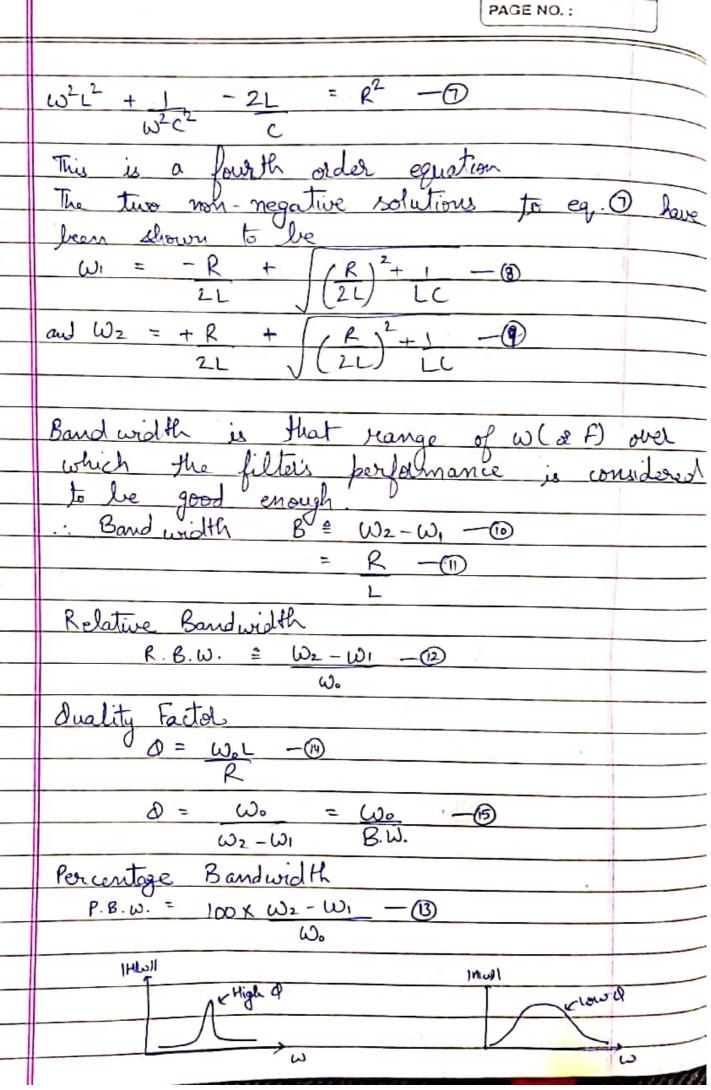


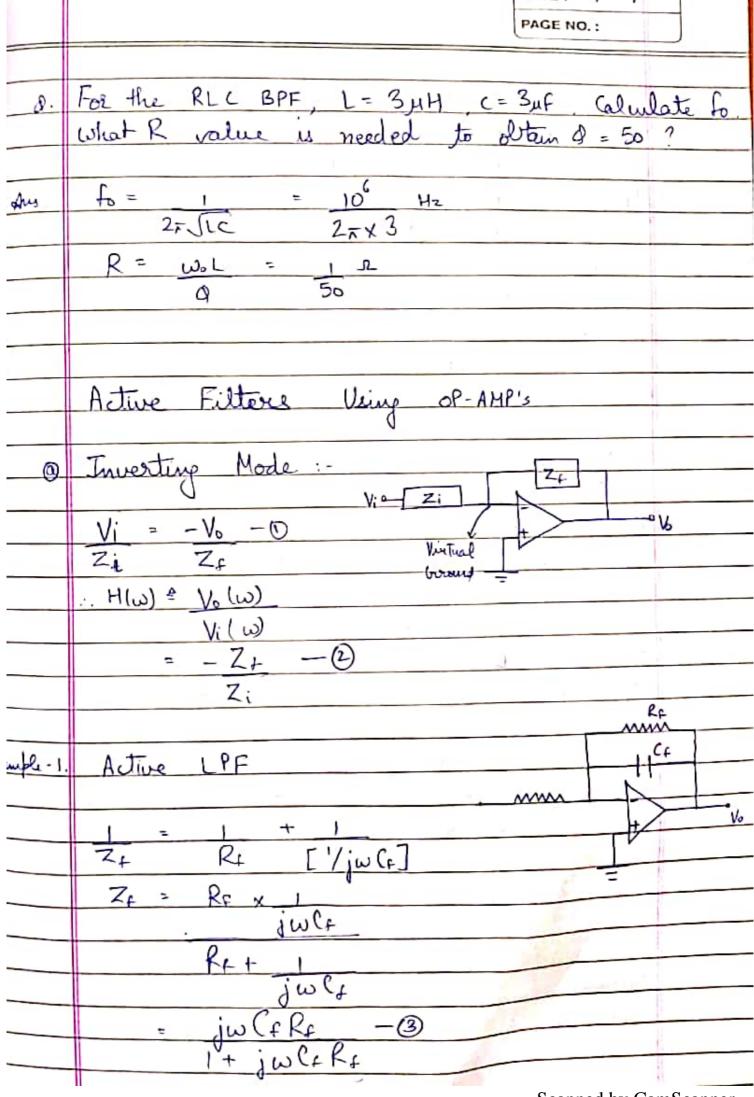
	PAGE NO.:
	Vo = Vi -@
	1+jwRC
	$\frac{H(\omega) = V_0(\omega)}{V_1(\omega)} = 1 - 3$ $V_1(\omega) = 1 + i\omega RC$
	1H(w) = 1 -0
	$\sqrt{1+\omega^2R^2c^2}$
	At w= Wc , IHI = 1 -B
	J2
-	$\sqrt{\frac{1}{2}} = \frac{1}{1120}$
	$\int \frac{\sqrt{2}}{1+\omega^2 R^2 c^2}$ $2 = 1+\omega^2 R^2 c^2$
	:. Wc = 1 - 0 . fc - 1 - 0
	JRC 2xJRC
۵.	Veins a luf capacité deign RC LPF for te = 1 MHL & fc = 1 KHZ & fc = 100 MHZ
<u> </u>	THE WITE IDO MILE
Aus.	1×106 = 1 , R=1
-	2 R X 1 X 10° 2 K
ð.	Find H(w)?
	1 1 2 1
Au.	I = Vi
	R+ 1/jux
	Vo = IR MIA
	HW = R = jwRC = R+jwRC
	juc x+juic
11	Scanned by CamScanner

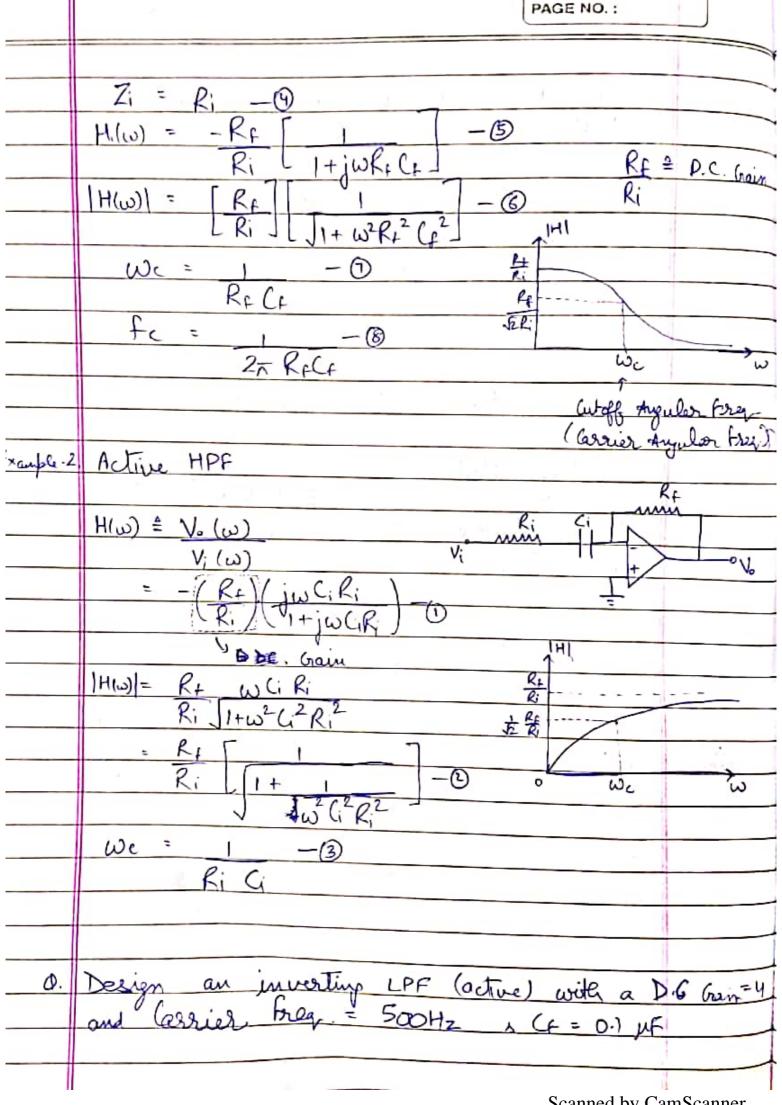
	DATE: / /
	PAGE NO.:
	- 11
	Filters examples 1 x 2 were of "Re Fitter" category. They can also be termed "Passive" filters because also be termed "First-order" filters because the mathematical expression for HIW) contains only W terms (now 2 terms, now 3 terms, etc.)
	because to DC input is needed. They can
	also be termed "First - order" filters because
	the mathematical expression for HW contains
23	only W terms (now 2 terms, now 3 terms, etc.)
	O .
xample-3.	Passive Fruit - Order RL Filters (LPF)
	$I = V; -0$ $R + j\omega L$ $V_0 = RI = RV; -0$ $R + j\omega L$ $V_1 = RV$ $R + j\omega L$
	R+jwl T
	V ₀ = RI = RV; -② V; ₹R V ₀
	R+JWL
	$H(\omega) = V_0(\omega) = R$
	Vi(w) R+jwl IHIWIT
	$H(\omega) = \frac{1}{1 + i(\omega)}$
	1+ jwl 1/2
	H(w) = 1 -9 0 wc w
	$1 + \omega^2 L^2$
	$\frac{1+\omega^2L^2}{R^2}$
	H cutoff
	1 = 1
	$\int \frac{\sqrt{2}}{R^2}$
	$\sqrt{R^2}$
	$\therefore 2 = 1 + \omega x^2 L^2$
	> Wc = R - 0 :. Fc = R - 0
-	L 2⊼L
4.5	

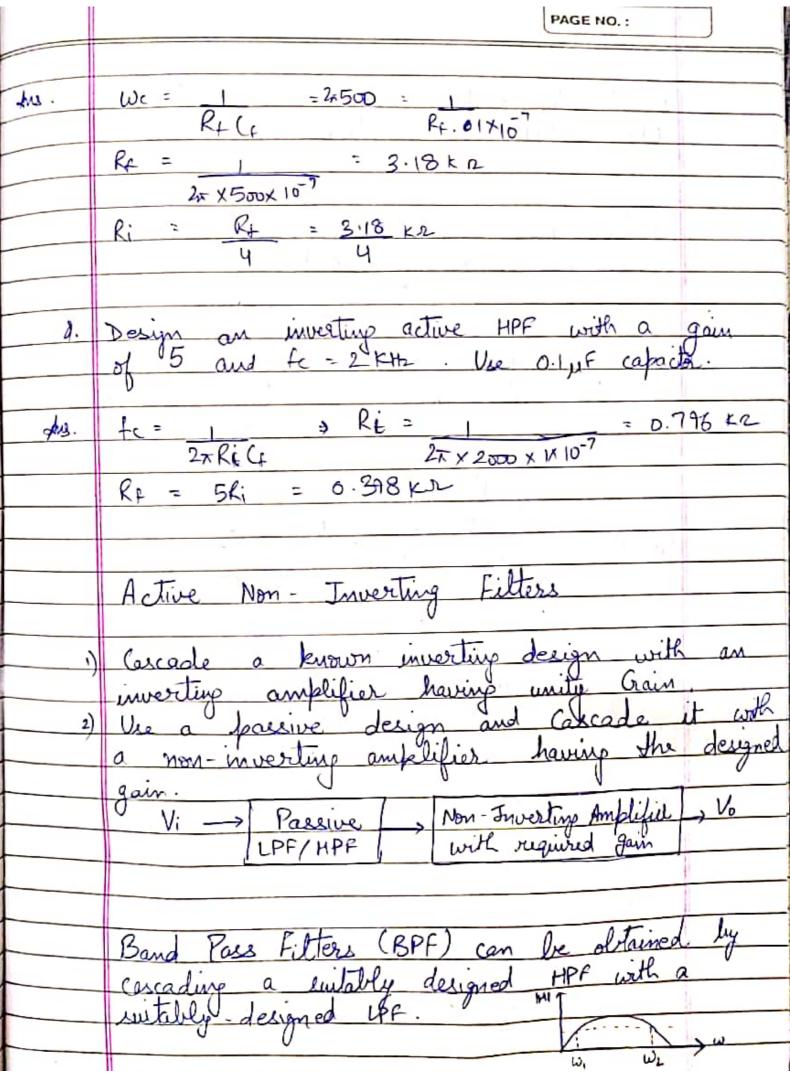
DATE: PAGE NO. : Frankle-4 basive First-Older Filter (HPF) R R+jwL -14(w) WC :.fc = R -0 BOL R-L LPF, if L=3 µH, what R is need to obtain fc = 350 KHz? 13:13:2 6.65 Aug.

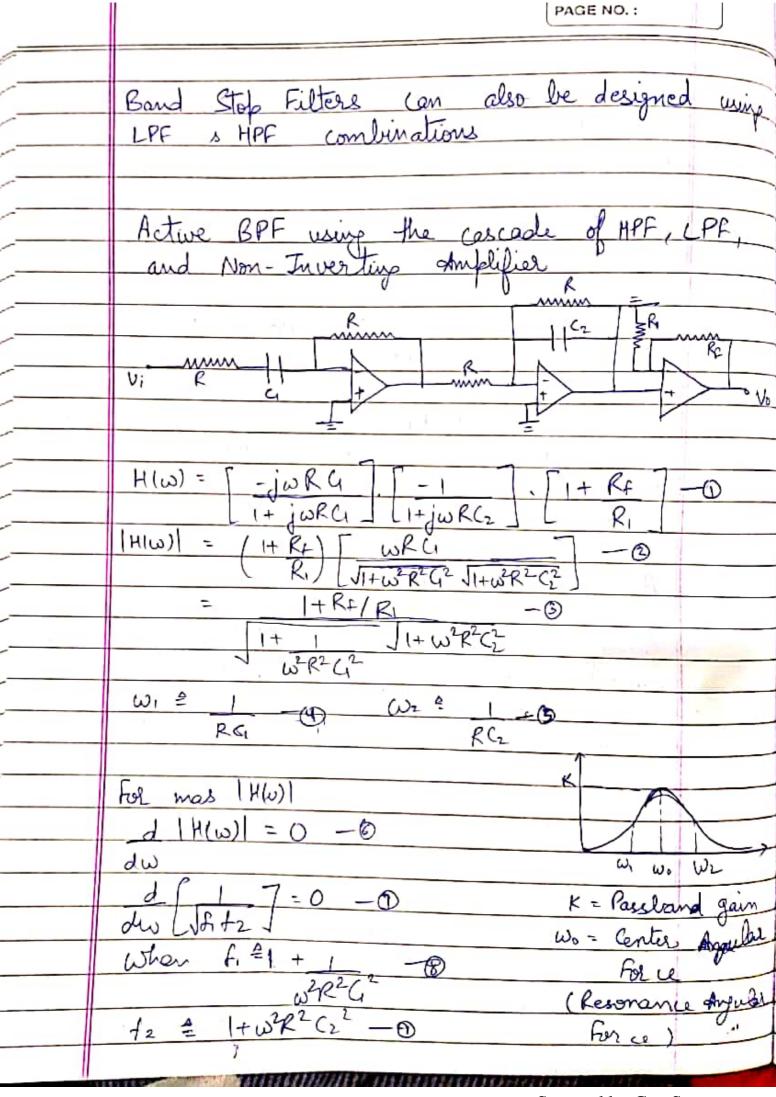
DATE : PAGE NO. : Second-older Passive Filter Vi -0 R+jwl-J wc Vo = RI - 3 Vo = RVi R+j(wr-1) .. H(w) = V.(w) |H(w)| = At resomence, w= wo and |H(w) = 1 WOL Wo C (WL-1)2- R2











PAGE NO. : July $\frac{d}{d\omega} \left[(f_1 f_2)^{1/2} \right] = 0$ $\frac{d\omega}{d\omega} \left[(f_1 f_2)^{3/2} \right] = 0$ $\frac{f_1}{dw} \frac{df_2}{dw} + \frac{f_2}{dw} \frac{df_1}{dw} = 0 \quad \sqrt{D}$ $\frac{df_1}{dw} = -\frac{2}{Rc^2w^2}$ $W_0 = \frac{1}{R} \int_{C_1C_2} \frac{1}{r^{-1/2}}$ $= \frac{1}{R} \int_{C_1C_2} \frac{1}{RC_2} = \frac{1}{R} \int_{C_2C_2} \frac{1}{RC_2}$ We define $f_1 = W_1$ 2π Pareland Grain $|H(\omega)| = 1 + R + |R|$ $\int_{\omega^2 R^2 \zeta_1^2} 1 + \omega^2 R^2 \zeta_2^2$ K = 1 + R + |R|

PAGE NO.:
Comparator circuit using an OP-AMP in open loop.
V+ Vec V- + Vcc (HIGH) for V+ > V- - Vcc = - Vcc (LOW) for V+ < V-
Disadvantage - Even for a small change in input (could by say random noise signals), the output can witch from one elate to another in quite an impredictable fashion (random behaviour).
Schmitt Trigger is a comparator circuit which uses possible positive feedback to make the output more predictable.
Non - Inverting Schmitt Trigger:
Vi. Riv. V+ + Whigh enough of low enough
KCL V: -V+ + Vo-V+ = 0 -0
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$
$\frac{V_{+} \begin{bmatrix} R_{1} + R_{2} \\ R_{1} R_{2} \end{bmatrix}}{R_{1} R_{2}} = \frac{V_{1} + V_{0}}{R_{1} R_{2}}$

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7	V+ = [R2] V; + [R1] Vo - @)
	$V_{+} = \begin{bmatrix} R_{2} \\ R_{1} + R_{2} \end{bmatrix} V_{1} + \begin{bmatrix} R_{1} \\ R_{1} + R_{2} \end{bmatrix} V_{0} - \textcircled{2}$
(A)	is HIGH that is $V_0 = + V_{CC}$. Then eq @ becomes
T.	is HIGH that is Vo = + Vcc. Then eg @ becomes
	$V_{+} = \begin{bmatrix} R_{2} \\ R_{1} + R_{2} \end{bmatrix} V_{1} + \begin{bmatrix} R_{1} \\ R_{1} + R_{2} \end{bmatrix} V_{2} - 3$
X.	The output will always switch to the
	other state when V+ crosses o in
	either direction. For V+ = 0 cg @ yields
	V: = -R1 Vcc - 9
	R ₂
1982	
	We define a liveshold vollage Vm by
	We define a Threshold voltage Vm by Vm = R1 Vic - 5
	so that eq. 9 can be written as Vi = - Vm - 0
(b)	TO V. Dalle lesson - VT the withet well
6)	entel to lar i.e. Vo = - Voc
	Those es (2) will yield
	V+ = / R2 Vi - / R. Vcc -0
	Jf Vi falls befour - Vm the sutfut will with to low , i.e. $V_0 = -V_{CC}$ Then eq @ will yield $V_+ = \begin{pmatrix} R_2 \\ R_1 + R_2 \end{pmatrix} V_1 - \begin{pmatrix} R_1 \\ R_1 + R_2 \end{pmatrix} V_{CC} - O$
	Hence the output will now switch at
	Vin = + VTh -18
Beer .	
0	This sequence can continue
107	

