

10M

Module 1 :- Numerical sums of 10M  
Thevenin, Norton, Mesh, Norton's, Max power

10M

Module 2 :- Numerical on RL, RC, RLC series  
only numericals from notes, No theory or derivation  
From back of notes

10M

Module 3 :- 10M  
Steps, UPS, inverter  
Switch mode power supply

10M

Module 4 :-  
No number conversions (X)

Theory :-

Short notes  
10M

- 1) Number system
- 2) Grey code
- 3) BCD code
- 4) Binary Arithmetic using 1 bit

10M

Module 5 :-

~~K-Map compulsory~~ 10M *ratta + understanding*

~~Steps in designing combinational circuit~~

↳ ~~what is combinational circuit~~ → *Ratta*

↳ ~~Write and reduce one eq<sup>n</sup>~~ → *understanding*

*(=) This has been done from AJ*

• Rules of binary arithmetic

• ~~Boolean Laws [10 Laws]~~ → *Ratta*

~~Shortnote on K-map~~

→ *for this write defn of kmap 2, 3, 4 variable grid diagram*

Theory

• ~~K-map grouping rules~~

Theory

• ~~Minimization of boolean expression~~

• ~~De-Morgan's Law~~ *(2 demorgan's law along with table)*

Realization of basic gates using NAND and NOR  
~~Shortnote on SOP and POS~~ → *Ratta*

*Same as this*

*only numericals*

*only numericals*

*Full ratta.*

*Full ratta*



optional

6) Module 6:- [10 M]

Half adder, Full adder, Half subtractor, Full subtractor, encoder, decoder, multiplexer, de-multiplexer

④

Highlight the role of Select Lines in 2ns/mr