Lab 4.0

Camera & LCD interface implementation

# Introduction

# Goal

The goal of this lab is for you to implement the detailed design you proposed in lab 3.0 for an FPGA-based system which can interface with one of the following peripherals on the DE0-Nano-SoC:

* [TRDB-D5M](http://www.terasic.com.tw/cgi-bin/page/archive.pl?No=281) camera
* [LT24](http://www.terasic.com.tw/cgi-bin/page/archive.pl?Language=English&CategoryNo=68&No=892) LCD display

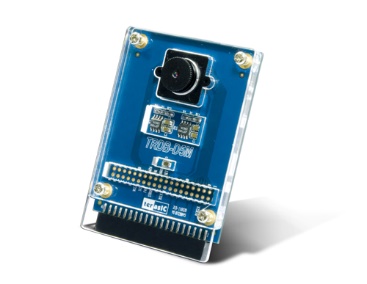


Figure 1. TRDB-D5M Camera Interface & LT24 LCD Interface