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Transmitting Data Using the STM8S SPI Master Mode

So far in <u>The Way of the Register</u> series we have only looked at SPI from a slave device point of view as we have been working towards creating a Netduino GO! module. For every slave device there must be a master, here we will look at configuring the STM8S to operate in SPI master mode.

The project will look at controlling a TLC5940 in order to emulate the work described in the post <u>TLC5940 16</u> <u>Channel PWM Driver</u>. We could simply bit-bang the data out to the chip but instead we will use the SPI interface to achieve this.

The project breaks down into the following steps:

- 1. Generate the grey scale clock and blank signals
- 2. Bit-Bang data out over GPIO pins to create an operational circuit
- 3. Convert the data transmission to SPI

See the quoted post for a description of how this chip works and for an explanation of the terminology used.

Generating the Grey Scale Clock and Blank Signals

The TLC5940 generated 4,096 grey scale values by using a PWM counter. Once the counter reaches 4096 pulses it stops until it is told to restart. The Blank pulse acts as a restart signal. This project will be controlling LEDs and so will want to continuously keep the counter running. If we did not keep the counter in the TLC5940 running then the LEDs would light for a short while and then simply turn off and remain off.

The greyscale clock is generated by using the Configurable Clock Output (CCO) pin on the STM8S. This pin simply outputs the clock pulses used to drive the STM8S. Reviewing the data sheet we find that the maximum value for the grey scale clock is 30MHz. Using out standard clock initialisation generates a clock with a frequency of 16MHz (approximately). This is well within the tolerances of the TLC5940. To output this we need to make a simple modification to our standard code, name change the line:

```
1 CLK CCOR = 0; // Turn off CCO.
```

to:

```
CLK_CCOR = 1; // Turn on CCO.
```

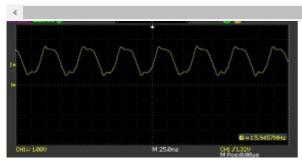
The starting point for our application becomes:

```
#if defined DISCOVERY
#include <iostm8S105c6.h>
```

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```
8
     #include <intrinsics.h>
 9
10
     //
11
     //
         Setup the system clock to run at 16MHz using the internal oscillator.
12
     //
13
     void InitialiseSystemClock()
14
     {
15
         CLK ICKR = 0;
                                               //
                                                    Reset the Internal Clock Register
         CLK_ICKR_HSIEN = 1;
                                                    Enable the HSI.
16
17
         CLK_ECKR = 0;
                                               //
                                                    Disable the external clock.
         while (CLK ICKR HSIRDY == 0);
                                                    Wait for the HSI to be ready for
18
                                               //
19
         CLK CKDIVR = 0;
                                               //
                                                    Ensure the clocks are running at
                                               //
20
                                                    Enable all peripheral clocks.
         CLK \ PCKENR1 = 0xff;
21
         CLK \ PCKENR2 = 0xff;
                                               //
                                                    Ditto.
22
         CLK CCOR = 1;
                                               //
                                                    Turn on CCO.
                                                    Turn off any HSIU trimming.
23
         CLK HSITRIMR = 0;
                                               //
                                               //
         CLK_SWIMCCR = 0;
24
                                                    Set SWIM to run at clock / 2.
25
                                               //
                                                    Use HSI as the clock source.
         CLK SWR = 0xe1;
                                                    Reset the clock switch control re
26
         CLK SWCR = 0;
                                               //
                                               //
27
         CLK SWCR SWEN = 1;
                                                    Enable switching.
28
         while (CLK_SWCR_SWBSY != 0);
                                               //
                                                    Pause while the clock switch is t
29
     }
30
     //
31
32
     //
         Main program loop.
33
     //
34
     void main()
35
     {
36
         //
37
             Initialise the system.
         //
38
39
           _disable_interrupt();
40
         InitialiseSystemClock();
41
           enable interrupt();
42
         while (1)
43
         {
44
               _wait_for_interrupt();
45
         }
46
     }
```

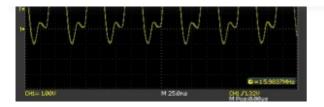
Wiring up the STM8S and connecting the scope to PC4 (CCO output pin) gives the following trace on the scope:



CCO On Scope

The trace on the scope has a minimum value of around 680mV and a maximum of 2.48V. In an ideal world this signal should range from 0 to 3.3V (based upon a 3.3V supply). Adding an inverter from a 74HC04 and feeding

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Inverter output on the scope

This is starting to look a lot better. The next task is to create the Blank signal. There are several ways of doing this. The most automatic way of doing this is to generate a <u>very short PWM pulse using one of the timers</u> in the STM8S. One drawback of this method is that it is more difficult to generate a Blank pulse on demand. Instead we will use the interrupt method described in the same article. Whilst not automatic it is still a trivial task to complete. We simply modify the code from the method to load the counters with 4,096. The code for the GPIO port, timer and interrupt becomes:

```
1
     //
 2
     //
         Timer 2 Overflow handler.
 3
     //
     #pragma vector = TIM2_OVR_UIF_vector
 4
 5
       interrupt void TIM2 UPD OVF IRQHandler(void)
 6
 7
         PD ODR ODR4 = 1;
 8
         PD_ODR_ODR4 = 0;
 9
         TIM2 SR1 UIF = 0;
                                       Reset the interrupt otherwise it will fire ag
10
     }
11
12
     //
13
         Setup Timer 2 to generate an interrupt every 4096 clock ticks.
     //
14
     //
15
     void SetupTimer2()
16
     {
17
         TIM2 PSCR = 0 \times 00;
                                   //
                                       Prescaler = 1.
         TIM2 ARRH = 0 \times 10;
                                   //
                                       High byte of 4096.
18
                                   //
19
         TIM2 ARRL = 0 \times 00;
                                       Low byte of 4096.
20
                                       Turn on the interrupts.
         TIM2_IER_UIE = 1;
                                   //
21
         TIM2 CR1 CEN = 1;
                                   //
                                       Finally enable the timer.
22
     }
23
24
     //
25
         Setup the output ports used to control the TLC5940.
     //
26
27
     void SetupOutputPorts()
28
     {
29
         PD ODR = 0;
                                       All pins are turned off.
30
         PD_DDR_DDR4 = 1;
                                       Port D, pin 4 is used for the Blank signal.
                                   //
         PD_CR1_C14 = 1;
31
                                       Port D, pin 4 is Push-Pull
                                   //
32
         PD CR2 C24 = 1;
                                   //
                                       Port D, Pin 4 is generating a pulse under 2 M
     }
33
```

Hooking up the scope to PD4 gives the following trace:

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Blanking pulses

The single pulses are being generated at a frequency of approximately 3.9kHz. A little mental arithmetic dividing the 16MHz clock by 4,096 comes out to about 3,900.

Zooming in on the signal we see:



Single Blanking Pulse

This shows that the signal is 125nS wide. This is acceptable as the minimum pulse width given in the data sheet is 20nS.

So at this point we have the 16MHz grey scale clock signal and the Blank pulse being generated every 4,096 clock pulses.

Connecting the TLC5940

The next task is to connect the STM8S to the TLC5940. You should refer to the article <u>TLC5940 16 Channel PWM Driver</u> for more information on the pins and their meaning. For this exercise we will use the following mapping:

| STM8S Pin | TLC5940 Pin |
|--------------------|-----------------------|
| PD4 | Blank (pin 23) |
| PD3 | XLAT (pin 24) |
| PD2 | VPRG (pin 27) |
| PD5 | Serial data (pin 26) |
| PD6 | Serial clock (pin 25) |
| PC4 (via inverter) | GSCLK (pin 18) |

You will note that the serial data and clock are currently connected to PD5 and PD6 respectively. Whilst the eventual aim is to communicate with the TLC5940 via SPI, the initial communication will be using Bit-Banging. We will move on to using SPI once the operation of the circuit has been proven using tested technology.

The first changes we will have to make create some #define statements to make the code a little more readable. We also add some storage space for the grey scale and dot correction data.

```
1  //
2  // Define which pins on Port D will be used as control signals for the TLC59
3  //
4  // BLANK - A pulse from low to high causes the TLC5940 to restart the counter
```

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```
#define PIN VPRG
                                           PD ODR ODR2
10
11
12
     //
         Bit bang pins.
13
     //
14
     #define PIN BB DATA
                                           PD ODR ODR5
15
     #define PIN BB CLK
                                           PD ODR ODR6
16
17
        Values representing the modes for the VPRG pin.
     //
18
19
     #define PROGRAMME DC
     #define PROGRAMME GS
20
                                           0
21
22
     //
23
     // TLC5940 related definitions.
24
25
     #define TLC NUMBER
     #define TLC_DC_BYTES_PER_CHIP
                                           12
26
27
                                          TLC NUMBER * TLC DC BYTES PER CHIP
     #define TLC DC BYTES
     #define TLC GS BYTES PER CHIP
28
29
     #define TLC GS BYTES
                                          TLC NUMBER * TLC GS BYTES PER CHIP
30
31
     //
        Next we need somewhere to hold the data.
32
     //
33
34
     unsigned char _greyScaleData[TLC_GS_BYTES];
     unsigned char _dotCorrectionData[TLC_DC_BYTES];
35
```

The Bit-Banging methods should look familiar to anyone who has been reading any of the posts in <u>The Way of the Register</u> series.

```
1
 2
     //
 3
     //
         Bit bang data.
 4
     //
 5
     //
         TLC5940 expects the data to be shifted MSB first.
         is shifted in on the rising edge of the clock.
 6
     //
 7
     void BitBang(unsigned char byte)
 8
 9
     {
         for (short bit = 7; bit >= 0; bit--)
10
11
         {
              if (byte & (1 << bit))</pre>
12
13
              {
14
                  PIN BB DATA = 1;
15
              }
              else
16
17
              {
                  PIN_BB_DATA = 0;
18
19
              PIN_BB_CLK = 1;
20
              PIN_BB_CLK = 0;
21
22
         PIN BB DATA = 0;
23
24
     }
```

```
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```

Related to the Bit-Banging methods are the two methods which will send the grey scale and dot correction data:

```
1
2
3
    // Send the grey scale data to the TLC5940.
4
    //
5
    void SendGreyScaleData(unsigned char *buffer, int length)
6
7
        PIN VPRG = PROGRAMME GS;
8
        BitBangBuffer(buffer, length);
9
        PulseXLAT();
10
        PulseBlank();
    }
11
12
    //-----
13
14
15
    // Send the dot correction buffer to the TLC5940.
16
    //
17
    void SendDotCorrectionData(unsigned char *buffer, int length)
18
19
        PIN VPRG = PROGRAMME DC;
20
        BitBangBuffer(buffer, length);
        PulseXLAT();
21
22
        PulseBlank();
23
    }
```

We also need a few methods to make the TLC5940 latch the data and also restart the counters:

```
//-----
1
2
   //
3
   // Pulse the Blank pin in order to make the TLC5940 reload the counters and
4
   // restart timer.
5
   //
6
   void PulseBlank()
7
   {
8
      PIN BLANK = 1;
9
      PIN BLANK = 0;
   }
10
11
   //-----
12
13
14
      Pulse the XLAT pin in order to make the TLC5940 transfer the
15
      data from the latches into the appropriate registers.
   //
16
   //
17
   void PulseXLAT()
```

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Next we need to set the initial condition. For this we set the TLC dot correction off and also turn all of the LEDs off:

```
//-----
1
2
    //
3
       Initialise the TLC5940.
4
    //
5
    void InitialiseTLC5940()
6
        for (int index = 0; index < TLC DC BYTES; index++)</pre>
7
8
            dotCorrectionData[index] = 0xff;
9
10
        for (int index = 0; index < TLC_GS_BYTES; index++)</pre>
11
12
           greyScaleData[index] = 0;
13
14
15
        SendDotCorrectionData( dotCorrectionData, TLC DC BYTES);
        SendGreyScaleData( greyScaleData, TLC GS BYTES);
16
    }
17
```

The final support method we need to add is the method which sets the brightness of an LED. The brightness is a 12-bit value (0-4095). This means each LED uses 1.5 bytes for the brightness value. The following methods breaks down the value and ensures that the correct bits are set in the grey scale buffer depending upon which LED is being changed:

```
//-----
1
2
    //
3
    // Set the brightness of an LED.
4
5
    void SetLEDBrightness(int ledNumber, unsigned short brightness)
6
7
        int offset = (ledNumber >> 1) * 3;
        if (ledNumber & amp; 0x01)
8
9
            greyScaleData[offset + 1] = (unsigned char) ( greyScaleData[offset +
10
11
            greyScaleData[offset + 2] = (unsigned char) (brightness & 0xff);
12
        }
13
        else
14
            _greyScaleData[offset] = (unsigned char) ((brightness & 0x0ff0) >
15
            _greyScaleData[offset + 1] = (unsigned char) ((brightness & 0x0f) >>
16
17
        }
18
    }
```

We should also create a similar method for changing the dot correction value for an LED. This is left as an exercise for the reader as we will not be changing this value in this code.

Proving the concept

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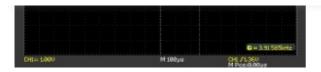
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```
1
 2
 3
     // Main program loop.
 4
     //
 5
     void main()
 6
     {
 7
             Initialise the system.
 8
         //
 9
           disable interrupt();
10
         InitialiseSystemClock();
11
12
         SetupOutputPorts();
13
         SetupTimer2();
14
         InitialiseTLC5940();
15
           enable_interrupt();
16
         //
17
             Main program loop.
18
         //
19
         //
20
         int brightness = 0;
21
         int counter = 0;
         while (1)
22
23
24
               wait for interrupt();
25
              counter++;
              if (counter == 20)
26
27
              {
28
                  TIM2 CR1 CEN = 0;
29
                  counter = 0;
                  for (int index = 0; index < 16; index++)</pre>
30
31
                  {
                      SetLEDBrightness(index, brightness);
32
33
                  SendGreyScaleData(_greyScaleData, TLC_GS_BYTES);
34
35
                  brightness++;
                  if (brightness == 4096)
36
37
38
                      brightness = 0;
39
                  TIM2 CR1 CEN = 1; // Finally re-enable the timer.
40
41
              }
42
         }
43
     }
```

If you connect a scope to the cathode of one of the LEDs you will see that the wave form slowly changes over time. At the start, the LED is fully on and the trace on the scope shows a horizontal line, i.e. a constant voltage. As time moves on and the value in the dot correction buffer changes you start to see a PWM signal similar to the following:

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PWM Output On Scope 1

This trace shows the signal when the LEDs are a little brighter:



PWM Output On Scope 2

Having arrived here we now know that the circuit has been connected correctly and that the control logic in the main method works. We can now move on to considering what we need to do in order to use SPI in master mode. The aim will be to simply remove the Bit-Banging methods and replace these with an interrupt driven SPI master algorithm.

SPI Master

So now we have a working circuit we need to look at SPI on the STM8S. Firstly let's remind ourselves of the serial communication parameters for the TLC5940. This chip reads the data on the leading clock edge (CPHA = 1). We have also set the clock idle state to low (CPOL = 0).

It is also advisable to start off using the lowest clock speed for SPI in order to confirm correct operation of the software and circuit. Lower speed are less likely to be subject to interference.

SPI Registers

You should review the previous articles on SPI communication if you are not already familiar with the SPI registers we have used so far. In this post we will only discuss the new settings required to switch from being a SPI slave device to a SPI master device.

SPI_CR1_BR - Baud Rate Control

The SPI baud rate is determined by the master clock frequency and the value in this register. The divisor used to set the baud rate according to the following table:

| SPI_ | _CR1_ | _BR Divisor |
|------|-------|-------------|
| 000 | | 2 |
| 001 | | 4 |
| 010 | | 8 |
| 011 | | 16 |
| 100 | | 32 |
| 101 | | 64 |
| 110 | | 128 |
| | | |

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SPI_CR1_MSTR - Master Selection

Setting this bit switches SPI into master mode (see also SPI CR1 SPE).

Note that the reference for the STM8S also states that this bit (and SPI_CR1_SPE) will only remain set whilst NSS is high. It this therefore essential to connect NSS to V_{cc} if this device is not being used as a slave device.

Implementing SPI

Using SPI presents us with a small problem, namely the program will have to start to operate in a more asynchronous way. The code presented so far has only one interrupt to be concerned with, namely the timer used to control the Blank signal. Adding SPI to the mix means that we will have to also consider the SPI interrupt as well. It also adds the complication of sending dot correction data followed by grey scale data. This last problem will not be covered here and is left as an exercise for the reader.

The initialisation code merely sets things up for us:

```
//-----
1
2
3
    // Initialise SPI to be SPI master.
4
5
    void SetupSPIAsMaster()
6
7
       SPI CR1 SPE = 0;
                                       // Disable SPI.
8
       SPI CR1 CPOL = 0;
                                           Clock is low when idle.
                                       //
9
       SPI_CR1_CPHA = 0;
                                       // Capture MSB on first edge.
                                           Enable the SPI TXE interrupt.
10
       SPI ICR TXIE = 1;
                                       // fmaster / 256 (62,500 baud).
11
       SPI CR1 BR = 7;
       SPI CR1 MSTR = 1;
                                       //
                                           Master device.
12
    }
13
```

Much of the code should be familiar as it has been used in previous posts discussing SPI slave devices. Not however that we do not enable SPI at this point. We simply set the scene for us to use SPI later.

The SPI data transfers will be controlled by using an interrupt service routine:

```
//-----
1
2
3
    // SPI Interrupt service routine.
4
    #pragma vector = SPI_TXE_vector
5
6
     7
8
       //
9
          Check for an overflow error.
       //
10
       //
       if (SPI SR OVR)
11
12
                                      // These two reads clear the over
13
          (void) SPI DR;
14
          (void) SPI SR;
                                      // error.
15
          return;
       }
16
```

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```
if ( txBufferIndex == txBufferSize)
22
23
24
                  while (SPI_SR_BSY);
25
                  SPI CR1 SPE = 0;
26
                  txBuffer = 0;
                  PulseXLAT();
27
28
                  PulseBlank();
29
                  TIM2 CR1 CEN = 1;
30
              }
              else
31
32
              {
                  SPI DR = txBuffer[ txBufferIndex++];
33
34
              }
35
         }
36
     }
```

The main works starts when we have established that the transmit buffer is empty (SPI_SR_TXE is set). If we have more data then we put the byte into the data register (SPI_DR). If we have transmitted all the data we have then we wait for the last byte to complete transmission (SPI_SR_BSY becomes false) before we start to terminate the end of the SPI communication.

In order to send some data we really just need to setup the pointers and counters correctly and then enable SPI. So the *SendGreyScaleData* method becomes:

```
_____
1
2
    // Send the grey scale data to the TLC5940.
3
4
    //
5
    void SendGreyScaleData(unsigned char *buffer, int length)
6
        PIN_VPRG = PROGRAMME GS;
7
8
        _txBuffer = buffer;
9
        _txBufferIndex = 0;
10
         txBufferSize = length;
        TIM2 CR1_CEN = 0;
11
        SPI CR1 SPE = 1;
12
13
    }
```

We also need to have a look at the main program loop as we use the __wait_for_interrupt() method in order to determine when we should start to process the next LED brightness value. We now need to ignore the interrupts when SPI is enabled otherwise the brightness will increase each time the transmit buffer is empty. A crude implementation eliminating the SPI interrupts is:

```
1
    int brightness = 0;
2
    int counter = 0;
3
    while (1)
4
5
          _wait_for_interrupt();
        if (!SPI CR1 SPE)
6
7
8
             counter++;
9
             if (counter == 20)
```

```
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```

```
SetLEDBrightness(index, brightness);
15
16
                  SendGreyScaleData(_greyScaleData, TLC_GS_BYTES);
17
                  brightness++;
18
                  if (brightness == 4096)
19
20
                  {
21
                      brightness = 0;
22
23
                  TIM2_CR1_CEN = 1;
                                           // Finally re-enable the timer.
24
             }
25
         }
26
     }
```

Making these changes and running the code shows that the system operated as before.

Increasing the Baud Rate

As noted earlier, the baud rate has been set low in order to reduce the chance of any problems being experienced due to interference. Now we have established that using SPI communication is possible and the circuit works as before we can start to increase the baud rate. Using our 16MHz clock we find we have the following baud rates which are theoretically possible:

SPI_CR1_BR Divisor SPI Frequency

| 000 | 2 | 8 MHz |
|-----|-----|----------|
| 001 | 4 | 4 MHz |
| 010 | 8 | 2 MHz |
| 011 | 16 | 1 MHz |
| 100 | 32 | 500 KHz |
| 101 | 64 | 250 KHz |
| 110 | 128 | 125 KHz |
| 111 | 256 | 62.5 KHz |

A little experimentation is called for. Being ambitious I started with a clock frequency of 1MHz. This resulted in a flickering effect on the LED display. So, 1MHz is too ambitious, let's start to reduce the frequency. I finally settled on 250 KHz as this allowed the circuit to function as before.

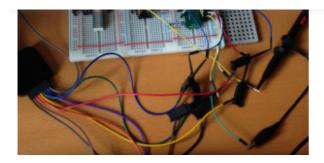
Conclusion

Using SPI master for data transmission was not as difficult as I originally thought. To make this application complete there are a few tasks to follow up on, namely:

- 1. Receiving data over SPI
- 2. Create the method to allow setting the dot correction values
- 3. Transmitting buffers from a queue
- 4. Minor tidying up of the timer control

The use of SPI here actually increased the time taken (597uS Bit-Banging c.f. 795uS for 250 KHz SPI) to reliably send the grey scale data to the TLC5940. I suspect that the time can be decreased if the circuit was taken from breadboard and put onto a PCB manufactured for the purpose. The breadboard for this circuit currently looks like this:

information. Accept & Close Read More



Bread Board And Flying Leads

As you can see, there is a lot of opportunity for interference with all those flying leads.

While the time taken might have increased, the load on the microcontroller will have decreased as the SPI method is interrupt driven. The actual transmission is off-loaded to the microcontrollers dedicated circuitry.

As usual, the source code for this project is available to download.

Tags: Electronics, LED, STM8, The Way of the Register

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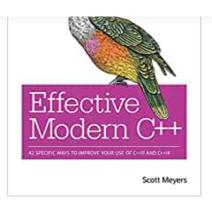
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