

Input:      CLK      →      1 bit

Reset      →      1 bit      ! default to initial state.

→ Each count. is shown

Output:

G

R

Y

$\Delta_{00}$	0001	1110	0000
$\Delta_{01}$	0010	1101	0000
$\Delta_{02}$	0100	1011	0000
$\Delta_{03}$	1000	0111	0000
$\Delta_{10}$	0000	1110	0001
$\Delta_{11}$	0000	1101	0010
$\Delta_{12}$	0000	1011	0100
$\Delta_{13}$	0000	0111	1000

Stats:

Cnt 2

Cnt 1

Active low

0 to 5

0 to 1

0 to 3

3 bits

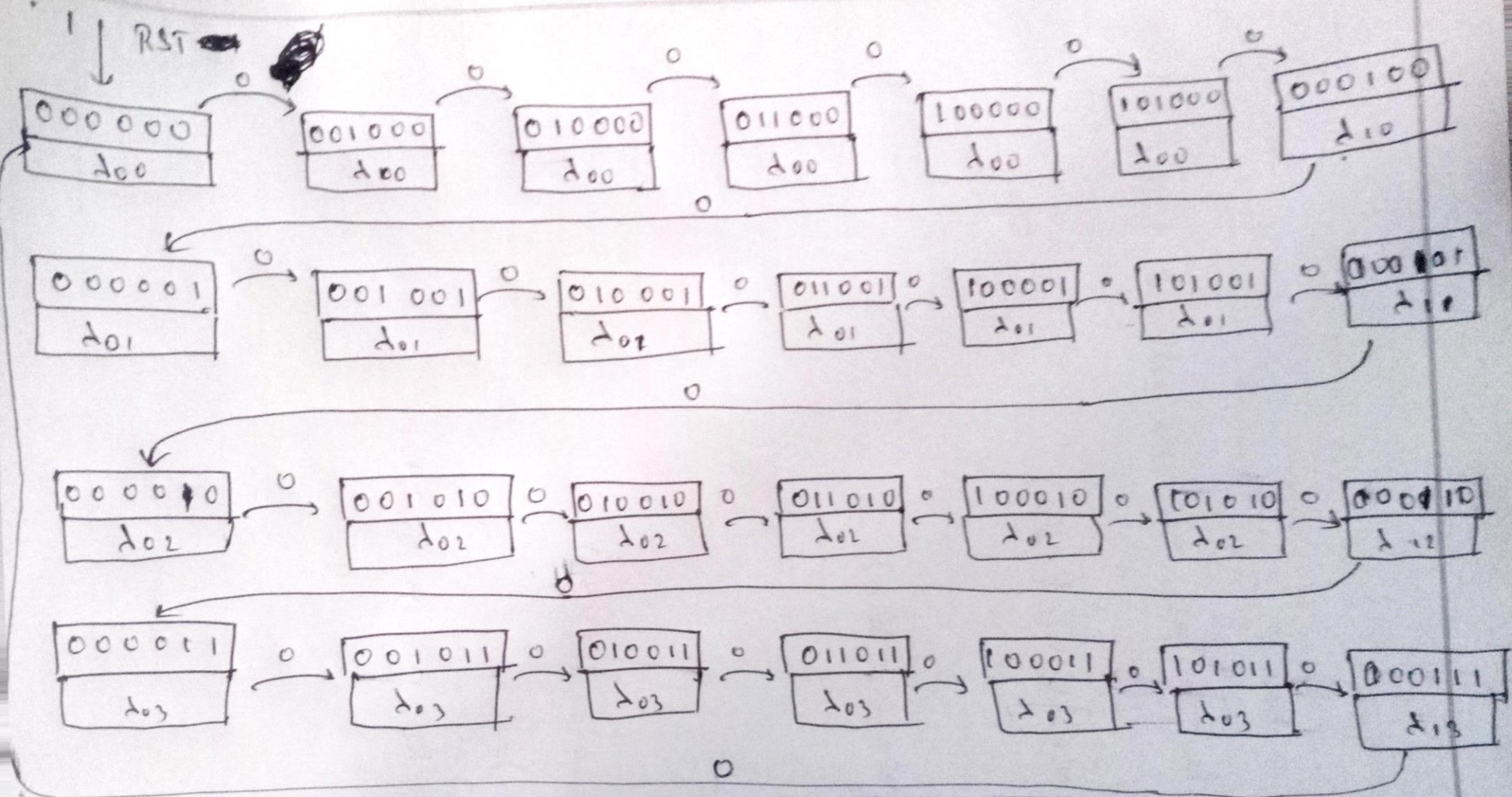
1 bit

2 bits.

3:1:2 → 7 bits.

Total states: 28.





→ Each transition at a <sup>rising edge</sup> clk cycle.

Output is shown for each transition.  
Input is RST

→ RST=1 to each state will take it to 0000000/lambda\_00

Moore Machine STG of Traffic Light.