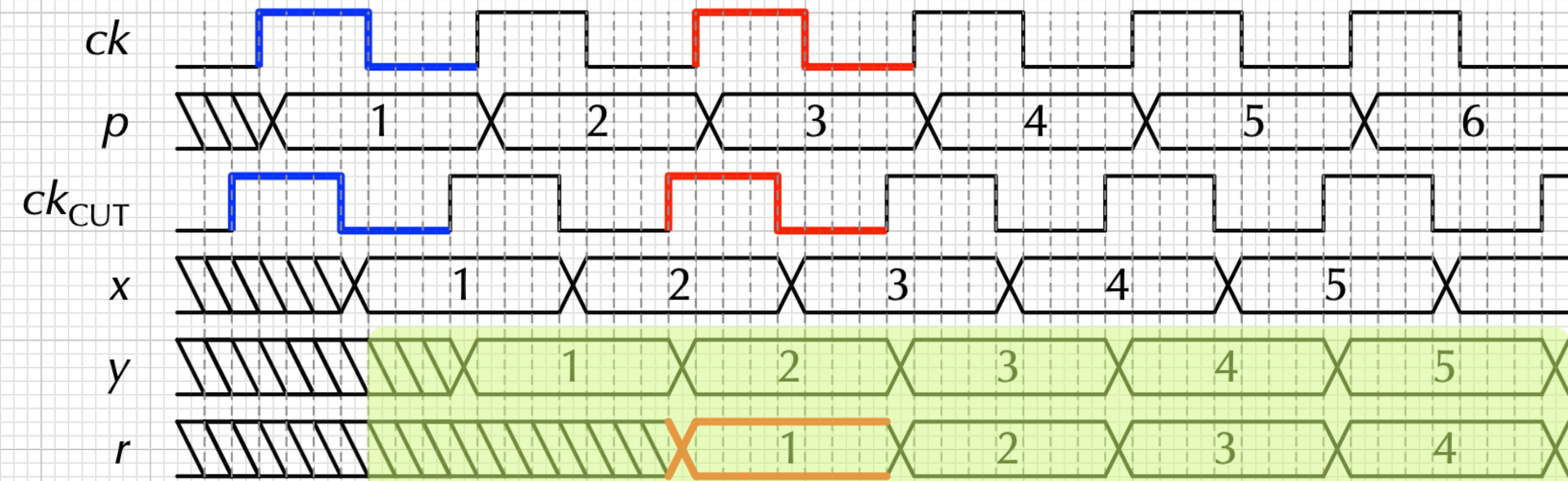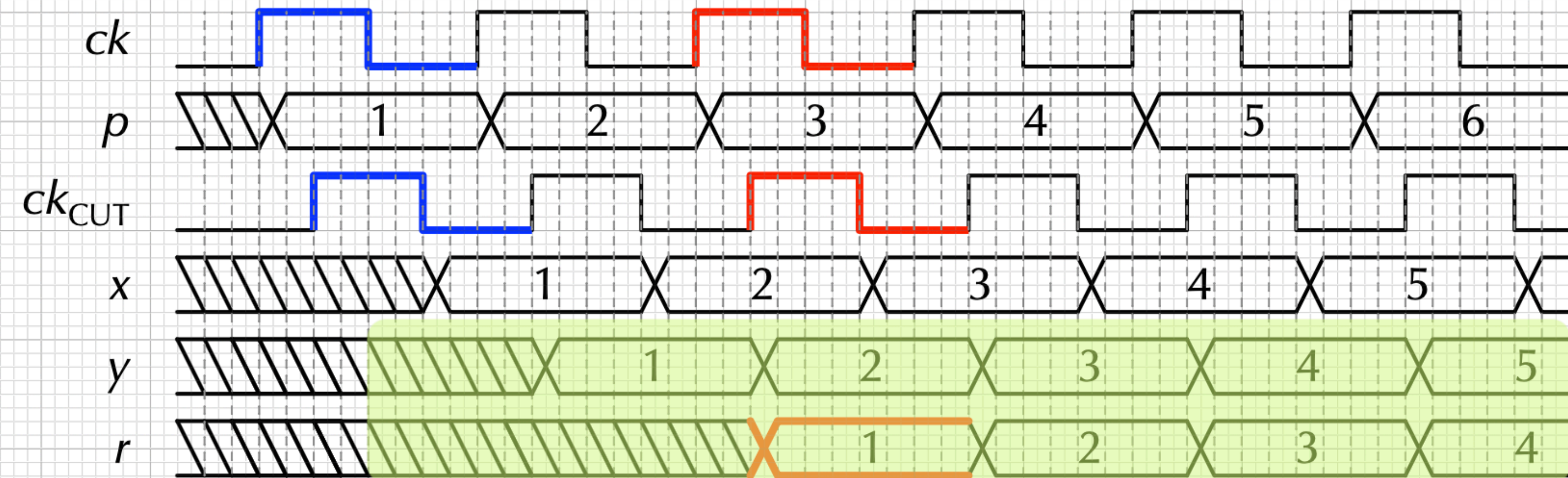# HW 01 Retiming — reference solution

- Task I

- Task II

  - Ignore the flip-flop D-to-Q delay and setup/hold-time requirements.

  - Let T be the clock period.

  - Without the re-timing circuit, $-T \leq \tau \leq 0$.

  - With the re-timing circuit, $-\dfrac{T}{2} \leq \tau \leq \dfrac{T}{2}$.