

Introduction to Electronics  
Summer 2020

ECSE-2050

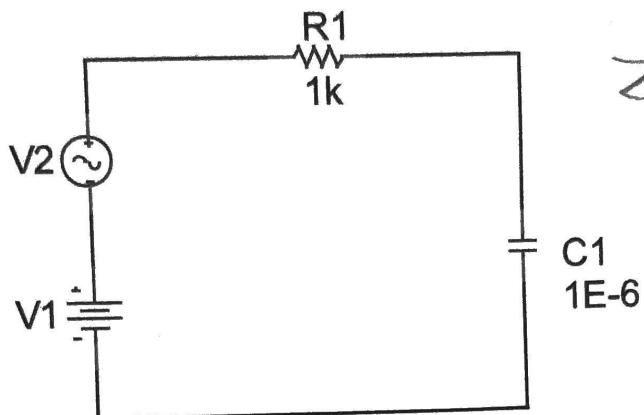
Name \_\_\_\_\_

Homework 1

Reading: Circuits Review

Submission: Use Gradescope to upload your solution

1) Superposition

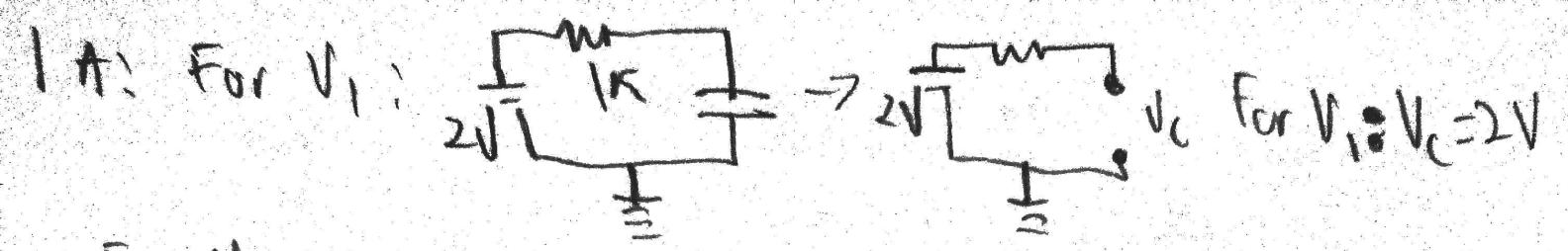


$$Z_C = 1000 \Omega; \rightarrow \omega = 1000 \text{ rad/s}$$

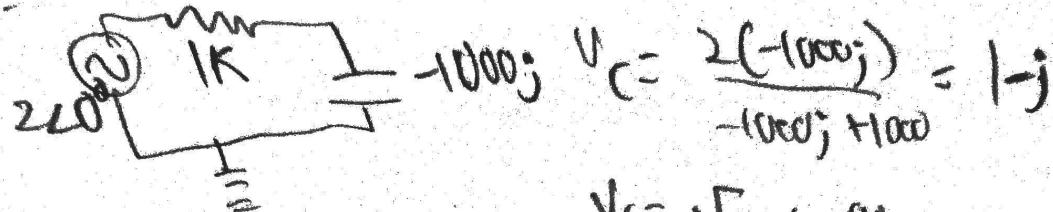
$$-10 \text{ V} \rightarrow \omega = 10^5 \text{ rad/s}$$

In the above circuit, V1 is a DC source and V2 is a sinusoidal source, with voltages  $V_1 = 2\text{V}$  (assume the capacitor is at DC steady state for V1, ie. not a step function input)  $V_2 = 2\sin(\omega t) \text{ V}$  - Assume AC steady state as well

- a) For  $\omega=1000 \text{ [rad/s]}$ , use superposition to determine the time domain expression for the voltage across the capacitor,  $V_c$ .
  - a. For several cycles of the sinusoidal signal, plot the voltage across the capacitor.
  - b. What is the average voltage of the capacitor signal?
  - c. What is the peak-to-peak voltage of the capacitor signal?
- b) For  $\omega=10^5 \text{ [rad/s]}$ , use superposition to determine the time domain expression for the voltage across the capacitor,  $V_c$ .
  - a. For several cycles of the sinusoidal signal, plot the voltage across the capacitor.
  - b. What is the average voltage of the capacitor signal?
  - c. What is the peak-to-peak voltage of the capacitor signal?
  - d. For this frequency, how would you compare the peak-to-peak voltage to the average voltage?



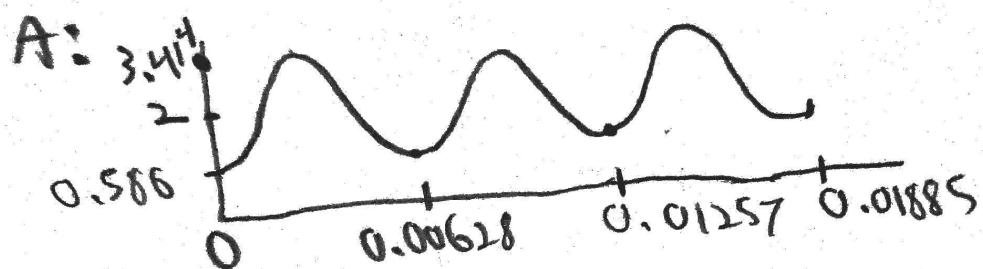
For  $V_2$ :



$$V_c = \sqrt{2} \angle -90^\circ$$

$$V_c = \sqrt{2} \sin(-90^\circ)$$

$$V_{1c} + V_{2c} = \sqrt{2} \sin(\omega t + -90^\circ) + 2 = V_c(t)$$



B: sine wave centered at 2V: avg =  $2\sqrt{2}$

C: peak to peak = 2.828 V

1B: For  $V_1 = V_C = 2V$  for  $V_2 = 2 \times \left( -0j \right) = .002 - j.02$   
 $\frac{1000 + -10j}{1000 + -10j}$

$$V_C(t) = .02 \sin(\omega t + 90^\circ) + 2$$

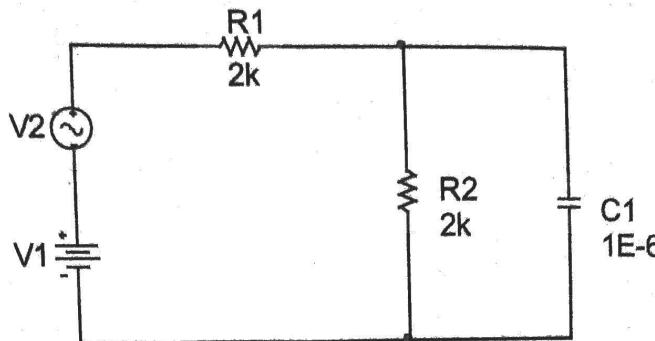
$$V_C = .02 \sin(\omega t + 90^\circ) + 2$$

B: The average is 2V

C: The peak to peak is .04V

D: The peak to peak is much smaller than the average.

2) An important, familiar concept.



In the above circuit,  $V_1$  is a DC source and  $V_2$  is a sinusoidal source, with voltages  
 $V_1 = 4V$  (assume the capacitor is at DC steady state for  $V_1$ , ie. not a step function input)  
 $V_2 = 4\sin(\omega t) V$

a) For  $\omega=1000$  [rad/s], determine the time domain expression for the voltage across the capacitor,  $V_c$ .

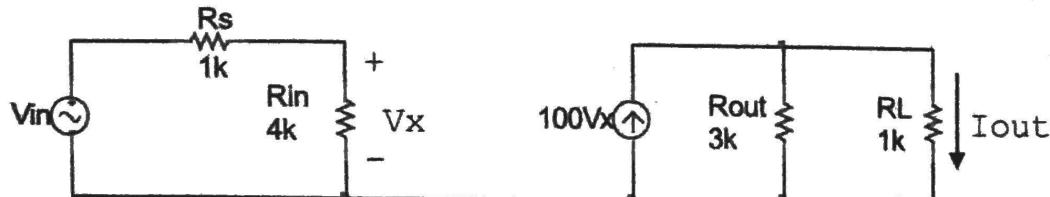
$$\text{For } V_1: V_c = \frac{4(2k)}{4k} = 2V \quad \text{For } V_2: Z_{R2C_1} = \left(\frac{1}{400}; \frac{1}{3000}\right)^{-1} = 400-800j$$

$$V_c = \frac{4(400-800j)}{2000+400-800j} = 1-j \rightarrow \sqrt{2} \sin(\omega t + -90^\circ)$$

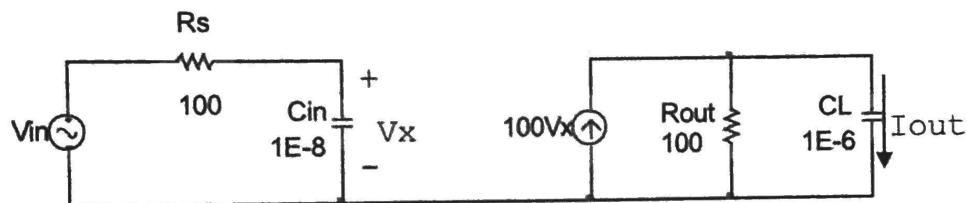
$$\therefore V_c(t) = \sqrt{2} \sin(\omega t + -90^\circ) + 2V$$

Revised: 5/26/2020  
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3) Dependent sources



- For the above circuit, symbolically determine the transfer function,  $H(s) = I_{out}(s)/Vin(s)$ .
- Symbolically, determine the Thevenin voltage,  $V_{Th}$ , for the indicated load.
- Symbolically, determine the Norton current,  $I_N$ , for the indicated load.
- Symbolically, using test voltage concepts, determine the Thevenin resistance,  $R_{Th}$ , for the indicated load.



- For the above circuit, determine the transfer function,  $H(s) = I_{out}(s)/Vin(s)$ .
- Plot the Bode magnitude plot for the transfer function using the log-log plot (available on the last page). On the plot, identify the poles

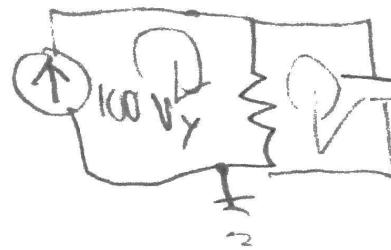
3A:  $V_x = \frac{Vin(R_{in})}{R_{in} + R_s}$     $I_{out} = \frac{100V_x(R_{out})}{R_L + R_{out}} = \frac{100 Vin(R_{in})}{R_{in} + R_s} \cdot \frac{(R_{out})}{R_L + R_{out}}$

$$\frac{I_{out}}{Vin} = \frac{100 (R_{in})(R_{out})}{(R_{in} + R_s)(R_L + R_{out})} = H(s)$$

B: Take off  $RL$     $V_{Th} = 100V_x \cdot R_{out} = \frac{100 Vin(R_{in})(R_{out})}{R_{in} + R_s} = V_{Th}$

C: Short across  $RL$     $I_N = 100V_x = \frac{100 Vin(R_{in})}{R_{in} + R_s} = I_N$

D:



$$V_{test} = 1V \quad i_1 = 100V_x \quad i_2 = I_{load}$$

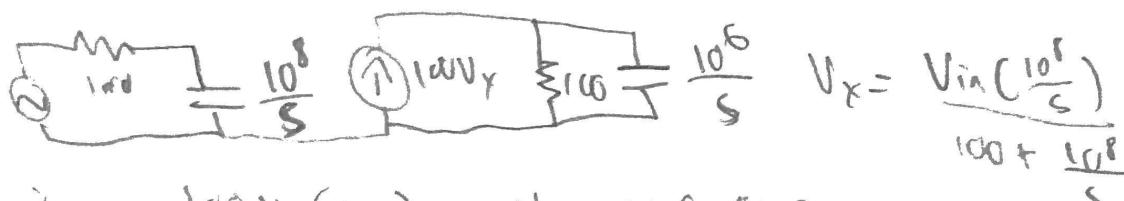
$$0 = R_{load} i_2 + V_{test} - 100V_x$$

$$i_2 = \frac{100V_x - V_{test}}{R_{load}}$$

$$\frac{V_{test}}{i_2} = R_{TH} \quad \frac{V_{test} \cdot R_{load}}{100V_x - V_{test}} = R_{TH}$$

$$R_{TH} = \frac{V_{test} \cdot R_{load}}{\frac{100V_{in}}{R_{in} + R_S} - V_{test}} = \frac{R_{in} \cdot V_{test} \cdot R_{load} \cdot R_S}{100V_{in} - R_{in} \cdot V_{test} \cdot R_S}$$

E:



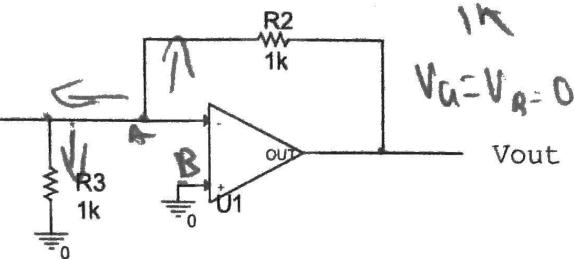
$$V_x = \frac{V_{in} \left( \frac{10^8}{s} \right)}{100 + \frac{10^8}{s}}$$

$$i_{out} = \frac{100V_x(100)}{100 + \frac{10^8}{s}} = \frac{V_{in} \cdot 100 \left( \frac{10^8}{s} \right) (100)}{100 + \frac{10^8}{s}}$$

$$H(s) \frac{i_{out}(s)}{V_{in}(s)} = \frac{\left( 100 \left( \frac{10^8}{s} \right) (100) \right)}{100 + \frac{10^8}{s}} \cdot \frac{1}{100 + \frac{10^8}{s}} = \frac{\frac{10^8 s}{(s+10^4)(s+10^4)}}{(s+10^4)(s+10^4)}$$

**4) Amplifier Circuits**

$$\frac{V_{out}}{V_{in}} = \frac{-1}{1k} = -1 = A$$



$$\frac{V_A - V_{in}}{1k} + \frac{V_A}{1k} + \frac{V_A - V_{out}}{1k} = 0$$

$$V_A = V_A = 0$$

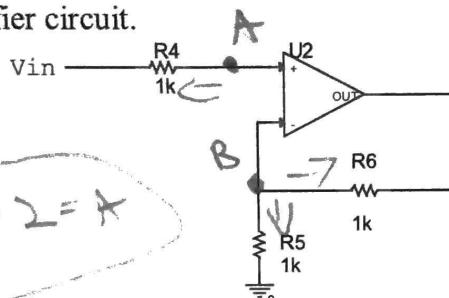
$$-\frac{V_{in}}{1k} = \frac{V_{out}}{1k}$$

a) Using op-amp 'rules' and circuit analysis, determine the gain,  $A = V_{out}/V_{in}$ , of the above amplifier circuit.

$$\frac{V_{in} - V_{out}}{1k} + \frac{V_{in}}{1k} = 0$$

$$\frac{2V_{in}}{1k} = \frac{V_{out}}{1k}$$

$$\frac{V_{out}}{V_{in}} = 2 = A$$



$$\frac{V_A - V_{in}}{1k} = 0 \quad V_A = V_{in}$$

$$V_A = V_B$$

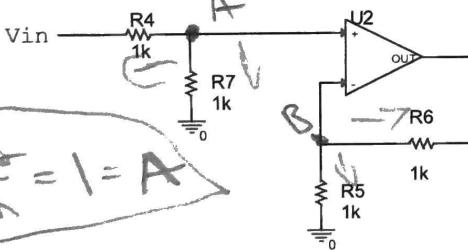
$$\frac{V_B - V_{out}}{1k} + \frac{V_B}{1k} = 0$$

b) Using op-amp 'rules' and circuit analysis, determine the gain,  $A = V_{out}/V_{in}$ , of the above amplifier circuit.

$$\frac{V_{in} - V_{out}}{2k} - \frac{V_{in}}{2k} = 0$$

$$\frac{2V_{in}}{2k} = \frac{V_{out}}{2k}$$

$$\frac{V_{out}}{V_{in}} = 1 = A$$



$$\frac{V_A - V_{in}}{1k} + \frac{V_A}{1k} = 0 \quad \frac{V_A - V_{in}}{1k} + \frac{V_A}{1k} + \frac{V_A}{1k} = 0$$

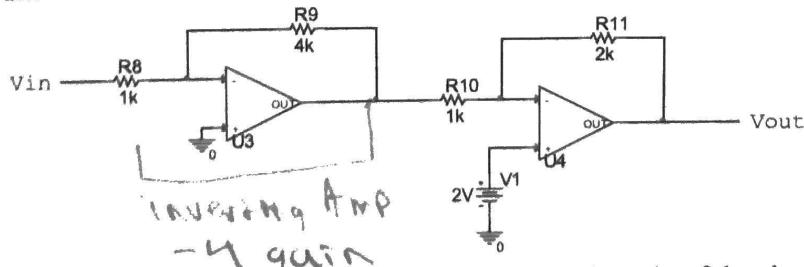
$$\frac{V_B - V_{out}}{1k} + \frac{V_B}{1k} = 0$$

$$\frac{2V_B}{1k} = \frac{V_{out}}{1k}$$

$$V_A = \frac{1}{2}V_{in}$$

$$V_A = V_B$$

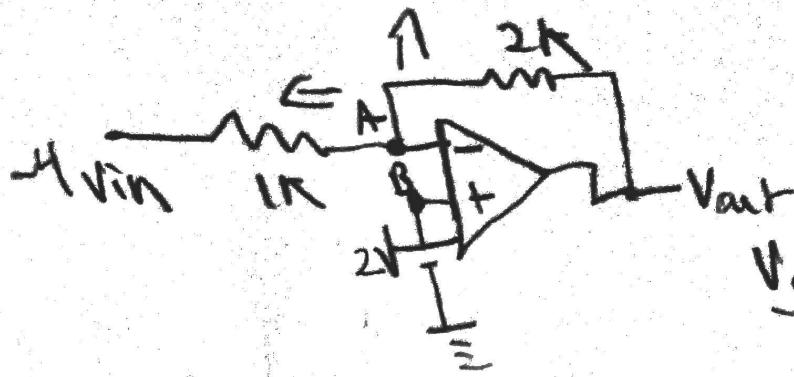
c) Using op-amp 'rules' and circuit analysis, determine the gain of the above amplifier circuit.



INVERTING AMP  
-1 gain

d) Using op-amp 'rules' and circuit analysis, determine the gain of the above amplifier circuit.

40:



$$\frac{V_B - (-4V_{in})}{1k} + \frac{V_B - V_{out}}{2k} =$$

$$VB = 2V \quad V_B = V_A$$

$$\frac{2V}{1k} + \frac{4V_{in}}{1k} + \frac{2V - V_{out}}{2k} = 0$$

$$\frac{6V}{2k} + \frac{4V_{in}}{1k} = \frac{V_{out}}{2k}$$

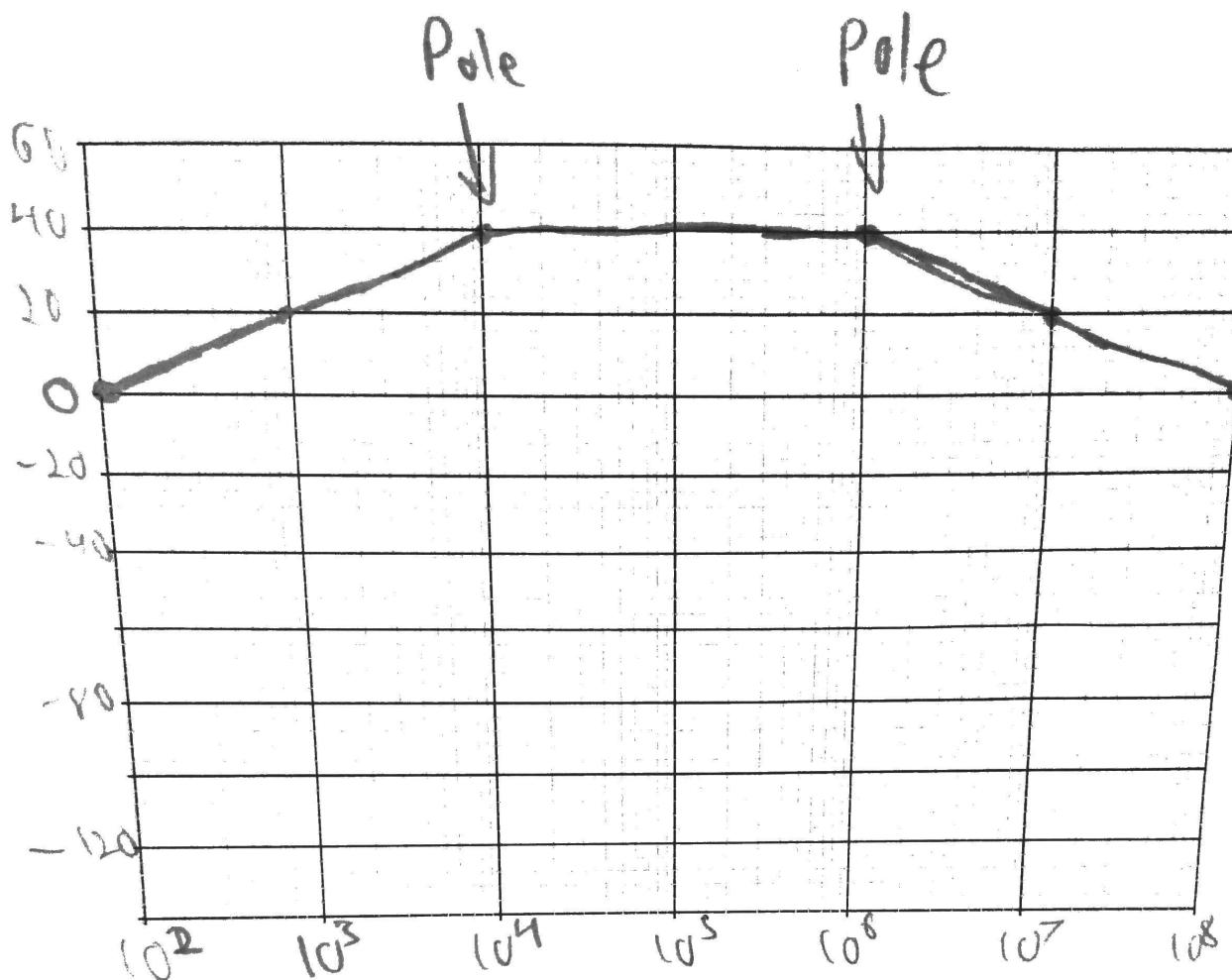
$$\frac{2V + 4V_{in}}{1k} + \frac{2V - V_{out}}{2k} = 0$$

$$V_{out} = 6V + 8V_{in}$$

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$$20 \log(10^{-2}) = -40 \text{ constant}$$

$$0 = 20 \text{ dB/dec rise}$$

$$10^4 = 0 \text{ dB} \rightarrow -20 \text{ dB decade}$$

$$10^6 = 0 \text{ dB} \rightarrow -20 \text{ dB decade}$$

Poles at  $10^6$  and  $10^4$