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10/19/19
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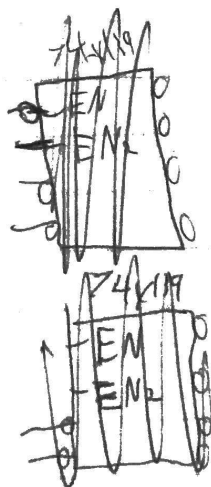
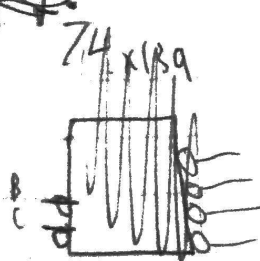
CoCo HW #6

6.24:

~~This circuit will detect if there is more than 4 HIGH input and output 0 if it is a~~

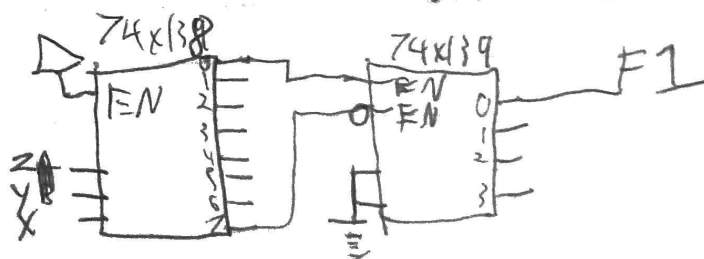
The circuit will detect an odd number of 1's in the input and output 0 if there is an even number of 1's.

6.43:

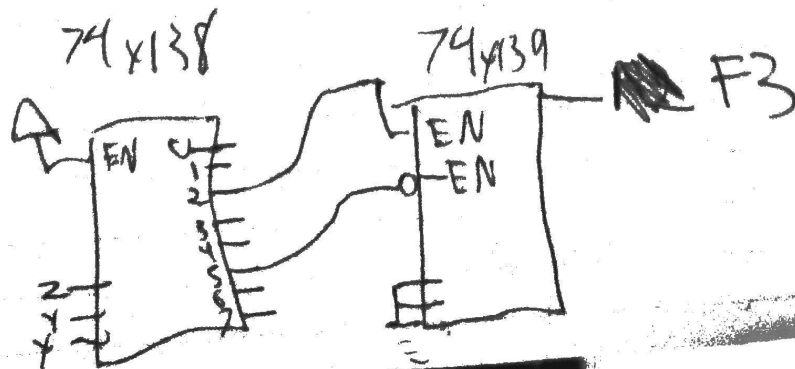


F1:

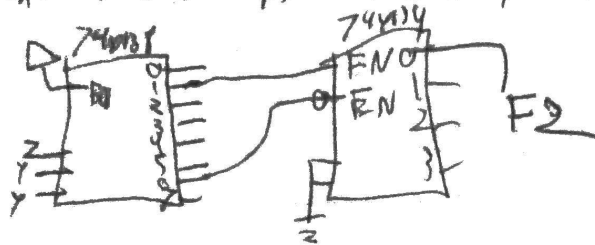
74x138: Active HIGH - HIGH
74x139: Active HIGH - HIGH



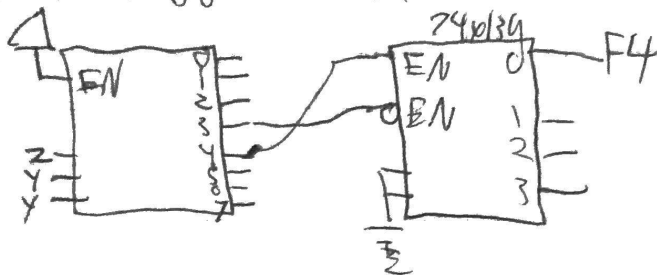
F3: 74x139 Active HIGH - HIGH
74x138 Active HIGH - HIGH



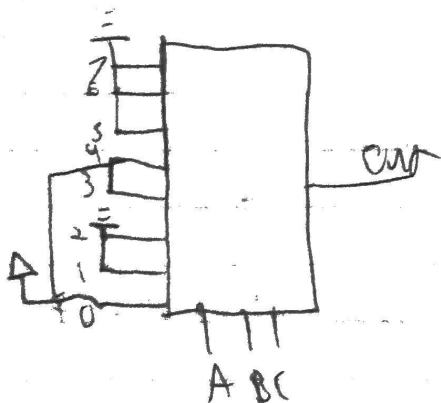
F2: Both chips Active High-High



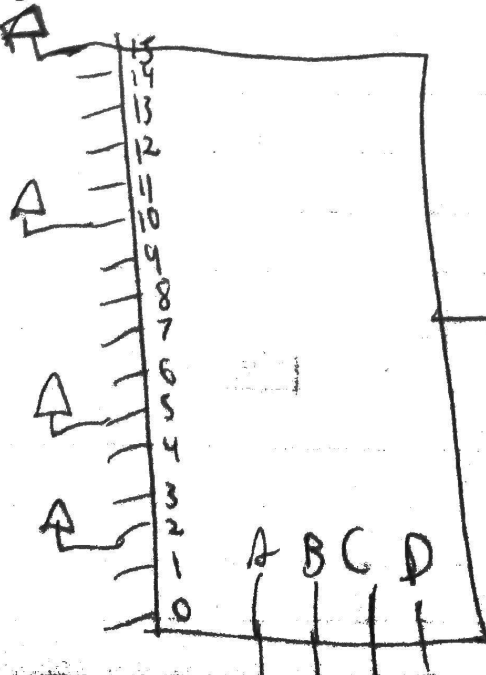
F4: 74x138 Active H-H



3 I:

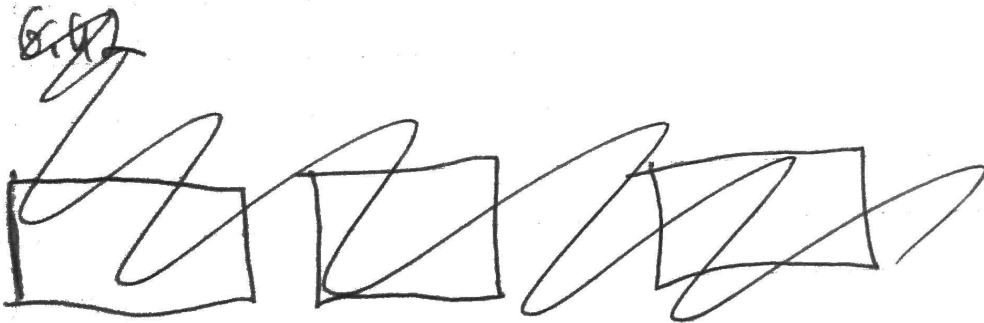


3 II:



6.81:

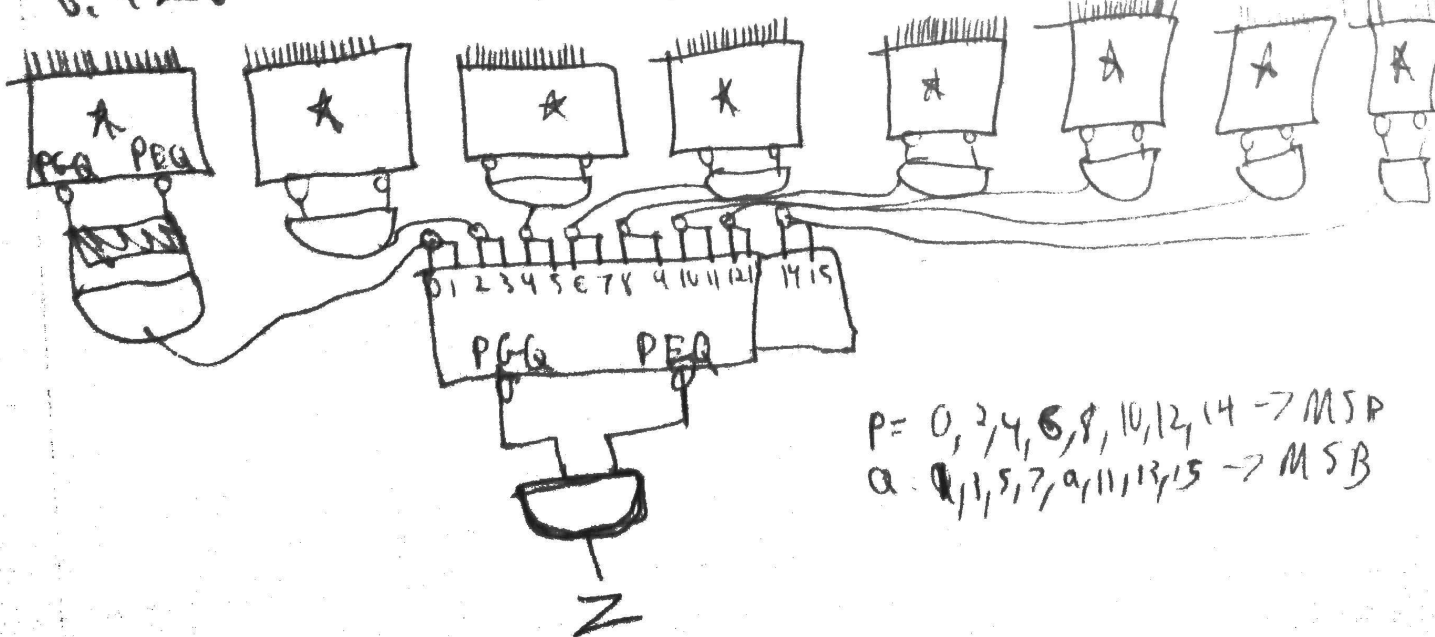
A	B	C	Z
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	0



6.92:

All Inputs

MSB \uparrow



P = 0, 2, 4, 6, 8, 10, 12, 14 \rightarrow MSB
 Q = 1, 3, 5, 7, 9, 11, 13, 15 \rightarrow MSB

Q. A: IV because it is the smallest number so there will be less dependency on carry calculations

P: III because it is the largest and therefore will have the largest dependency on carry values.

HW #6 Resubmission!

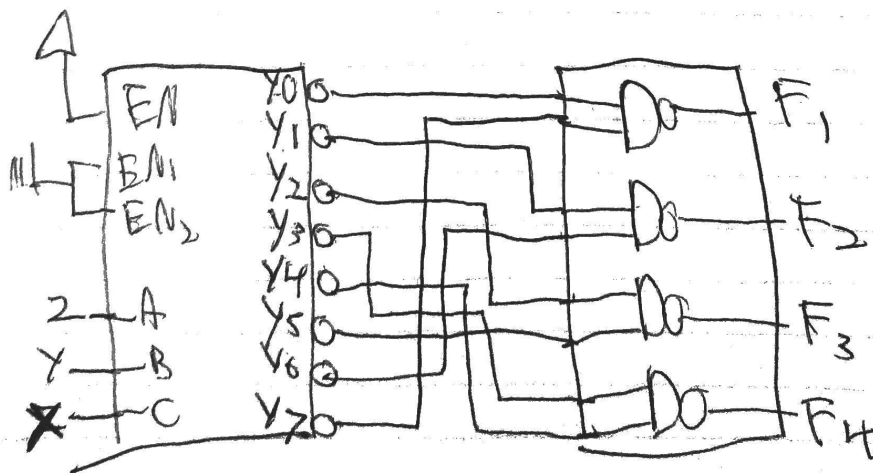
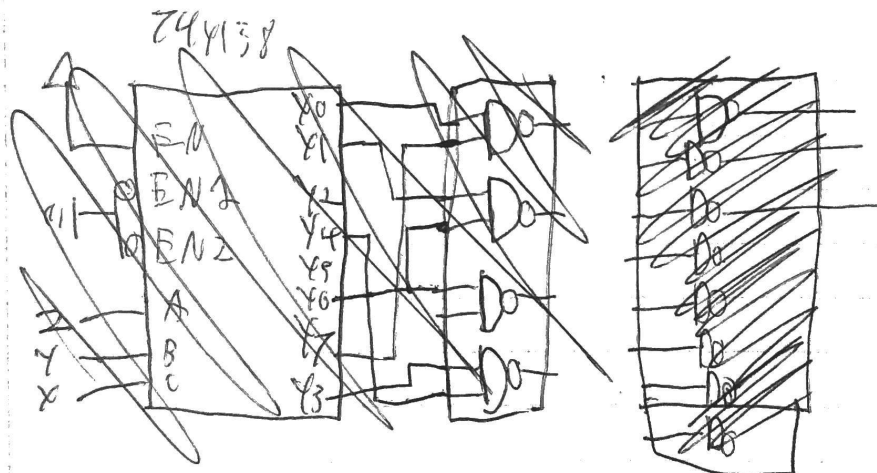
2. It detects the parity of the number. Of whether it is even or not. This circuit can tell if the input is even.

$$F_1 = X'Y'Z' + XYZ$$

$$F_3 = X'YZ' + XY'Z$$

$$F_2 = X'Y'Z + XYZ'$$

$$F_4 = X'YZ + XY'Z'$$



4.

A	B	C	Z
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	1

~~A~~B

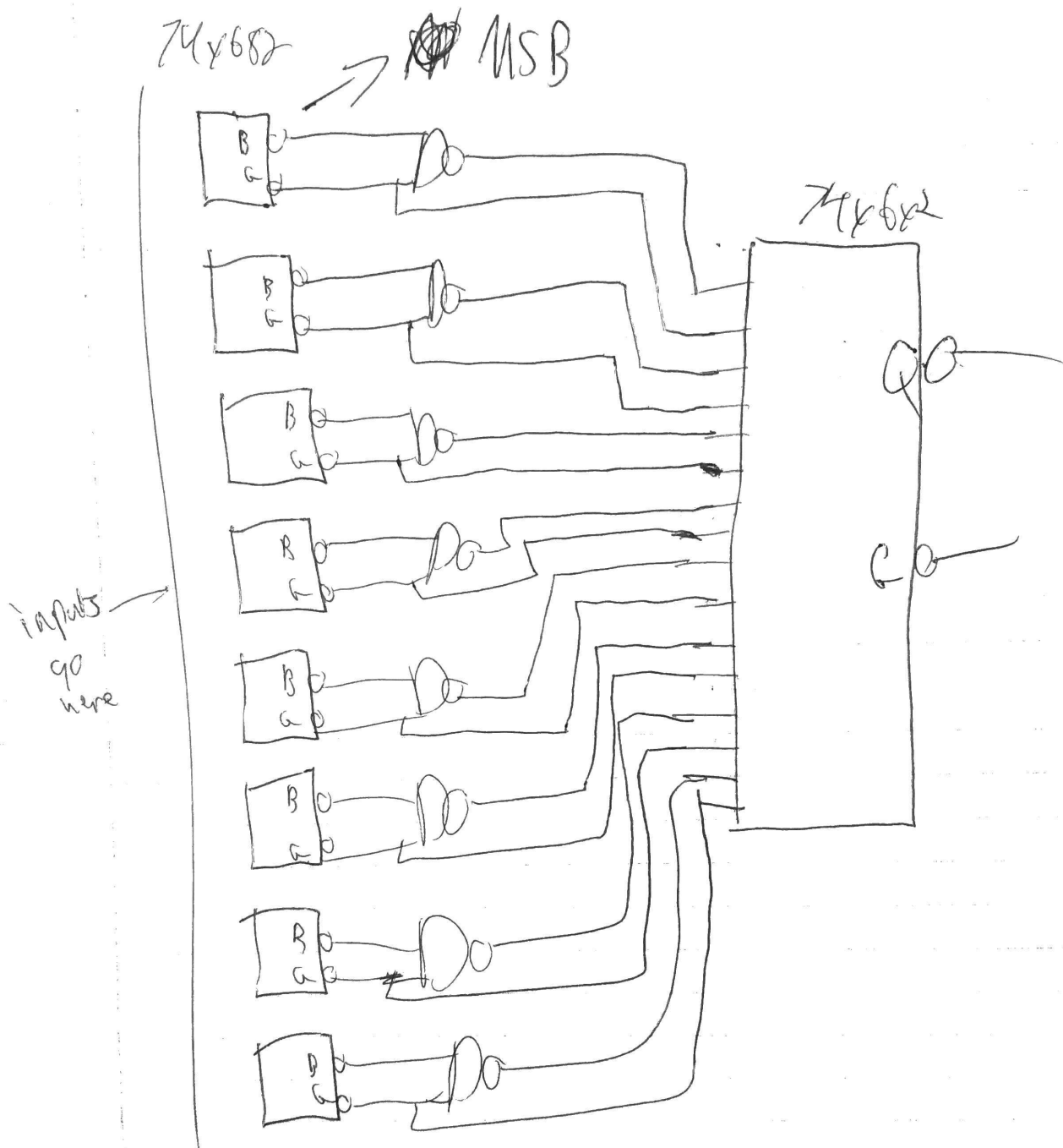
$$\overline{A}\overline{B}C + \overline{A}B\overline{C} + A\overline{B}\overline{C} + ABC = Z$$

* There isn't much to say here. I just analyzed the circuit incorrectly. I believe I thought the input was always tied to A because the other paths always seemed closed off.

6. (a) iii. This value has minimal carry operations unlike what I thought. Therefore it will run the fastest

(b) II. This value has the most carry outs so it will take longer.

5.



* I was almost to this solution. I thought that the outputs should feed into an AND instead of NAND. I also fed the inputs together on the layer 2 chip ~~which~~ which didn't make sense. I see what I did need to do. Comparing the G output would immediately show which one is greater.