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Activity 2:

1: LOAD <N2>
   STORE <TEMP>   → Prof Hameed said
   LOAD < N1>   to keep it this way
   STORE < N2>
   LOAD < N3>
   STORE < N1>
   LOAD < TEMP>
   STORE < N3>

2. PC → MAR
   Memory Read:
     MBR → IR:

IF IR <op code> == STORE_TO_MEMORY
THEN

IR <addr> → MAR
Memory Read:

MBR → AC:

PCH → PC