Saaif Ahmed

Clock

DGND

15 A3
13 A2 **85**
12 A1
10 A0

A>B 5
A=B 6
A<B 7

1 B3
14 B2
11 B1
9 B0

4 A>Bin
3 A=Bin
2 A<Bin

DGND

0   1

191

14 CLK

4 CTEN
5 D/U̅   MXMN 12
11 LD   RCO 13

9 D   QD 7
10 C   QC 6
1 B   QB 2
15 A   QA 3

DGND

DGND

3

Floor_msb
Floor_lsb

04

13   12
11   10
9   8
5   6
3   4
1   2

DGND

ns	84000	84200	84400	84600	84800

Floor_msb
Floor_lsb
Clock

Only attempted required sections

7 segment Display: Wired as per normal
Switches: Top switch = MSB
          Bottom switch = LSB
          Two switches were used to represent Floor: 0,1,2,3

LED: The glowing LED represented the "path the elevator was taking. In this case the Yellow meant the elevator was traveling up

PROTOBOARD #113

H1 S4

R4

R3

S3
S2

R2

R1

LO S1

H1
1
2
3
4
5
6

U1
SN74LS14N
FFMC9209

NIS-3U70-4A

C1

16a

C1 U1 H1

R1

C2

U2

SN74LS47N
FBM9750

3078-1

DL1

NIS-3078-2C
COMPONENT SIDE

R1

C3

C4

U1

S1

H1 1

10HZ