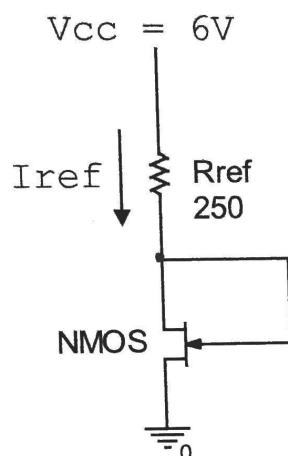


Homework 11

Reading: 7.1, 7.2-5 (FET discussion)

Problem 1) Current mirrors

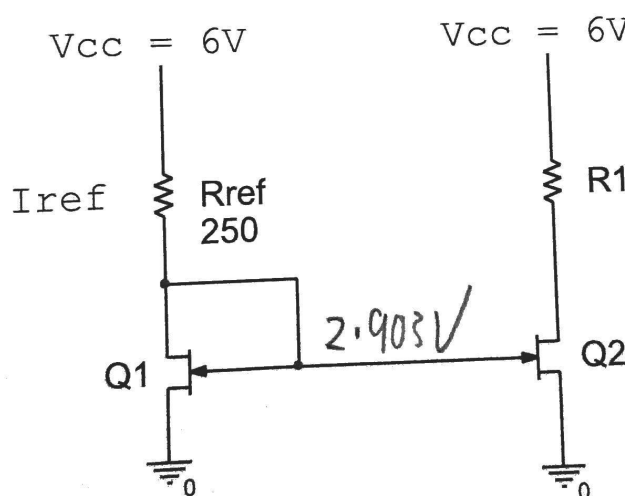


$$V_{cc} = V_R + V_{DS}$$

$$I = \frac{1}{2} k_n (V_{GS} - V_{TN})^2 R + V_{GS}$$

$$V_{GS} = 2.9 \text{ or } 1.3$$

$$\text{Thus } I_{ref} = 12.4 \text{ mA}$$

For the above circuit, $V_{TN} = 2.2\text{V}$ and $K_n = 50\text{mA/V}^2$.a) Determine the reference current through R_{ref} .

$$V_{DS} > V_{GS} - V_{TN}$$

$$V_{cc} - V_R > V_{GS} - V_{TN}$$

$$6 - V_R > 0.7$$

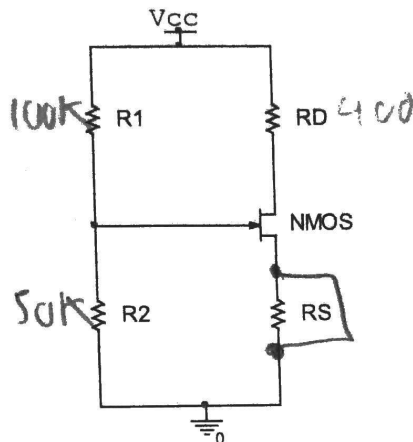
$$5.3 > R \cdot I_{ref}$$

$$427.42 > R$$

b) Both FETs are identical and have the same specifications as above. Determine the range of R_1 such that the current through R_1 is the mirror of I_{ref} ($I_{R1} = I_{ref}$).

$$R_1 < 427.42 \text{ } \Omega$$

Problem 2) Circuit sensitivity



The NMOSFET in the above circuit has characteristics, $V_{TN} = 2.2\text{V}$ and $K_n = 40\text{mA/V}^2$

- For $V_{CC} = 9\text{V}$, $R_1 = 100\text{k}$, $R_2 = 50\text{k}$, $R_D = 400\Omega$ and $R_S = 0\Omega$ (a short circuit), determine the DC bias characteristics, I_D , V_{GS} and V_{DS} .
- If the transistor threshold voltage changed to 2.1V , determine the same DC bias characteristics.
- For a change of $\sim 5\%$ of the threshold voltage, what percentage change did you observe in the drain current, I_D ?

A: Assume sat $V_{GS} = \frac{9V(50k)}{100k+50k} = 3V$ $I_D = \frac{1}{2}(0.04)(3-2.2)^2 = 12.8\mu\text{A}$

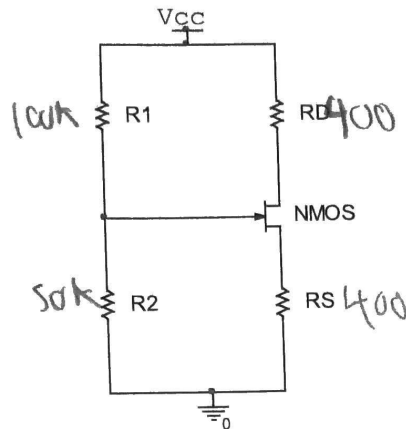
$V_{CC} = V_{GS} + V_{DS}$ $9 = (3) + V_{DS}$ $V_{DS} = 6V > 3-2.2$

saturation is checked $V_{GS} = 3V$ $V_{DS} = 6V$ $I_D = 12.8\mu\text{A}$

B: $I_D = \frac{1}{2}(0.04)(3-2.1)^2 = 0.0162$ $9 = (0.0162)(400) + V_{DS}$
saturation is checked $V_{GS} = 3V$ $I_D = 16.2\mu\text{A}$ $V_{DS} = 2.52 > 3-2.1$

C: $\frac{16.2 - 12.8}{12.8} = 0.26 \times 100 = 26.6\%$

Problem 3) Four resistor biasing and circuit sensitivity



The NMOSFET in the above circuit has characteristics, $V_{TN} = 2.2\text{V}$ and $K_n = 40\text{mA/V}^2$

- For $V_{CC} = 9\text{V}$, $R_1 = 100\text{k}$, $R_2 = 50\text{k}$, $R_D = 400\Omega$ and $R_S = 400\Omega$, determine the DC bias characteristics, I_D , V_{GS} and V_{DS} .
- If the transistor threshold voltage changed to 2.1V , determine the same DC bias characteristics.
- For a change of $\sim 5\%$ of the threshold voltage, what percentage change did you observe in the drain current, I_D ?
- For the original FET parameters and part a component values (except R_D), determine the range of R_D such that the FET is in saturation.
- For the original FET parameters and part a component values (except R_2), determine the range of R_2 such that the FET is in saturation.

7: $V_g = \frac{9(50\text{k})}{150\text{k}} = 3\text{V}$ assume saturation $3\text{V} = V_{GS} + \frac{1}{2}(0.04)(V_{GS} - 2.2)^2 \cdot 400$
 $V_{GS} = 2.46$ or $I_D = 1.35\text{mA}$
 $9 - (1.35 \times 10^{-3})400 - (1.35 \times 10^{-3})400 = V_{DS} = 7.918$ Saturation is valid.
 $V_{GS} = 2.46\text{V}$, $I_D = 1.35\text{mA}$, $V_{DS} = 7.92\text{V}$

8: $3\text{V} = V_{GS} + \frac{1}{2}(0.04)(V_{GS} - 2.1)^2 \cdot 400$ $V_{GS} = 2.38$ $I_D = 1.57\text{mA}$
 $9 - (400 \cdot 1.57\text{mA}) - (400 \cdot 1.57\text{mA}) = V_{DS} = 7.75\text{V}$ sat is valid
 $V_{GS} = 2.38\text{V}$, $I_D = 1.57\text{mA}$, $V_{DS} = 7.75\text{V}$ next page \rightarrow

$$C: \frac{1.57 \text{ mA} - 1.35 \text{ mA}}{1.35 \text{ mA}} = .16 \times 100 = 16.30\%$$

$$P: V_{CC} - V_{AD} > 2.46 - 2.2$$

$$9 - 2.6 - (1.35 \text{ mA}) 400 > V_{AD}$$

$$8.2 > V_{AD}$$

$$6074 > R_D$$

$$R_D < 6074$$

$$E: V_{CC} - V_{AD} - V_{AS} > V_{GS} - V_{TN}$$

$$V_{CC} - I_{DQ} R_D - I_{DQ} R_S > V_{GS} - 2.2$$

$$11.2 - 400 I_{DQ} - R_S > V_{GS} - V_{AS}$$

$$11.2 - 400 I_{DQ} > V_{GS}$$

$$11.2 - 400 I_{DQ} > V_{GS}$$

$$V_{GS} - V_{AS} > 2.2$$

$$V_{GS} - 400 I_{DQ} > 2.2$$

Transistor must
be on.

$$R_S > 33,000 \Omega$$

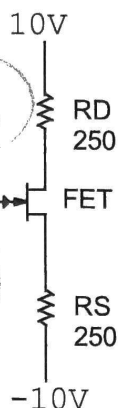
Problem 4) More biasing

$$V_{DS} = 20 - 2V_{DS} =$$

$$20 - 500 I_D = 6.7 = V_{DS}$$

$$V_{GS} = 3.35V$$

$$I_D = 26.6mA$$



$$V_{GS} + V_{DS} - 10 = 0$$

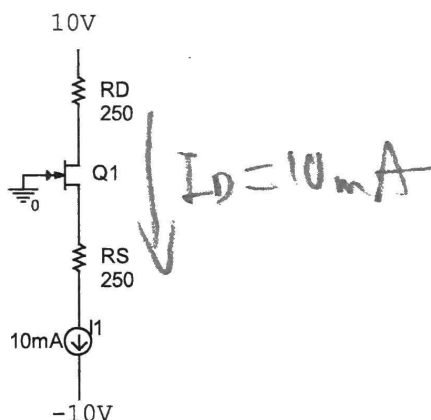
$$V_{GS} + 2(0.5(40mA/V^2)(V_{GS} - 2.2V)^2) = 10$$

$$V_{GS} = 3.35 \text{ or } 2.8$$

$$\text{thus } I_D = 26.59mA$$

The NMOSFET in the above circuit has characteristics, $V_{TN} = 2.2V$, and $K_n = 40mA/V^2$

a) Determine the bias characteristics, V_{GS} , V_{DS} , and I_D , of the above FET circuit.



The NMOSFET in the above circuit has characteristics, $V_{TN} = 2.2V$, and $K_n = 40mA/V^2$

b) Determine the bias characteristics, V_{GS} , V_{DS} , and I_D , of the above FET circuit.

$$V_{GS} = 10 - 250 \cdot 0.01 = 7.5V = V_{GS}$$

$$V_{DS} = 20 - 500 I_D = 15V = V_{DS}$$

$$10mA = I_D$$