Saaif Ahmed
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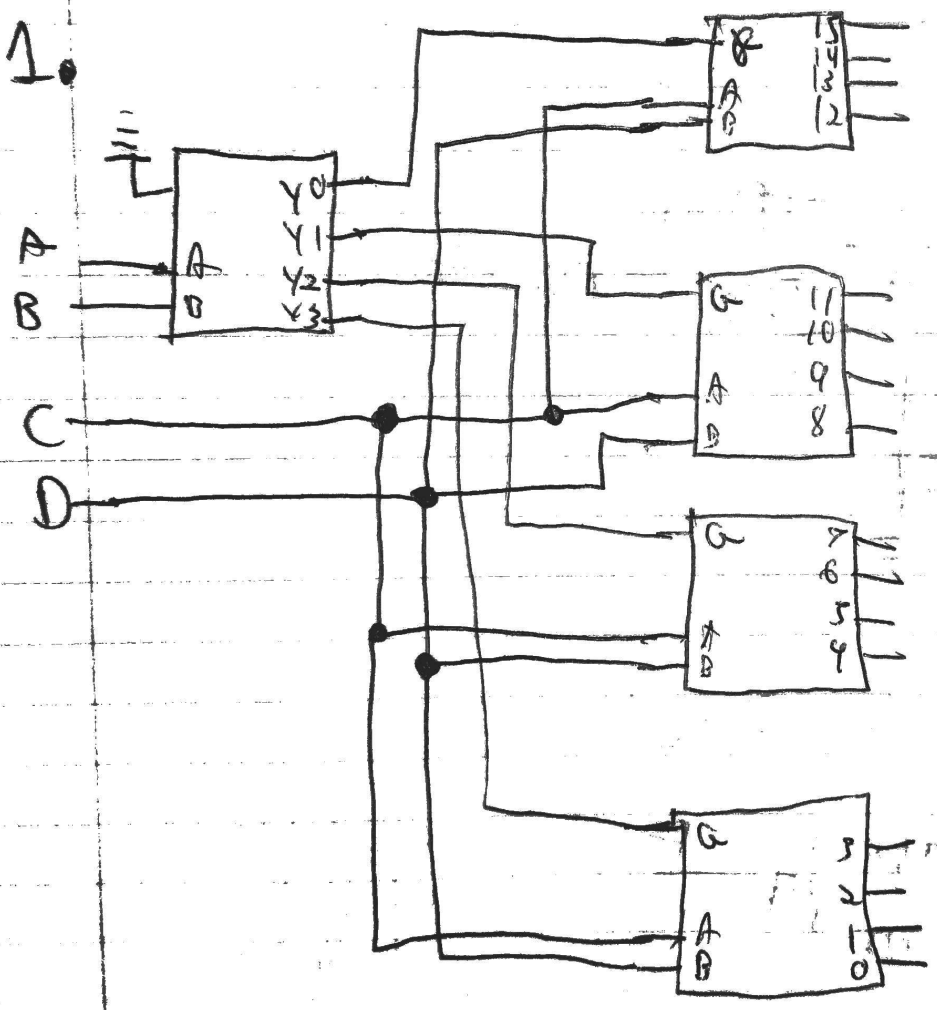
9/30/19
661925948

CoCo HW#4

A 5

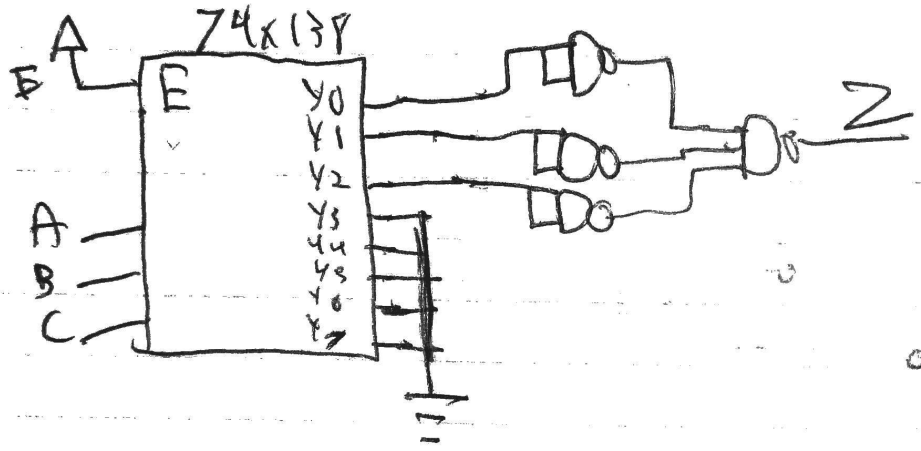
1.



6.20 B:

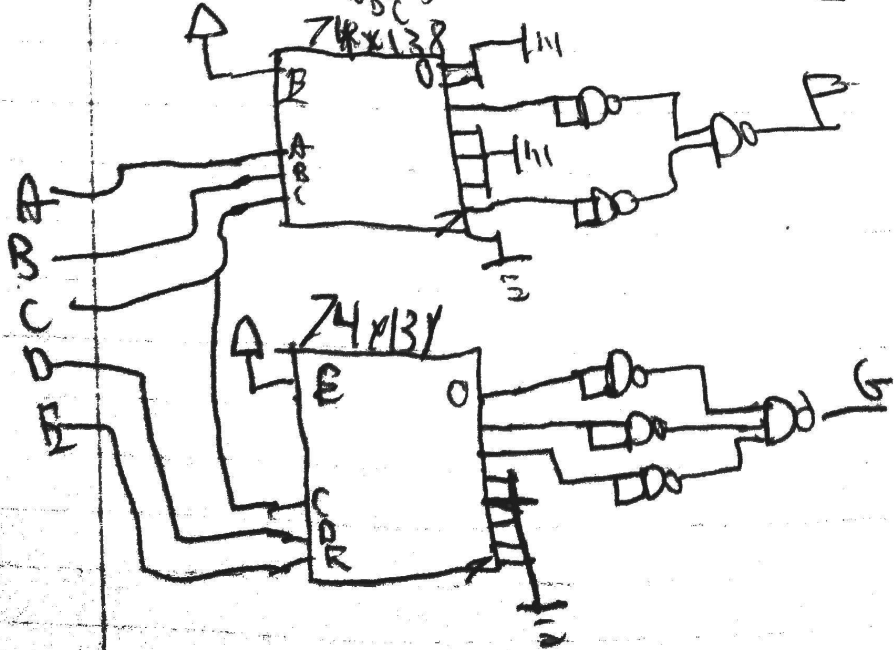
$F = \Pi_{ABC}(3,4,5,6,7) = \Sigma_{ABC}(0,1,2)$

C	B	A	Z
0	0	0	1
0	0	1	1
0	1	0	1



F:

$F = \Sigma_{ABC}(2,6) \quad G = \Sigma_{CDE}(0,2,3)$

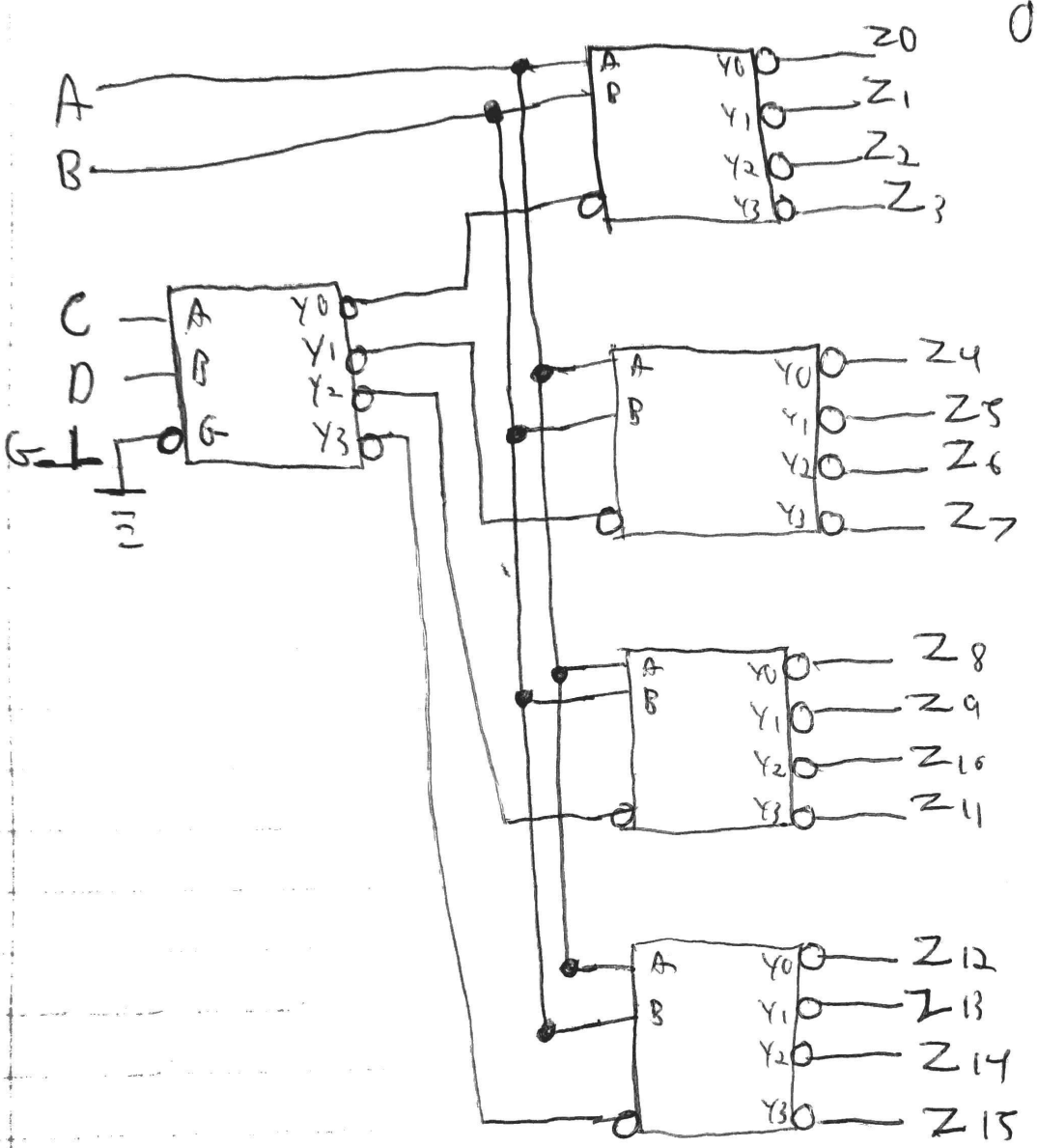


C	B	A	F
0	1	0	1
1	1	0	1

C	D	E	G
0	0	0	1
0	1	0	1
0	1	1	1

HW #4 Resub

1)

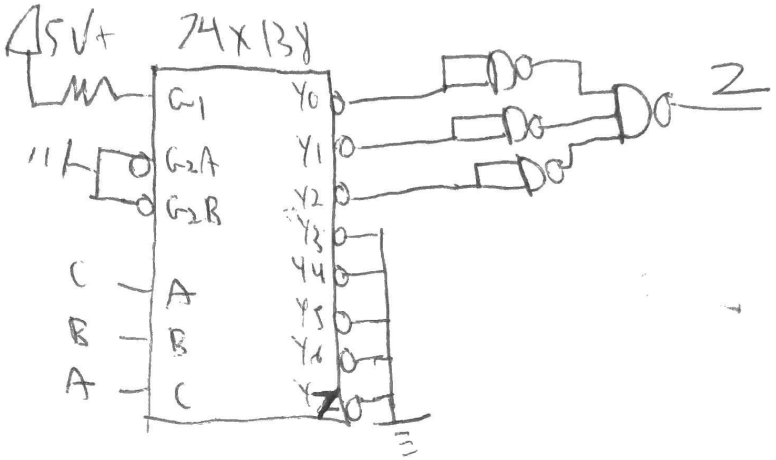


* My problem last time was not realizing that C, D are least significant inputs. I also added the inversion bubbles that I did not do last time.

6.20 B)

$$F = \Pi_{ABC}(3,4,5,6,7) = \Sigma_{ABC}(0,1,2)$$

A	B	C	Z
0	0	0	1
0	0	1	1
0	1	0	1



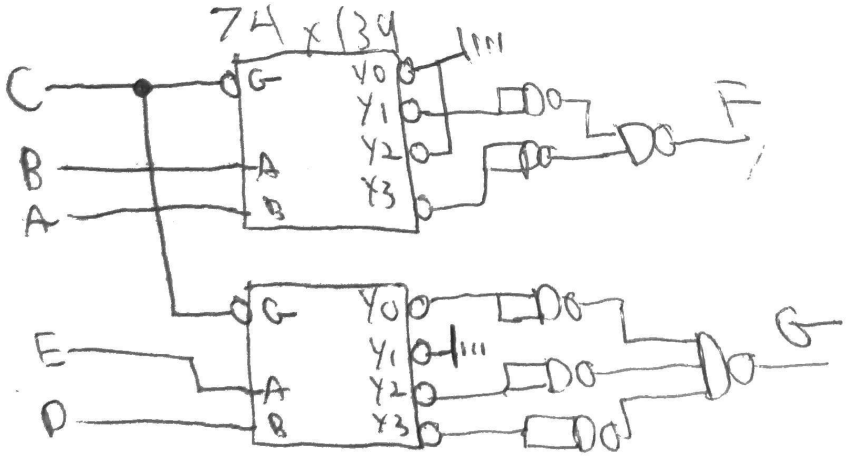
* My problem last time was I did not include inversion bubbles. I also forgot to label some parts of the chip. I also mixed up LSB and MSB values.

6.20 F)

$$F = \Sigma(2,6) \qquad G = \Sigma_{CDE}(0,2,5)$$

C	B	A	F
0	1	0	1
1	1	0	1

C	D	E	F
0	0	0	1
0	1	0	1
0	1	1	1



* My problem last time was messing up the significant bits. I did not realize that C can be used to control the enable pins but the solution helped me realize this.