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1. You would need a connection from registers to sign extension and a Mux to control the operation of doing either add or addi. We would also need to add an ALUsrc control wire.

2.

Reg Dst	ALUsrc	MemReg	RegWrite	MemRead	Branch	ALUOp1	ALUOp0
0	1	0	1	0		0	0	0

MemWrite
0