



Indian Institute of Technology Delhi

HAUZ KHAS, NEW DELHI - 110016

Programme Code: IITD/2024/PQIP/258927/37



Centre for Applied Research in Electronics

Continuing Education Programme

On

Digital VLSI Design

This is to certify that

Mr./Ms. Sahruday Gaje

has successfully completed the online certificate programme on "**Digital VLSI Design**" held from **20th October 2024** to **11th May 2025** by the Indian Institute of Technology Delhi.

PROF. ANKUR GUPTA
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