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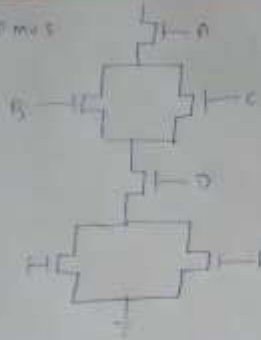
Ques: To Design CMOS circuit F. $(A+BC+DEF)$

Theory: CMOS is the semiconductor technology used in the transistors that are manufactured into most of today's computer microchips in CMOS technology. Both kind of transistors are used in a complementary way to form a current gate that form an effective means of electrical control.

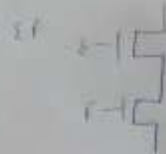
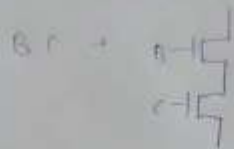
Now for PMOS circuit design:

The image shows hand-drawn circuit diagrams for PMOS and NMOS transistors. The PMOS diagram shows a PMOS transistor with its source connected to VDD and its gate to input A. The NMOS diagram shows an NMOS transistor with its source connected to ground and its gate to input B. There is also a small schematic of a CMOS inverter at the bottom left.

For Pmos



For Nmos



Complete diagram.



Now



