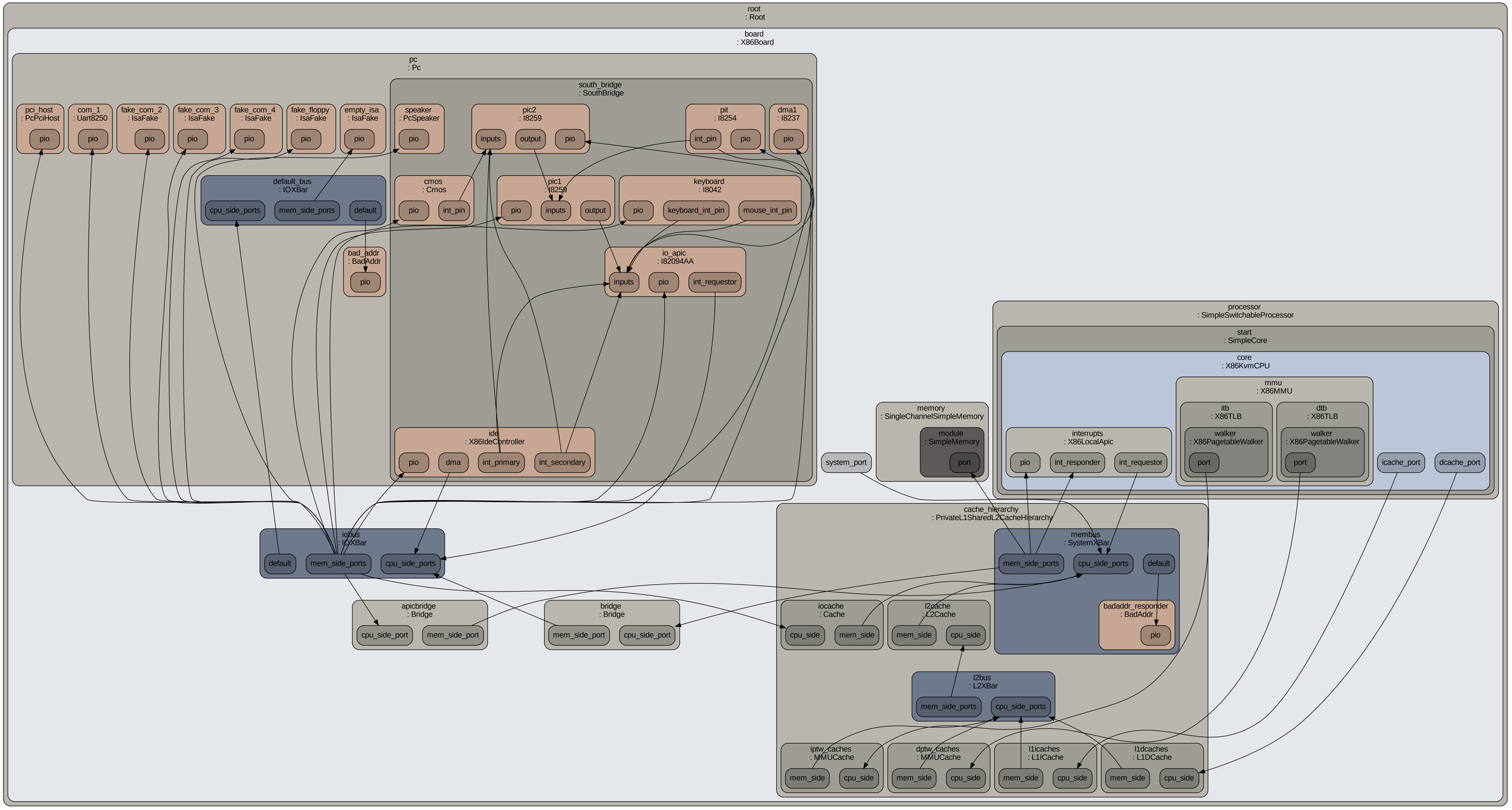
root
: Root
board
: X86Board
pc
: Pc
south_bridge
: SouthBridge
pci_host
: PcPciHost
pio
com_1
: Uart8250
pio
fake_com_2
: IsaFake
pio
fake_com_3
: IsaFake
pio
fake_com_4
: IsaFake
pio
fake_floppy
: IsaFake
pio
empty_isa
: IsaFake
pio
speaker
: PcSpeaker
pio
pic2
: I8259
inputs
output
pio
pit
: I8254
int_pin
pio
dma1
: I8237
pio
default_bus
: IOXBar
cpu_side_ports
mem_side_ports
default
cmos
: Cmos
pio
int_pin
pic1
: I8259
pio
inputs
output
keyboard
: I8042
pio
keyboard_int_pin
mouse_int_pin
bad_addr
: BadAddr
pio
io_apic
: I82094AA
inputs
pio
int_requestor
ide
: X86IdeController
pio
dma
int_primary
int_secondary
processor
: SimpleSwitchableProcessor
start
: SimpleCore
core
: X86KvmCPU
mmu
: X86MMU
itb
: X86TLB
walker
: X86PagetableWalker
port
dtb
: X86TLB
walker
: X86PagetableWalker
port
interrupts
: X86LocalApic
pio
int_responder
int_requestor
icache_port
dcache_port
memory
: SingleChannelSimpleMemory
module
: SimpleMemory
port
system_port
cache_hierarchy
: PrivateL1SharedL2CacheHierarchy
membus
: SystemXBar
mem_side_ports
cpu_side_ports
default
badaddr_responder
: BadAddr
pio
iocache
: Cache
cpu_side
mem_side
l2cache
: L2Cache
mem_side
cpu_side
l2bus
: L2XBar
mem_side_ports
cpu_side_ports
iptw_caches
: MMUCache
mem_side
cpu_side
dptw_caches
: MMUCache
mem_side
cpu_side
l1icaches
: L1ICache
mem_side
cpu_side
l1dcaches
: L1DCache
mem_side
cpu_side
iobus
: IOXBar
default
mem_side_ports
cpu_side_ports
apicbridge
: Bridge
cpu_side_port
mem_side_port
bridge
: Bridge
mem_side_port
cpu_side_port