EE224 Course Project

Design of a multi cycle processor, IITBProc

This is the complete design document containing the state diagrams for all the instructions and the complete entity.

-Submitted by
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Jayesh Choudhary(170070038)
Vishesh Verma(170070039)
Saksham Khandelwal(170070024)

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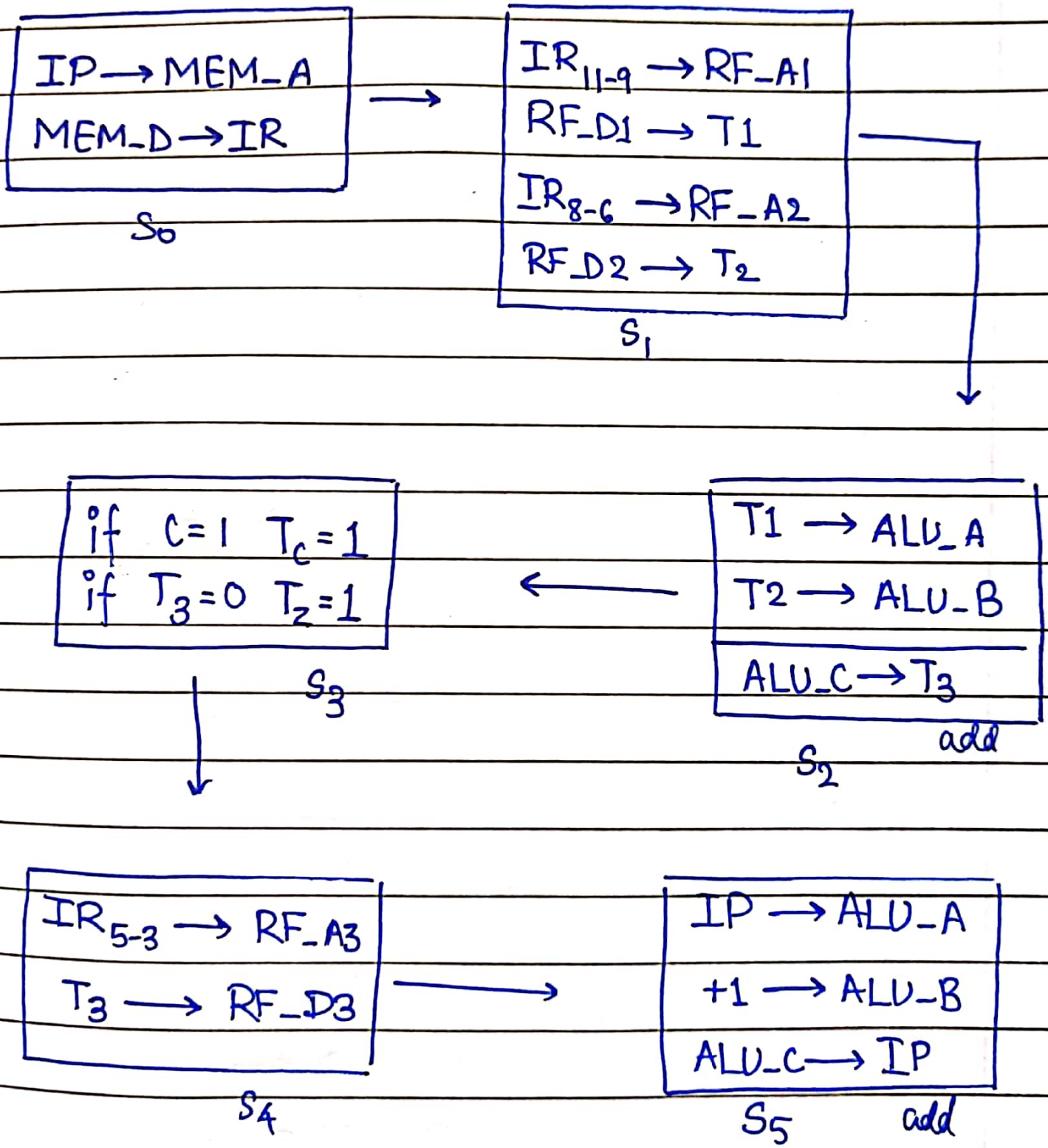

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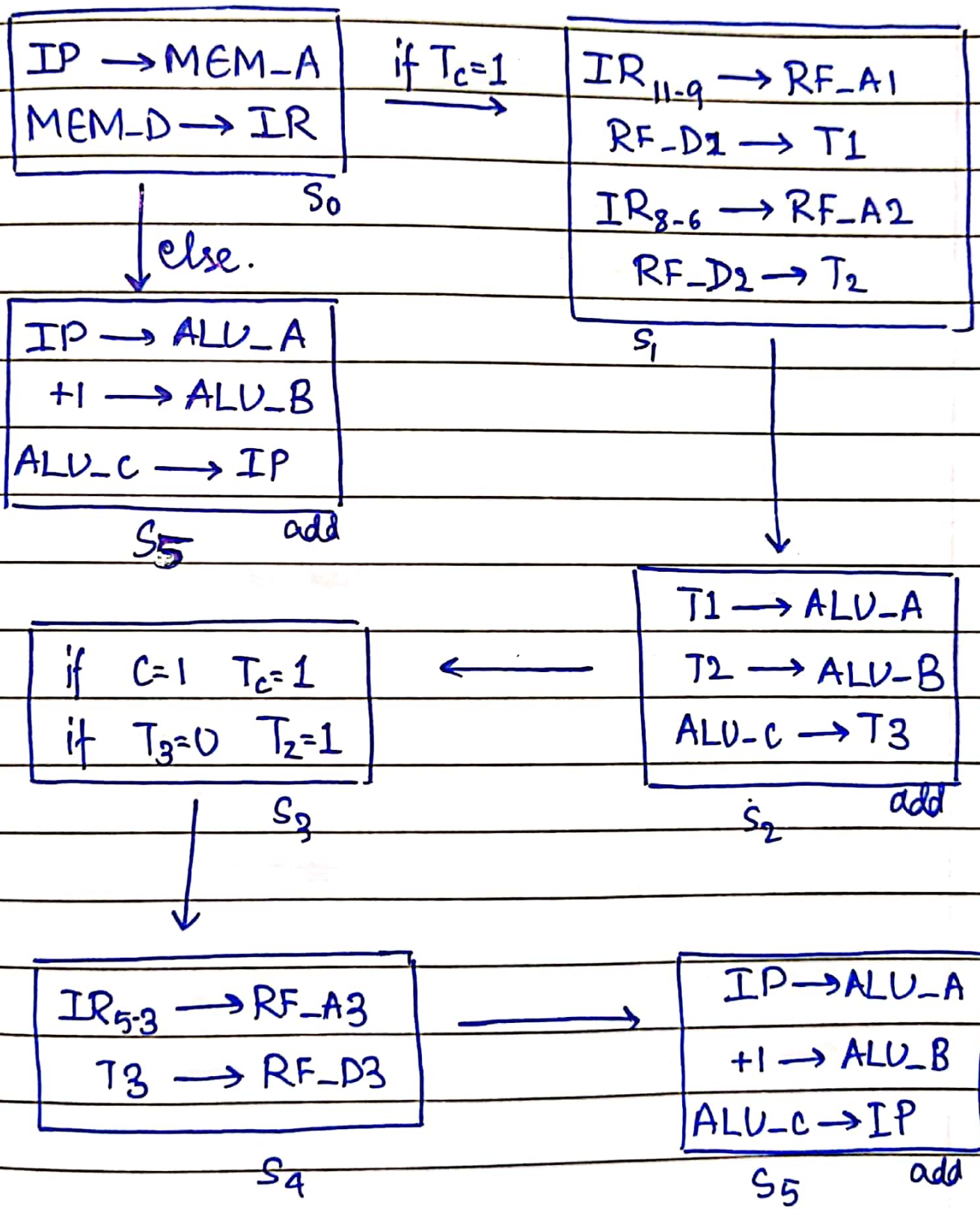

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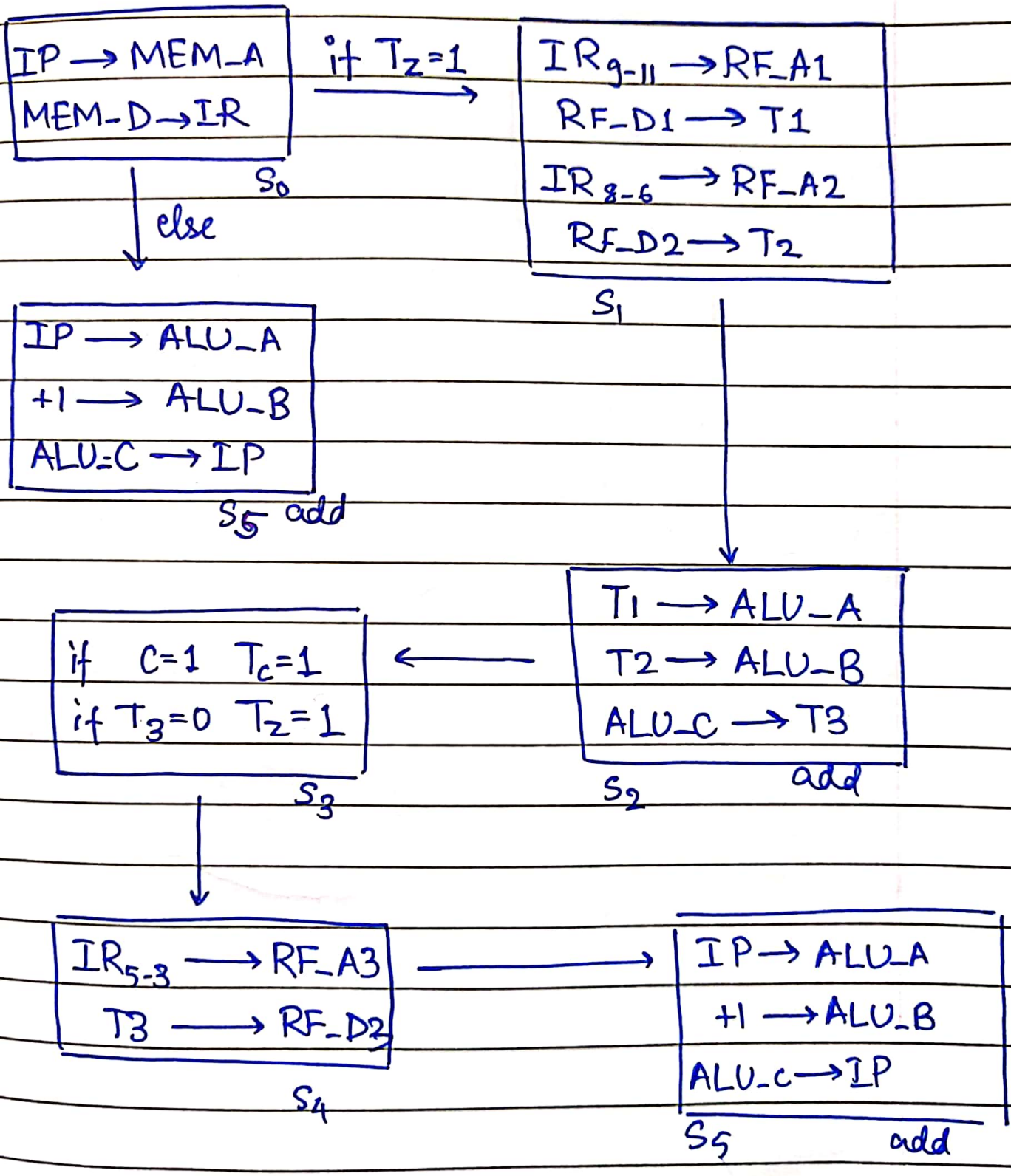

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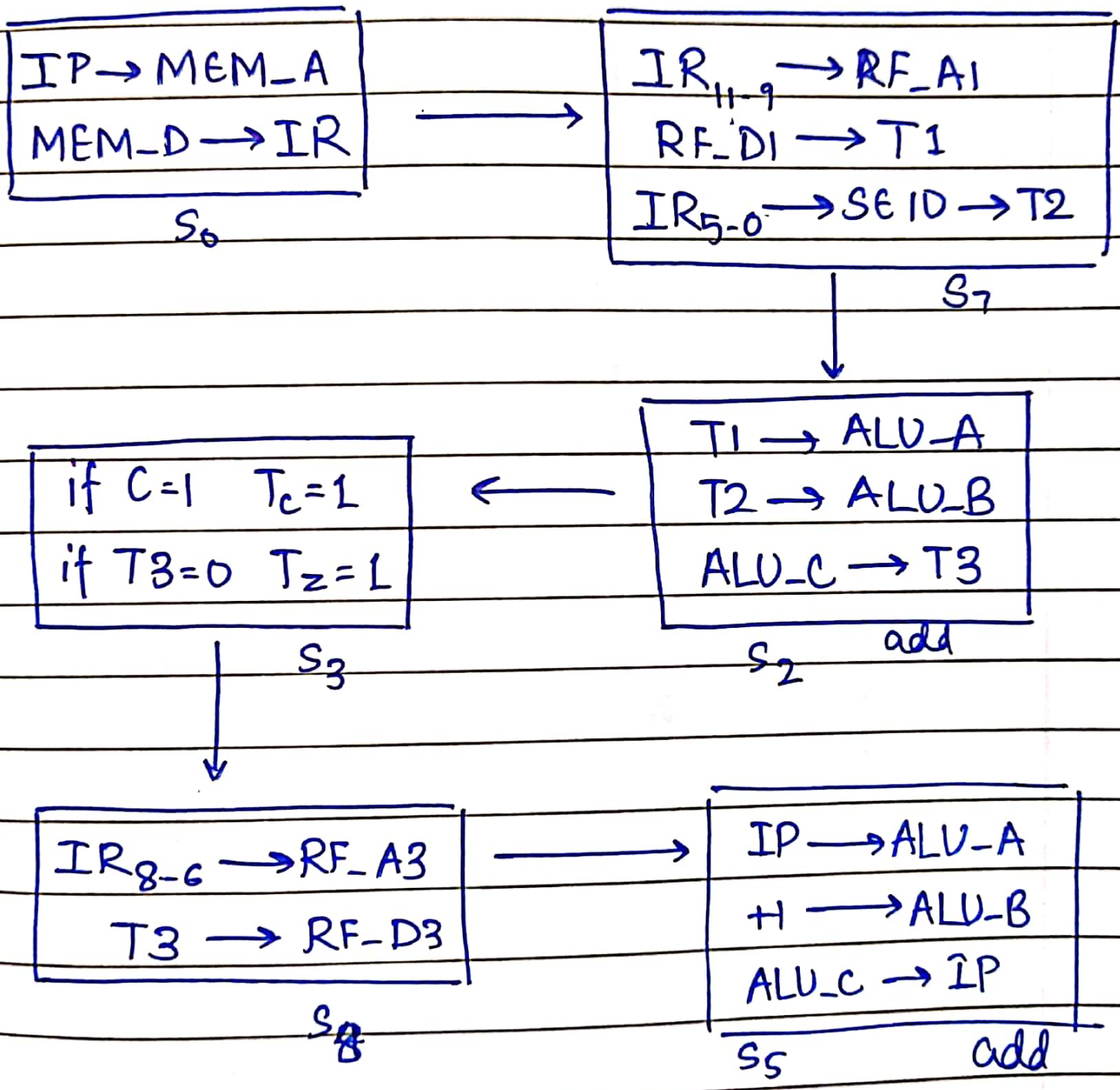

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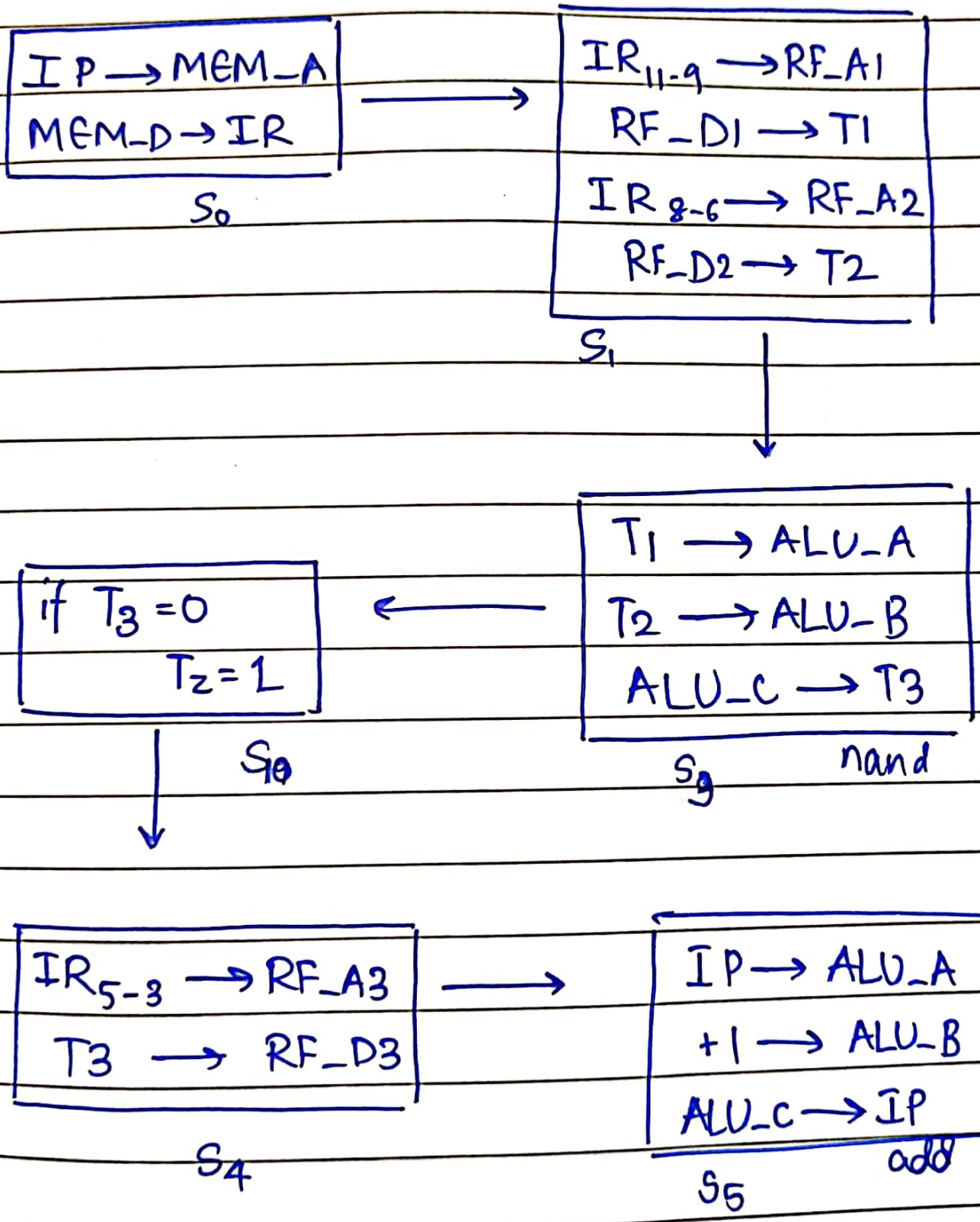

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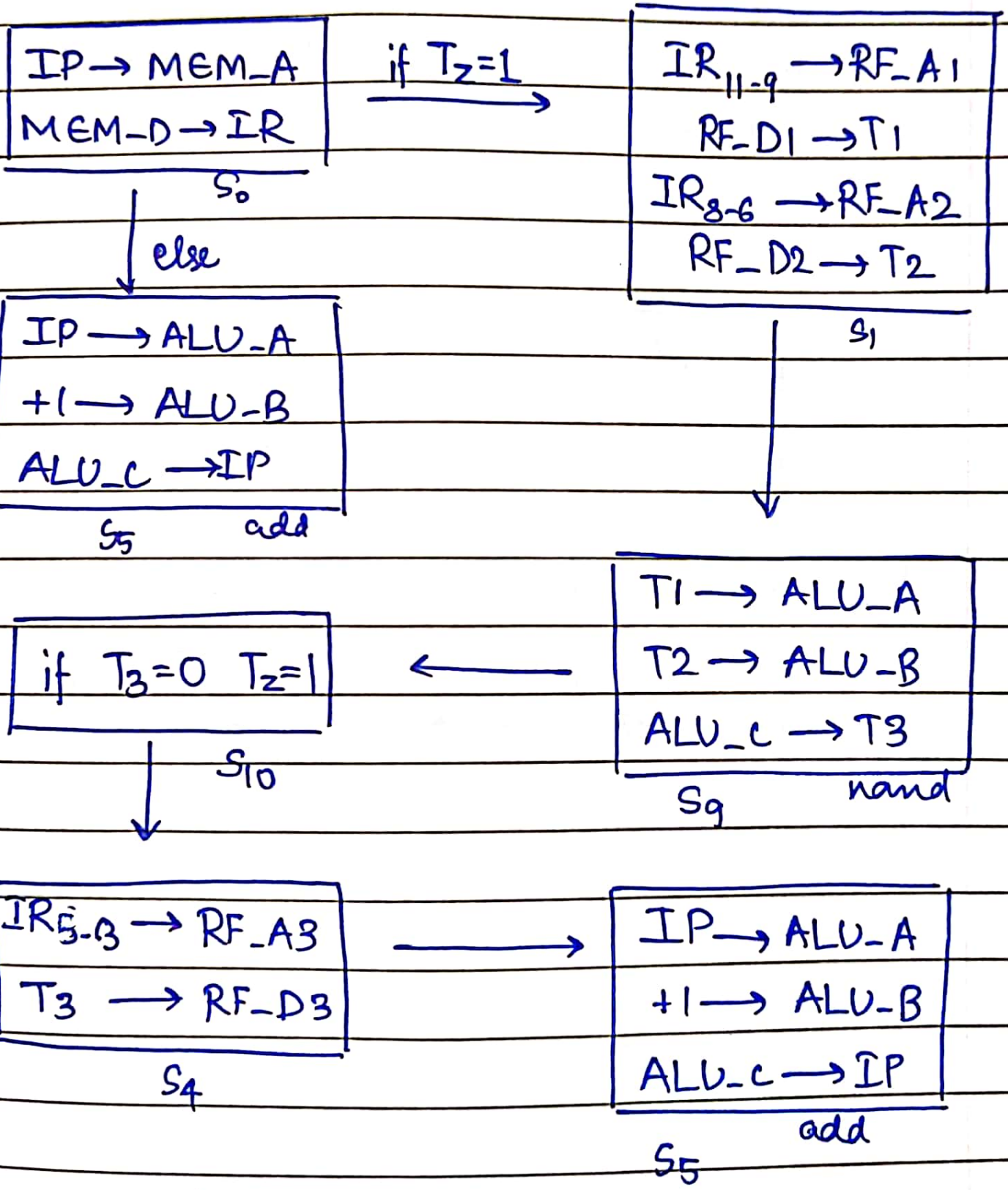

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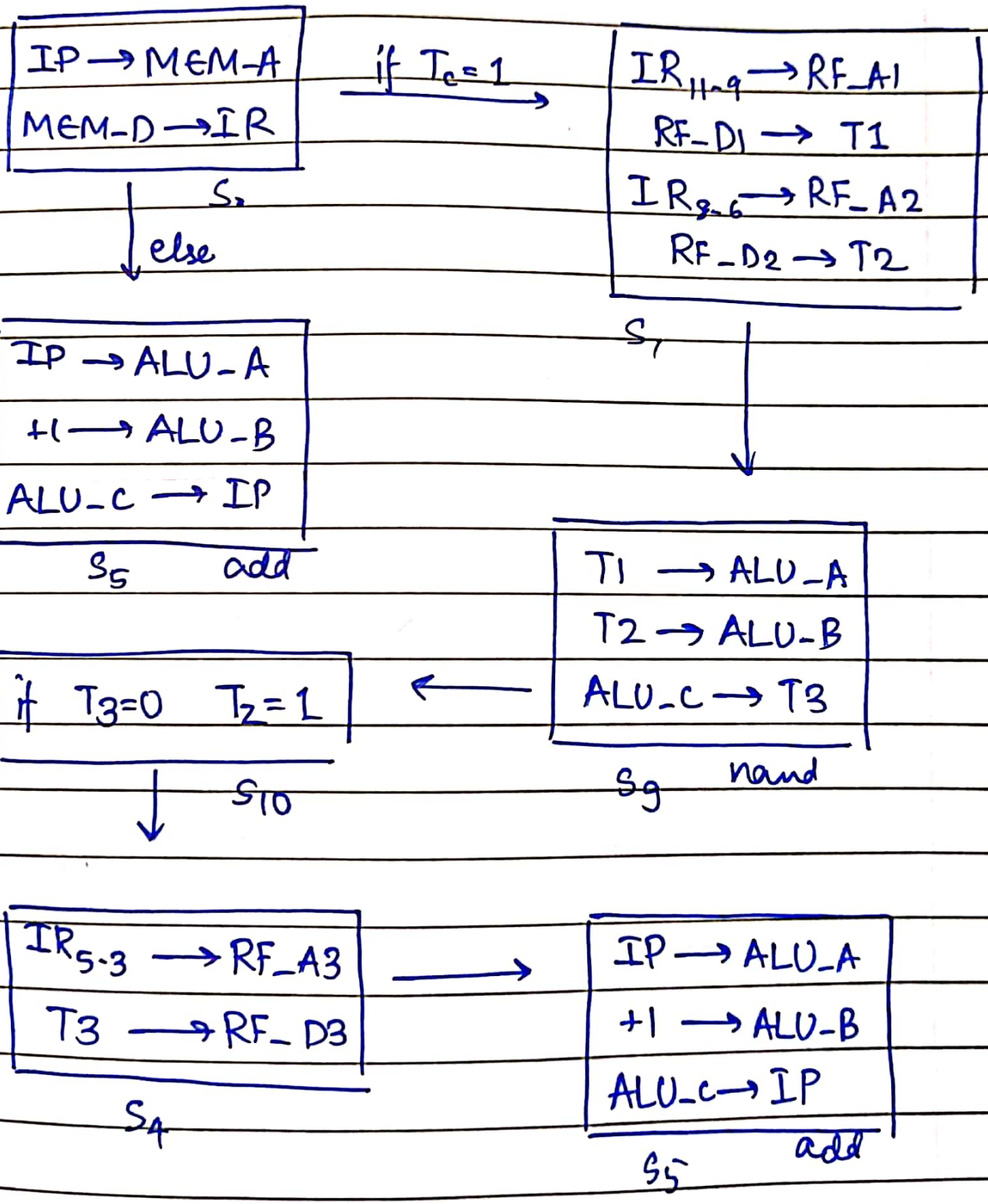

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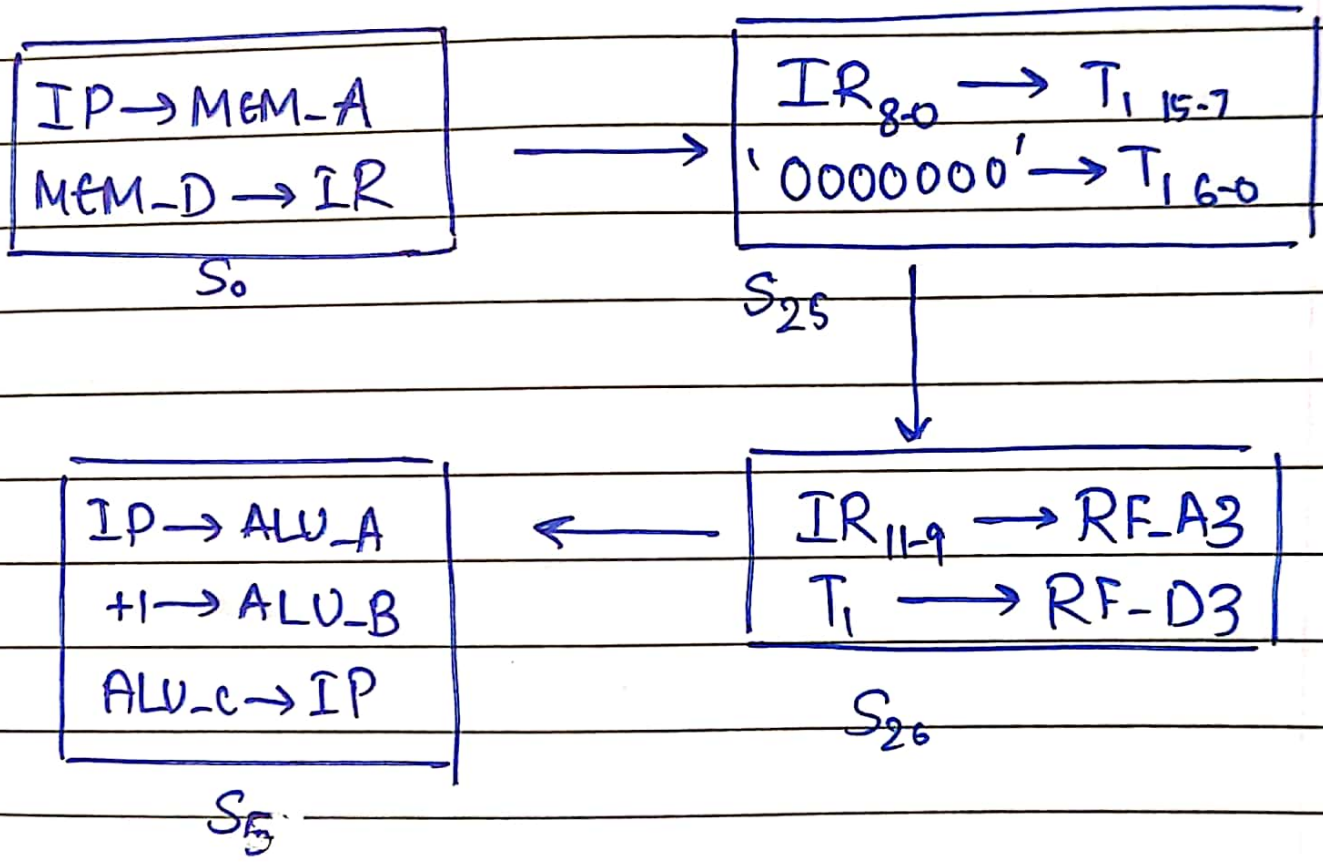

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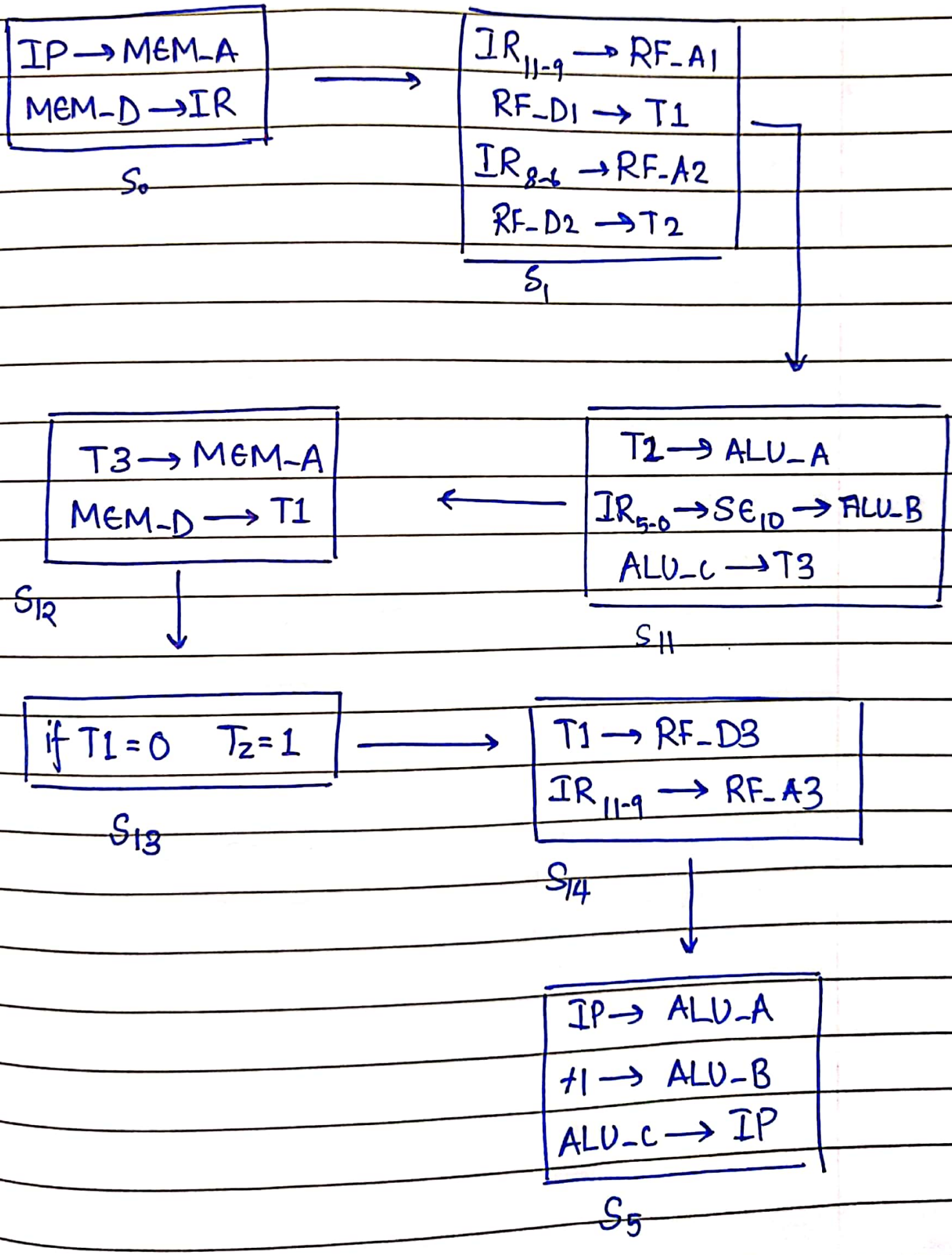

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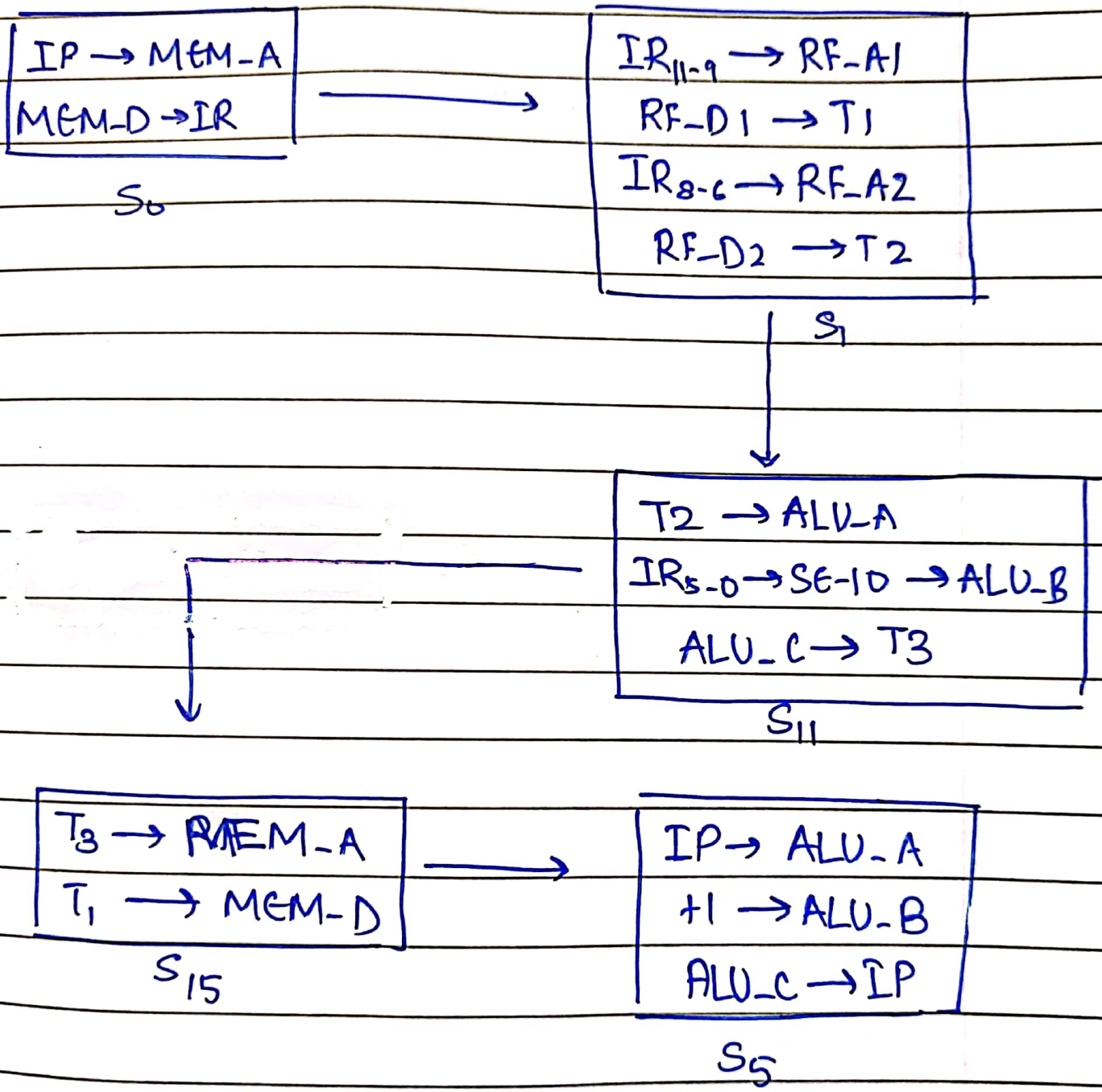

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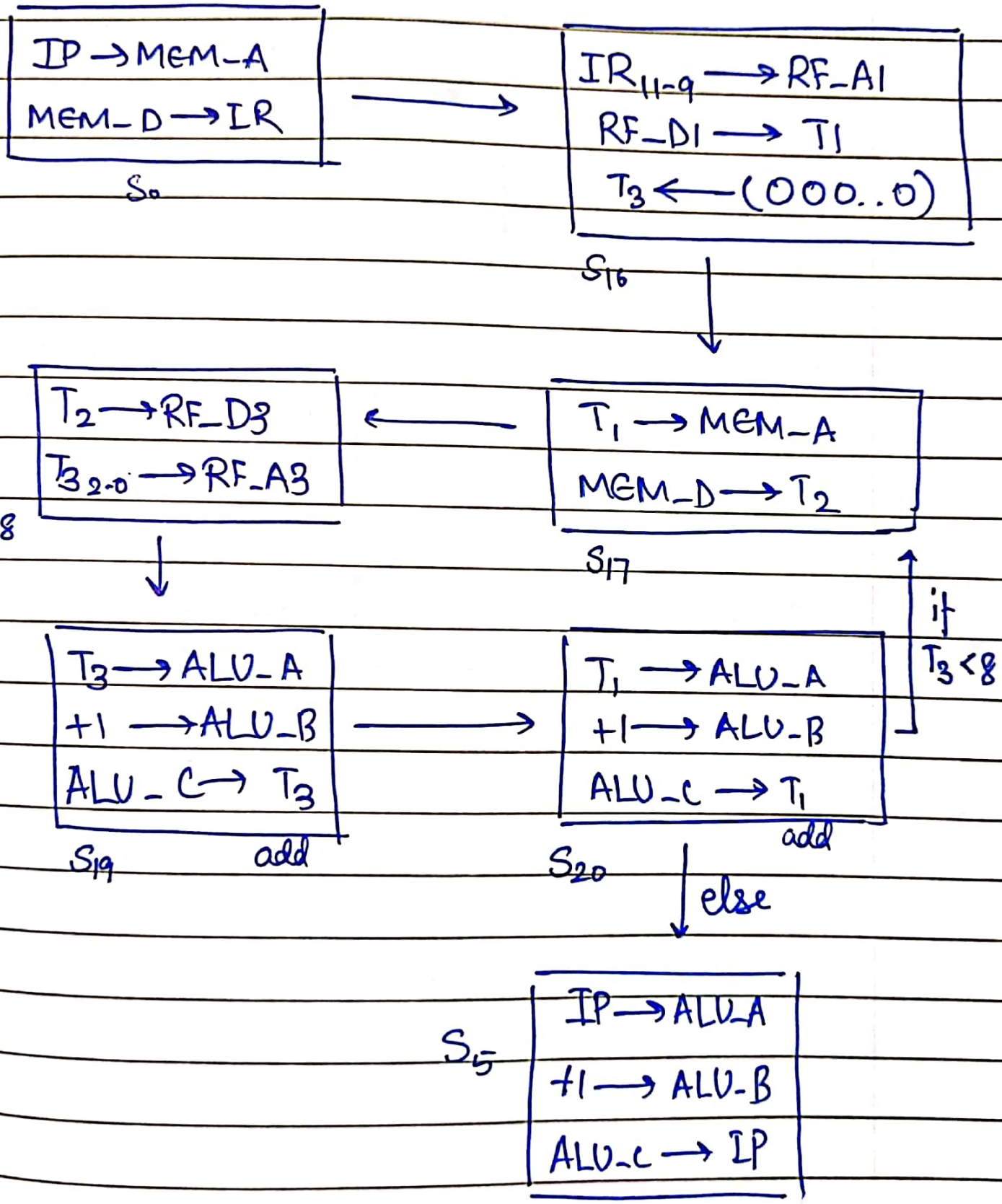

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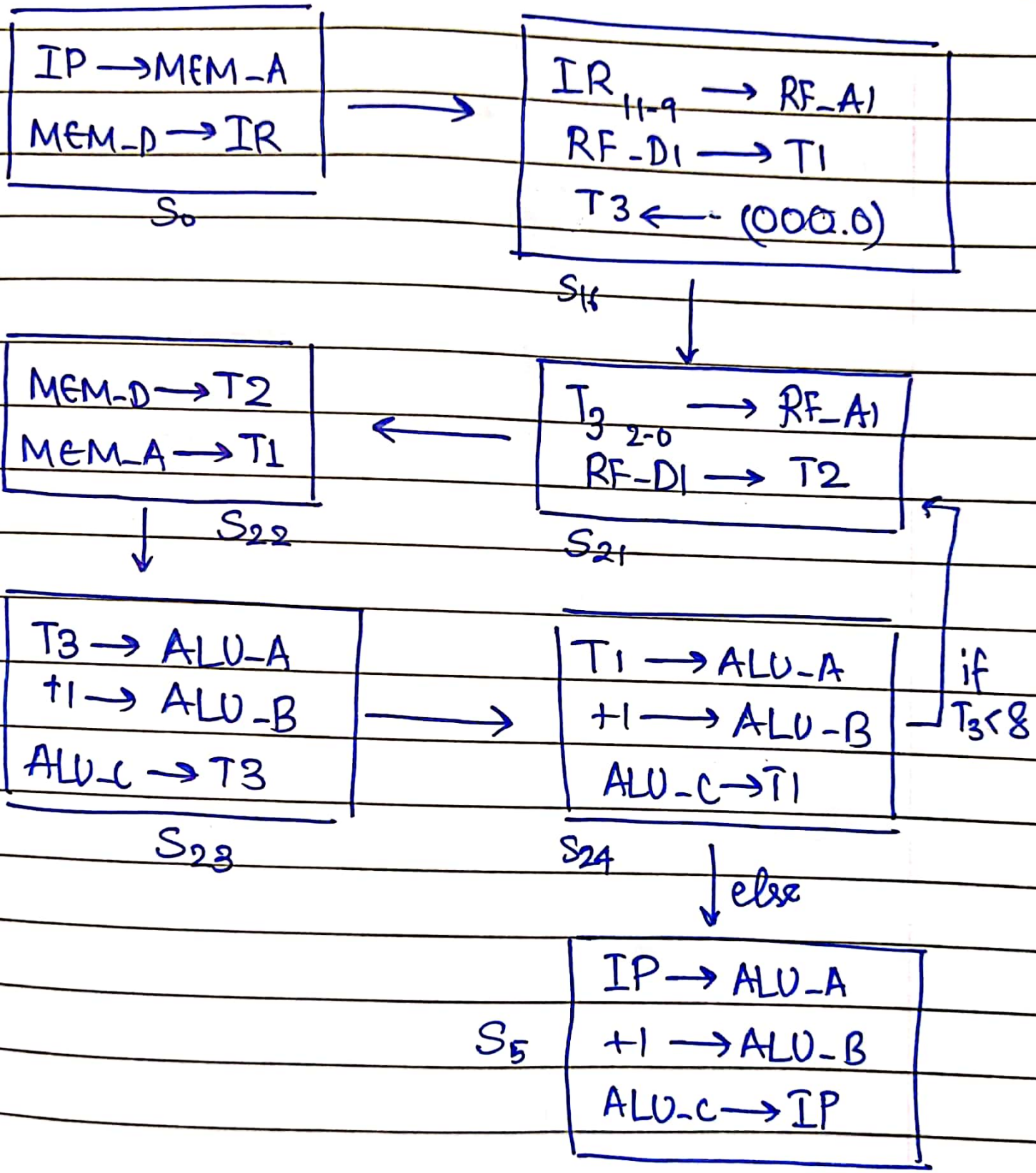

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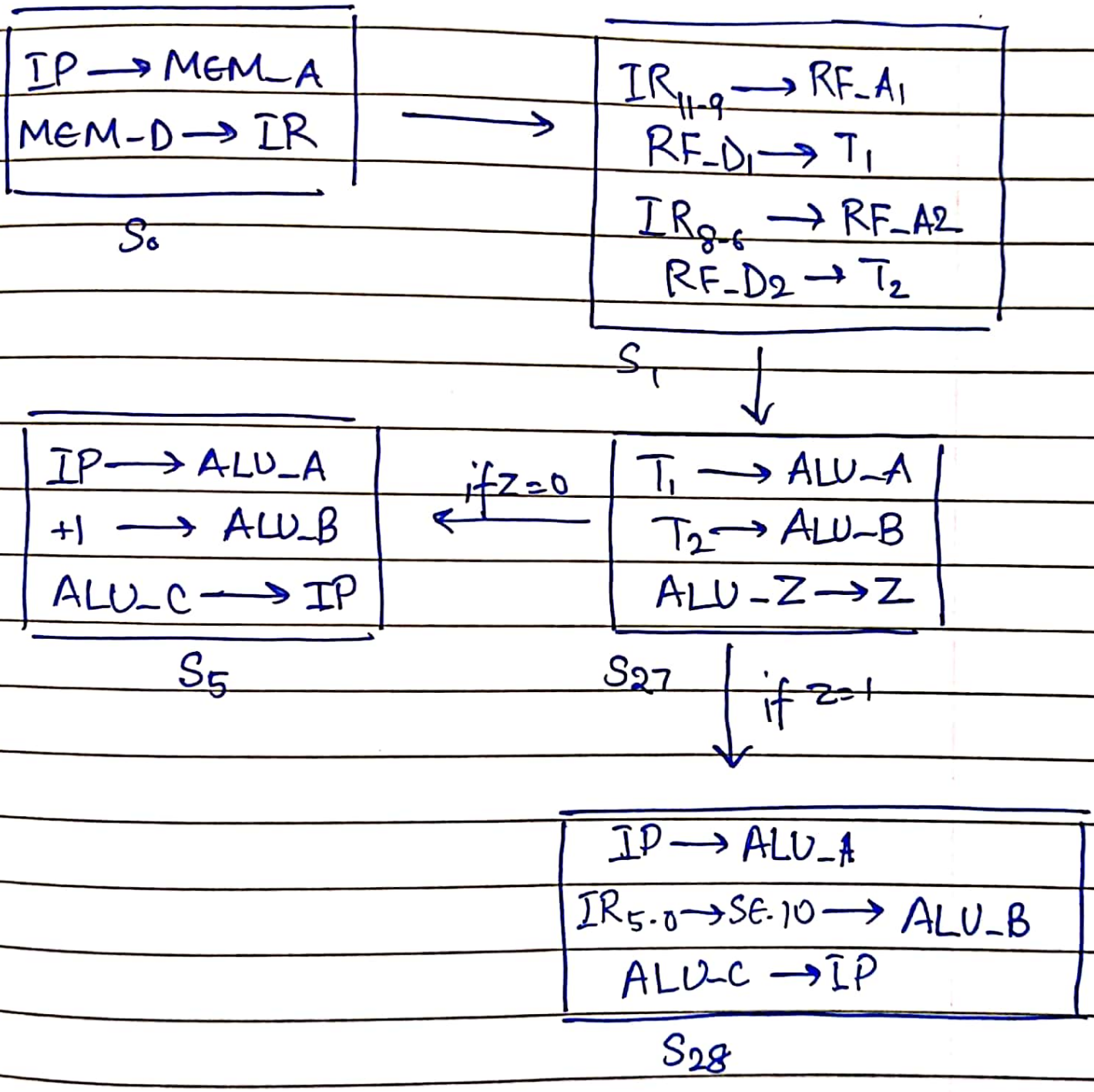

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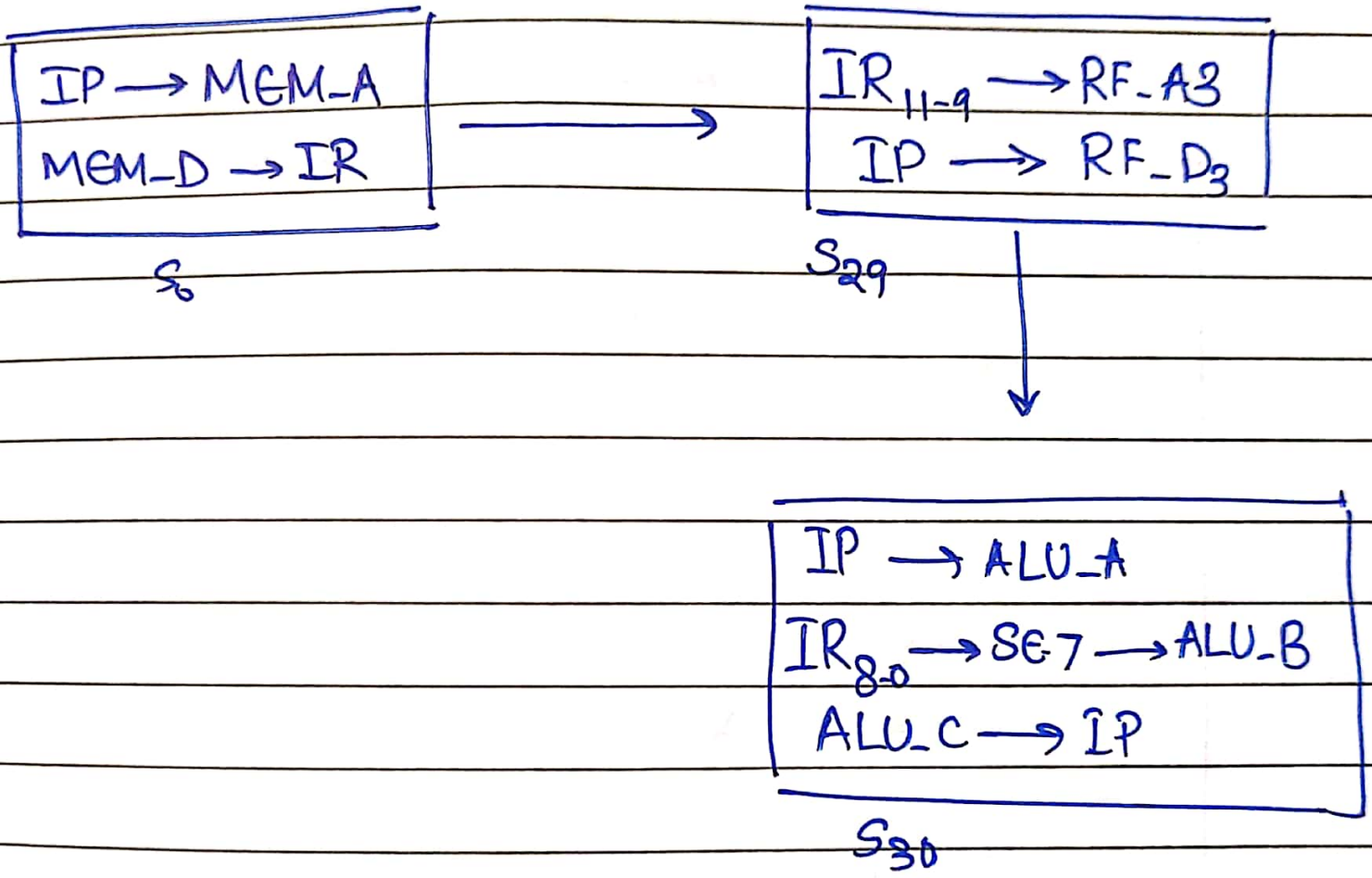

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