

## SCHOLASTIC ACHIEVEMENTS

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- Secured All India Rank **91** in **IIT-JEE '12** and All India Rank **26** in **AIEEE '12**
- Awarded the prestigious **KVPY** scholarship by **IISc**(Indian Institute of Science)
- Secured state **3<sup>rd</sup>** rank in SSTSE conducted by Unified Council and All India Rank **125** in Cyber Olympiad
- Secured state **88<sup>th</sup>** rank in EAMCET '12 among 380,000 students and qualified for **INPhO '12**, **INMO '09**

## INTERNSHIPS AND RESEARCH PROJECT

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### ALTISOURCE, *Mumbai*

[Summer Internship '15].

- Researched and evaluated existing quantitative model implementations for **loss mitigation problems** (debt modifications) and proposed use of new algorithm(**Genetic Algorithm**) for improving performance
- Evaluated algorithms such as **Pattern Search** and **GA** and used them in solving optimization problems; also analyzed the effects of constraints and initial point in these **optimization algorithms**

### NEUROMORPHIC ENGINEERING

Prof. Bipin Rajendran, IIT B [Summer '13].

- Learnt simulating **spiking neurons** using different models in MATLAB and Artificial Neural Networks
- Worked on design of spiking neural networks and learning rules to train them

### PLANCESS, a startup of IITB Alumni

[Summer Internship '14].

- Guided IIT-JEE aspirants by preparing and refining the material during the internship
- Proof reading, rank predictor estimation using their marks

## COURSE PROJECTS AND OTHER TECHNICAL PROJECTS

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### Eigenfaces Vs Fisherfaces, *Digital Image Processing*

[Jul-Nov '14].

- Demonstrated the performance of **Fisherfaces Vs Eigenfaces/PCA algorithm** under various conditions of illumination and facial expressions

### Mathematical Function Plotter, *Digital Circuits*

[Jan-Apr '14].

- Designed a Mathematical Function Plotter and tried to implement it on FPGA using VHDL

### Finite Machine based controller, *Microprocessors*

[Jul-Nov '14].

- Designed an **optimal datapath**(registers, functional units and point-to-point communication based steering logic) and implemented on FPGA using **synthesizable code** in VHDL

## POSITIONS OF RESPONSIBILITY

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### Alumni Secretary of Electrical Engineering Dept.

- Collaborating with **SARC**(Student Alumni Relation Cell) conducted **SAM**(Student Alumni Meet), **reunions** of Alumni(decennial, silver and golden jubilee)

### Others:

- Coordinator **E-cell**'14, *Operations Department* and **Mood Indigo**'13, *Food and Beverages Department*
- Mentored freshmen for the robotics competition, **XLR8**

## TECHNICAL PROFICIENCY

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- **Programming and Web Development:** Matlab, C++, SAGE, VHDL, Assembly, HTML, CSS.
- **Technical Softwares:** Arduino, Poisson, NGSpice, Solid Works, AutoCAD, Keil, GNU Radio,  $\text{\LaTeX}$ .

## OTHER ACHIEVEMENTS AND ACTIVITIES

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- Completed **Game Theory** course in **coursera** and earned certificate from **Georgia Institute of Technology**
- **Sudoku** Solver in C++, RF bot, **Line Following** bot
- Attended camps conducted by IISc. Bangalore and IISER Trivandrum as a part of KVPY
- Worked for **Insight** Student Media Body, IIT Bombay
- Marketing a product(**Arbit T-shirts**) and promoting, pricing it in the institute as a part of Marketing course