



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

HYDERABAD - 500 085, TELANGANA STATE, INDIA.

CONSOLIDATED MARKS MEMO / CREDIT SHEET



B.Tech. **ELECTRONICS & COMMUNICATION ENGINEERING**

CMM. No. : **C 0739098**

Serial No. : 21180041767

Name : **DONAGIRI SAIKRUSHNA**

Hall Ticket No. : **12B41A0419**

Year of Admission : 2012-2013



Name of the College : B4-K C E, ARMOOR

Month & Year of Final Exam : May, 2016

Class Awarded : **FIRST CLASS**

S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS
	Maximum Marks in Theory	25	75	100			Maximum Marks in Lab	25	50	75	
I YEAR											
1	ENGLISH	17	56	73	4	2	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	22	45	67	4
3	IT WORKSHOP & ENGINEERING WORKSHOP	24	46	70	4	4	MATHEMATICS - I	19	37	56	6
5	MATHEMATICAL METHODS	21	34	55	6	6	ENGINEERING PHYSICS	20	42	62	4
7	ENGINEERING CHEMISTRY	19	47	66	4	8	COMPUTER PROGRAMMING & DATA STRUCTURES	23	35	58	6
9	ENGINEERING DRAWING	24	31	55	4	10	COMPUTER PROGRAMMING LAB	23	44	67	4
11	ENGG. PHYSICS & ENGG. CHEMISTRY LAB	22	42	64	4						
I SEMESTER											
II YEAR											
1	MATHEMATICS-III	21	42	63	3	1	PRINCIPLES OF ELECTRICAL ENGINEERING	22	38	60	3
2	PROBABILITY THEORY & STOCHASTIC PROCESSES	25	29	54	3	2	ELECTRONIC CIRCUIT ANALYSIS	23	38	61	4
3	ENVIRONMENTAL STUDIES	23	26	49	3	3	PULSE & DIGITAL CIRCUITS	21	40	61	4
4	ELECTRIC CIRCUITS	22	28	50	4	4	SWITCHING THEORY & LOGIC DESIGN	23	37	60	4
5	ELECTRONIC DEVICES & CIRCUITS	20	42	62	4	5	ELECTROMAGNETIC THEORY & TRANSMISSION LINES	22	34	56	4
6	SIGNALS & SYSTEMS	22	44	66	4	6	ELECTRICAL ENGINEERING LAB	24	48	72	2
7	ELECTRONIC DEVICES & CIRCUITS LAB	24	47	71	2	7	ELECTRONIC CIRCUIT ANALYSIS LAB	22	48	70	2
8	BASIC SIMULATION LAB	23	50	73	2	8	PULSE & DIGITAL CIRCUITS LAB	23	48	71	2
I SEMESTER											
III YEAR											
1	CONTROL SYSTEMS	16	38	54	4	1	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	21	31	52	4
2	COMPUTER ORGANIZATION	21	35	56	4	2	ADVANCED ENGLISH COMMUNICATION SKILLS (LAB)	22	44	66	2
3	ANTENNAS & WAVE PROPAGATION	16	26	42*	3	3	OPERATING SYSTEMS	18	47	65	4
4	ELECTRONIC MEASUREMENTS & INSTRUMENTATION	22	33	55	4	4	DIGITAL COMMUNICATIONS	21	41	62	3
5	ANALOG COMMUNICATIONS	20	26	46	3	5	MICROPROCESSORS & MICROCONTROLLERS	23	26	49	4
6	IC APPLICATIONS	19	26	45	3	6	DIGITAL SIGNAL PROCESSING	20	27	47	4
7	ANALOG COMMUNICATIONS (LAB)	24	44	68	2	7	MICROPROCESSORS & MICROCONTROLLERS (LAB)	24	48	72	2
8	IC APPLICATIONS (LAB)	22	47	69	2	8	DIGITAL SIGNAL PROCESSING (LAB)	24	46	70	2
I SEMESTER											
IV YEAR											
1	MANAGEMENT SCIENCE	16	34	50	3	1	DIGITAL DESIGN THROUGH VERILOG HDL	18	29	47	3
2	EMBEDDED SYSTEMS	19	38	57	4	2	INDUSTRY ORIENTED MINI PROJECT	-	48	48	2
3	E-CAD AND VLSI LAB	21	46	67	2	3	SEMINAR	37	-	37	2
4	MICROWAVE ENGG. AND DIGITAL COMMNS. LAB	24	48	72	2	4	MAJOR PROJECT	48	148	196	10
5	VLSI DESIGN	22	36	58	4	5	COMPREHENSIVE VIVA	-	87	87	2
6	MICROWAVE ENGINEERING	21	33	54	3	6	SATELLITE COMMUNICATIONS	22	29	51	3
7	COMPUTER NETWORKS	18	A	18*	0	7	RADAR SYSTEMS	22	34	56	3
8	DIGITAL IMAGE PROCESSING	22	37	59	3						

(# Project Internal=50, External=150)

Number of Credits registered for : 200 Aggregate Marks Secured for best: 193

Aggregate Marks Secured **3507 OUT OF 5200 (67.44%)**

Date of Issue : **August 16, 2016**

(see overleaf for Rules concerned to award of class)



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CONTROLLER OF EXAMINATIONS