Date
No

ROM → Read only (Code in there)
↳ Floating gate mosfet

- mask → one code only → لعب الأطفال ↳ سد المصنع
- PROM → one code only → سد اليوزر
- EPROM → multiple codes → noise affected

Hyprid

- E^2PROM → electric ⌒→ more speed
  - Endurance
  - high cost
- flash →
  - Block access
  - Sector by Sector
  - low cost
- NVRAM → SRAM + Battery
  - SRAM + E^2PROM + Battery

cache → levels

Register files > cache > RAM > HDD

size →

← speed increase

- FPU → floating point unit
- MPU → Memory protection unit
- MMU → Memory management unit

ARCH

Von-neuman

memory mapped

Doesn't support pipe line

one memory system

Harvard

Support pipeline

port mapped

	RC	CR	CO
cost	↓	—	↑
Accuracy	↓	—	↑
Temp	↓	↑	↑
Gmi	↓	↑	↑
vibr	↑	↓	↓
Shifting time	↑	—	↓