North South University

Department of Computer Science and Engineering

Quiz-4, Section – 4, Spring-2017

Course No: **CSE 231** Course Title: **Digital Logic Design**

Time:20 min Full Marks:12

1. a. Draw the state diagram from the following state table 7

b. How many different states are there into this state table?

c. How many flip flops you need to implement the circuit based on this state table?

Present State		Input	Next State		Output
A	**B**	**x**	**A**	**B**	**y**
0	0	0			
0	0	1			
0	1	0			
0	1	1			
1	0	0			
1	0	1			
1	1	0			
1	1	1			

2. Draw the output of the following J-K flip flop (rising edge type). You can assume the initial value of Q =0 5