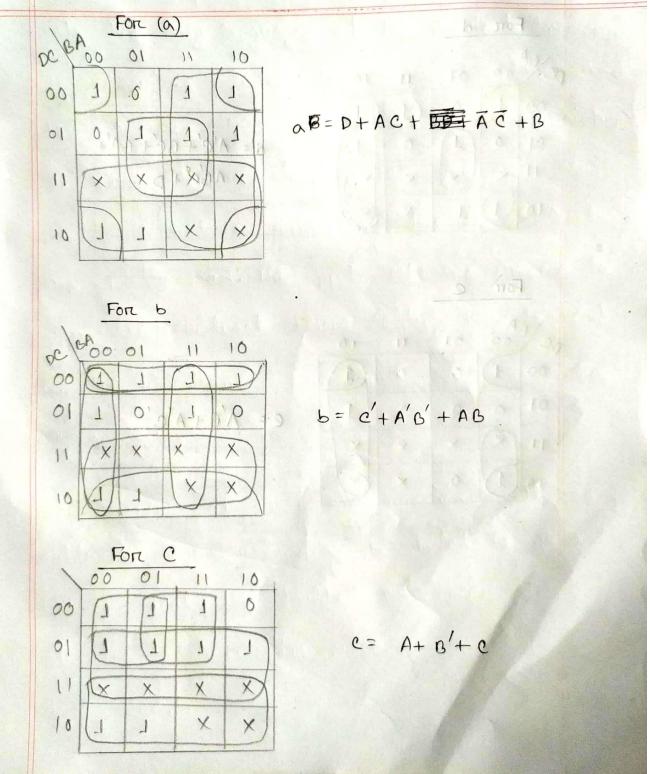
Class Assignment 06 10 Marks

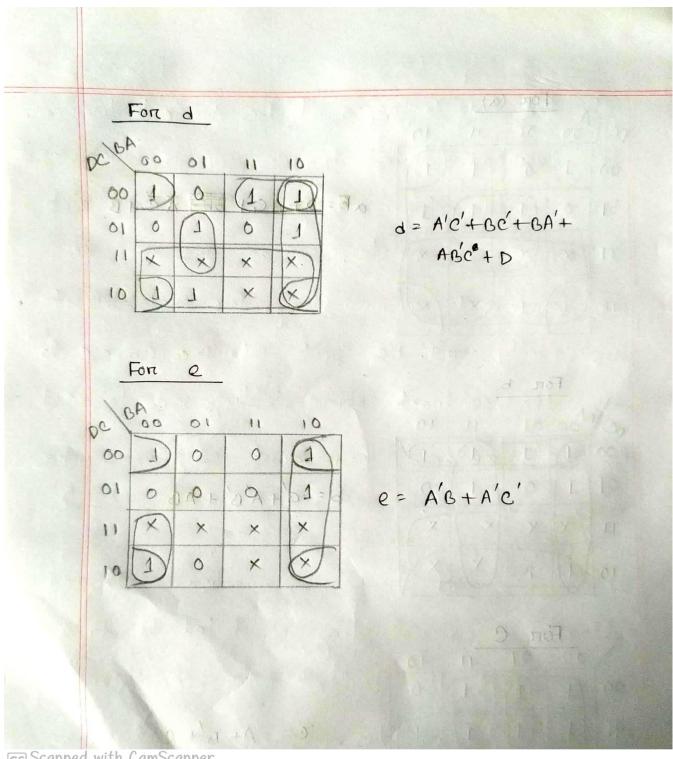
- 1. Complete the tables and attach the k-map simplification
- 2. Attach the drawing of logic diagram
- 3. Attach the simulation part

Truth Table:

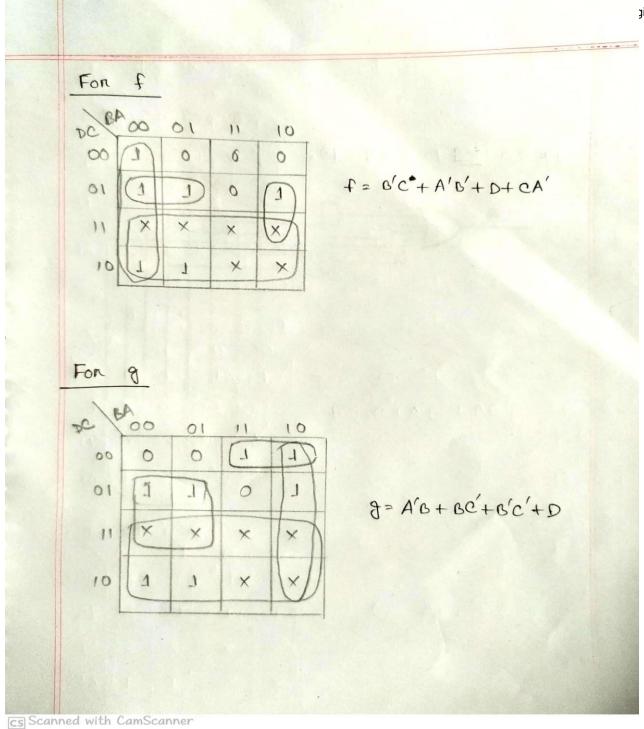
	Inputs				Outputs						
Decimal	D	С	В	Α	а	b	С	d	е	f	g
0	0 .	0	0	0	1	1	1	1	1	1	0
1	0	0	0	1	0	1	1	0	0	0	0
2	0	0	1	0	1	1	0	1	1	0	1
3	0	0	1	1	1	1	1	1	0	0	1
4	0	1	0	0	0	1	1	0	0	1	1
5	0	1	0	1	1	0	1	1	0	1	1
6	0	1	1	0	1	0	1	1	1	1	1
7	0	1	1	1	1	1	1	0	0	0	0
8	1	0	0	0	1	1	1	1	1	1	1
9	1	0	0	1	1	1	1	1	0	1	1



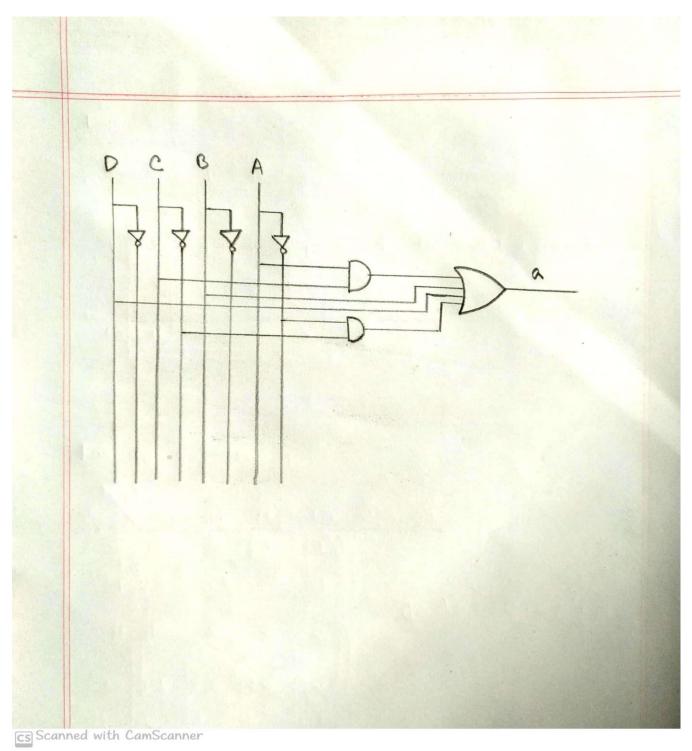
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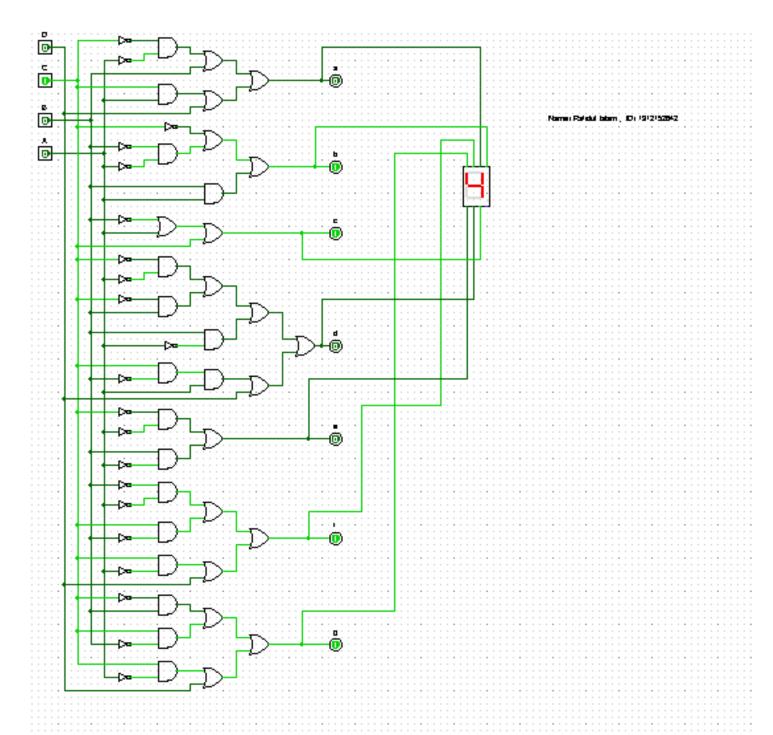
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K Map simplification



Logic Diagram



Simulaion