North South University

Department of Computer Science and Engineering
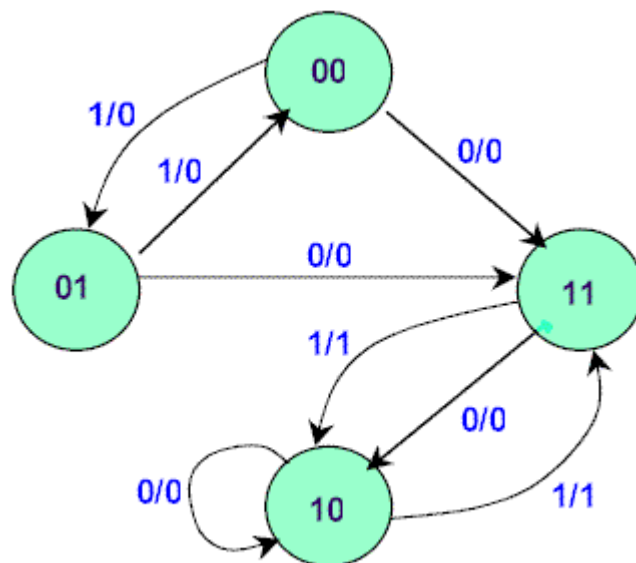
Quiz-4, Section – 5, Spring-2017

Course No: **CSE 231**   Course Title: **Digital Logic Design**

Time: 20 min   Full Marks: 12

1. a. Identify the number of states, inputs and outputs from the following state diagram   7
   b. Draw the state table based on the following state diagram
   c. How many flip flops required to implement the circuit based on this state diagram

2. Draw the output of the following J-K flip flop (rising edge type). You can assume the initial   5
   value of Q =0