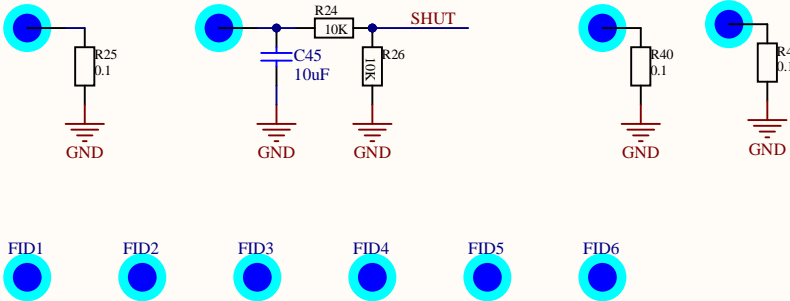
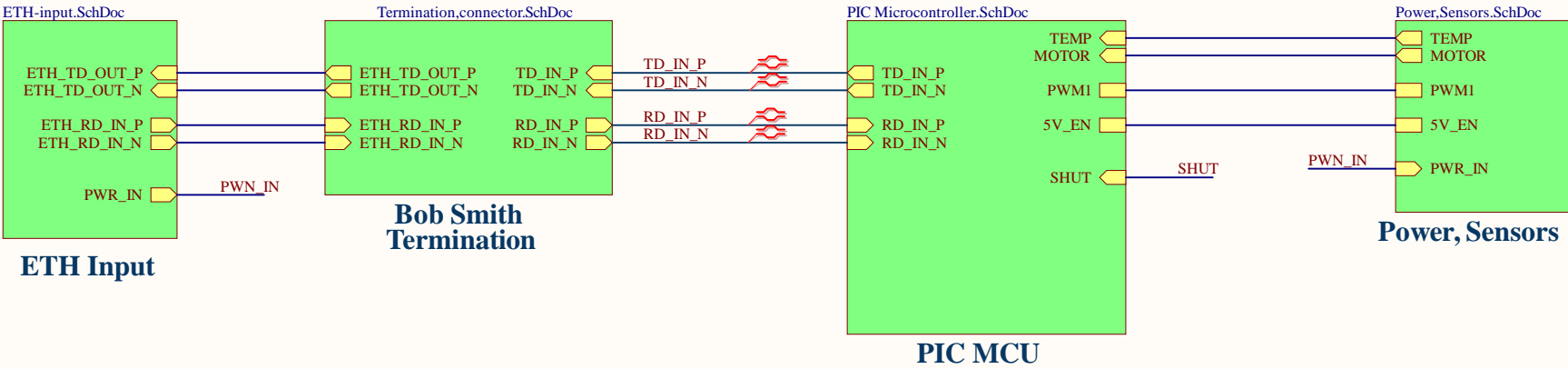


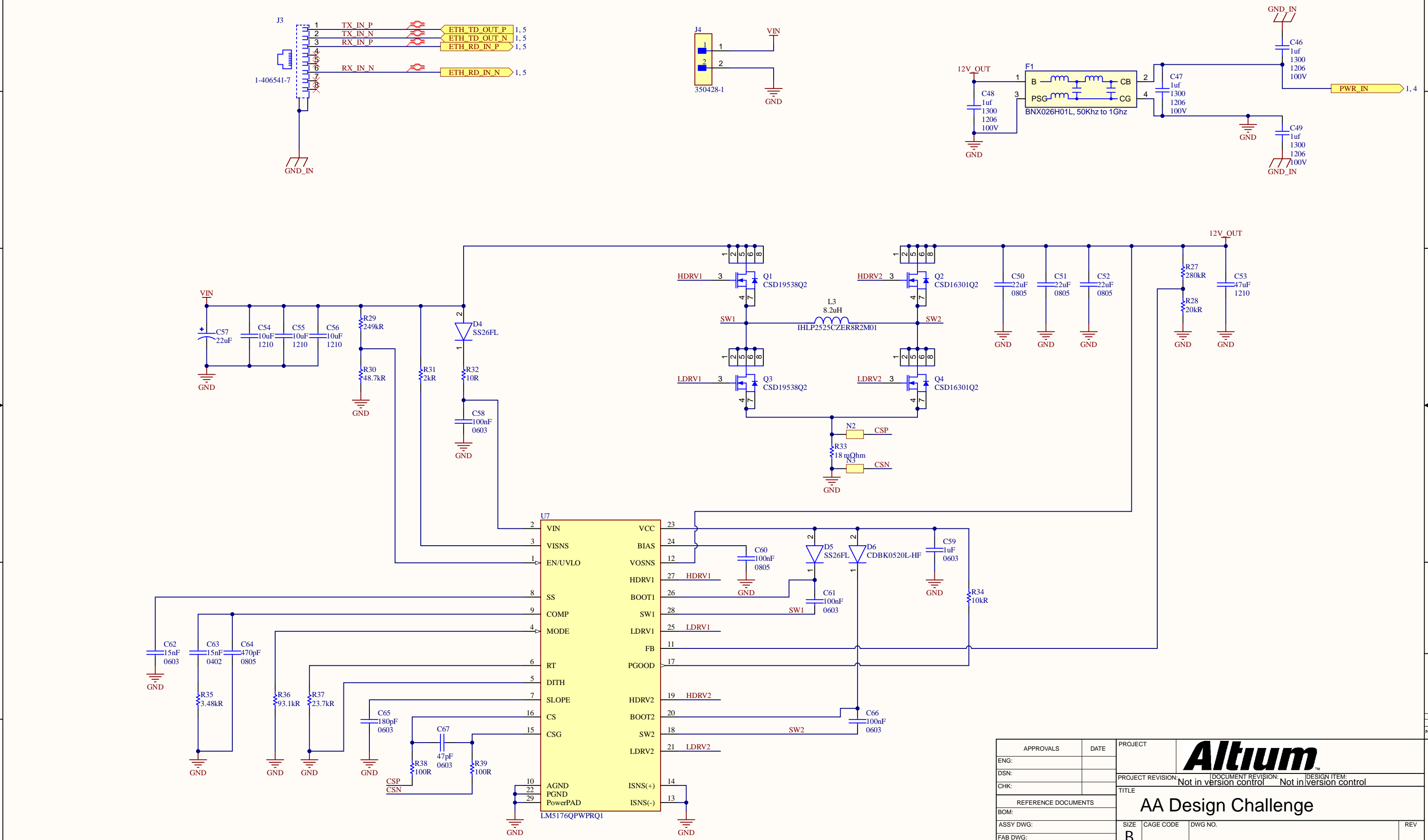
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Block Diagram



APPROVALS	DATE	PROJECT	Altium	
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DSN:		Not in version control Not in version control		
CHK:		TITLE		
REFERENCE DOCUMENTS		AA Design Challenge		
BOM:		SIZE	CAGE CODE	DWG NO.
ASSY DWG:		B		REV
FAB DWG:				
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		Block Diagram.SchDoc		

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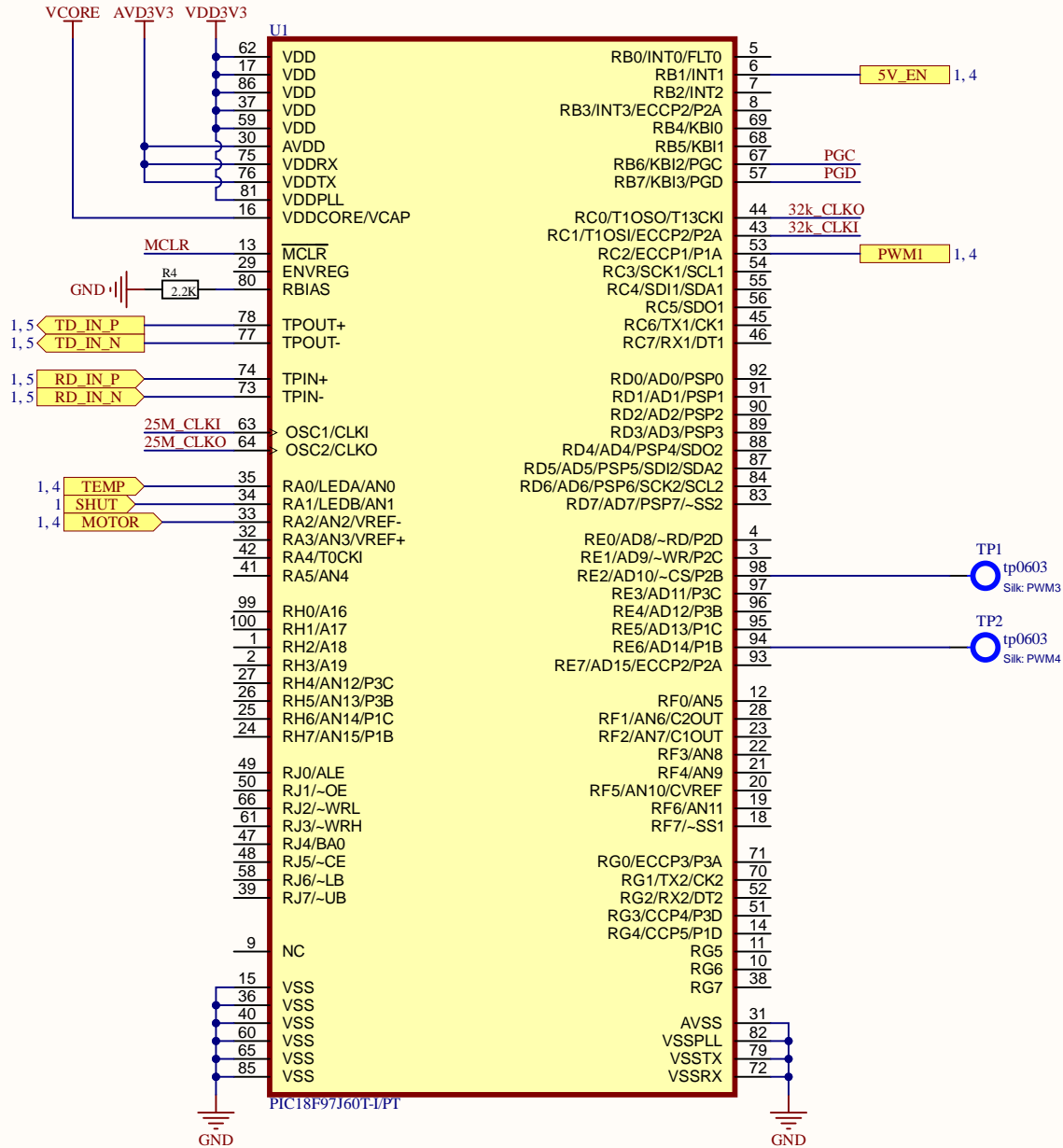
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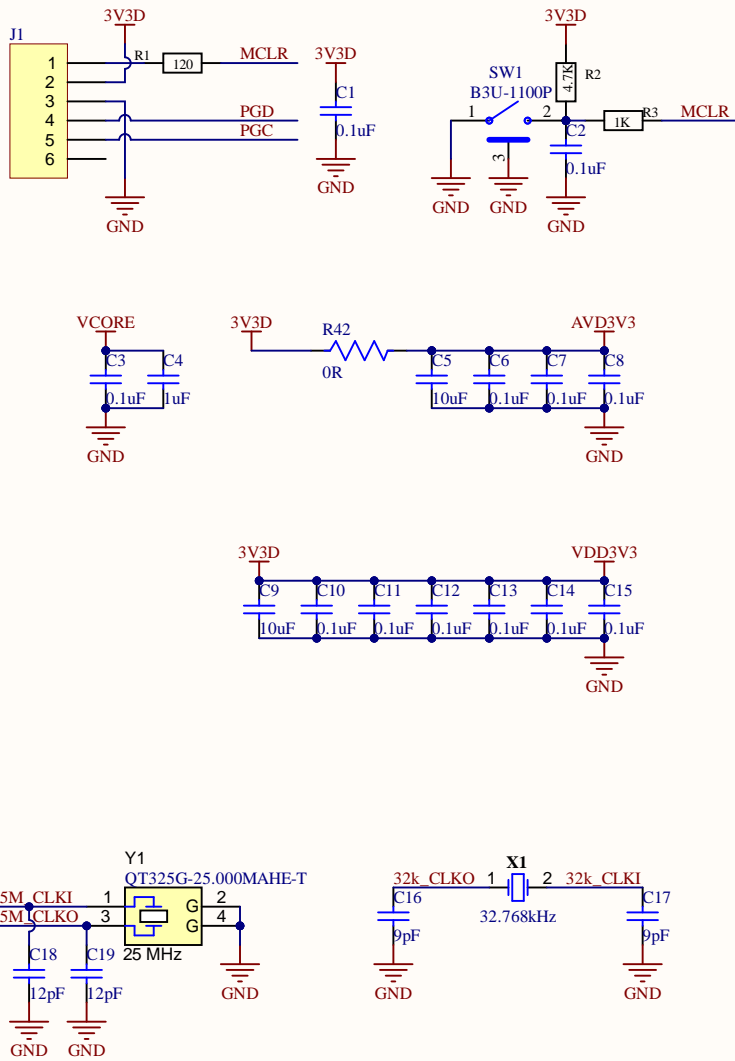
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PIC Microcontroller



ICD Connector

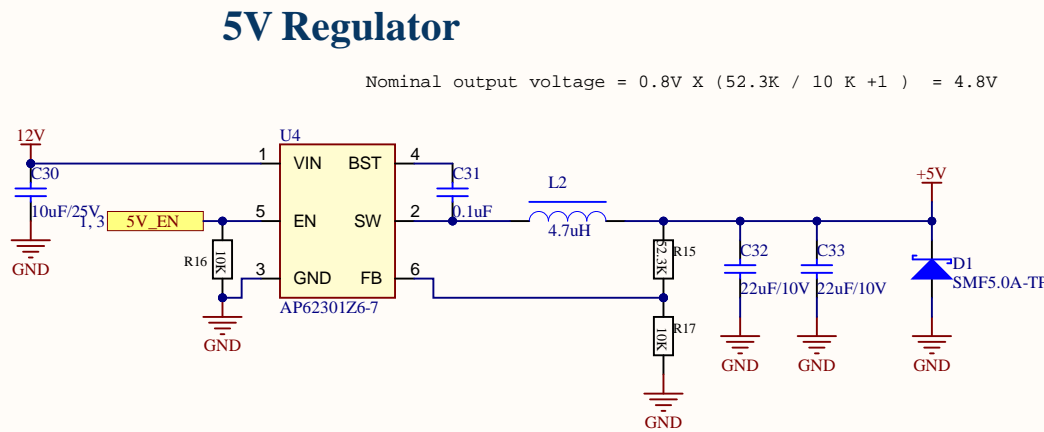
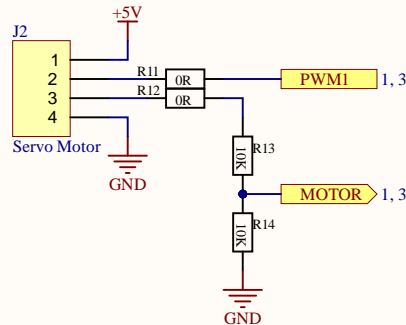
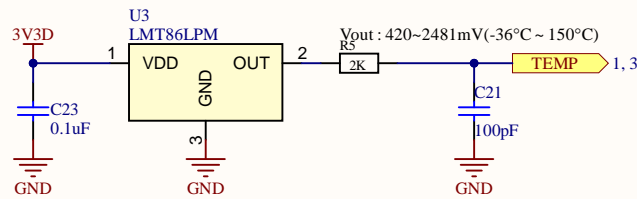
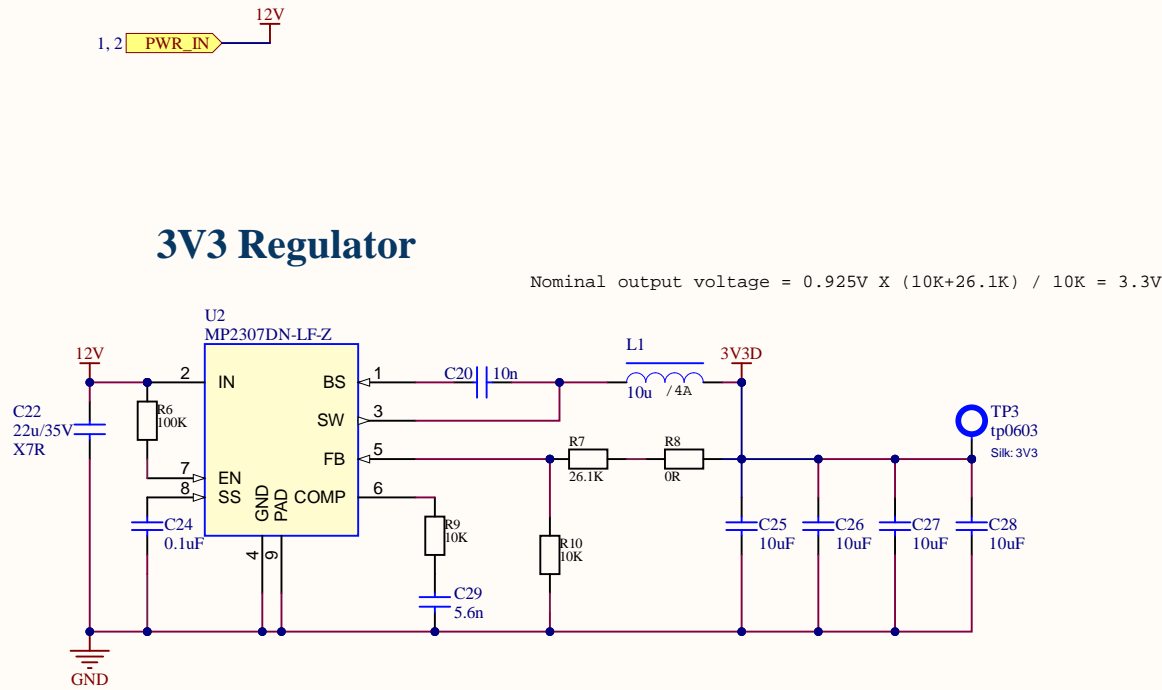



Manufacturer Part Number

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PCB DWG:	SCALE:	FILE NAME	PIC Microcontroller.SchDoc	SHEET	3 OF 5

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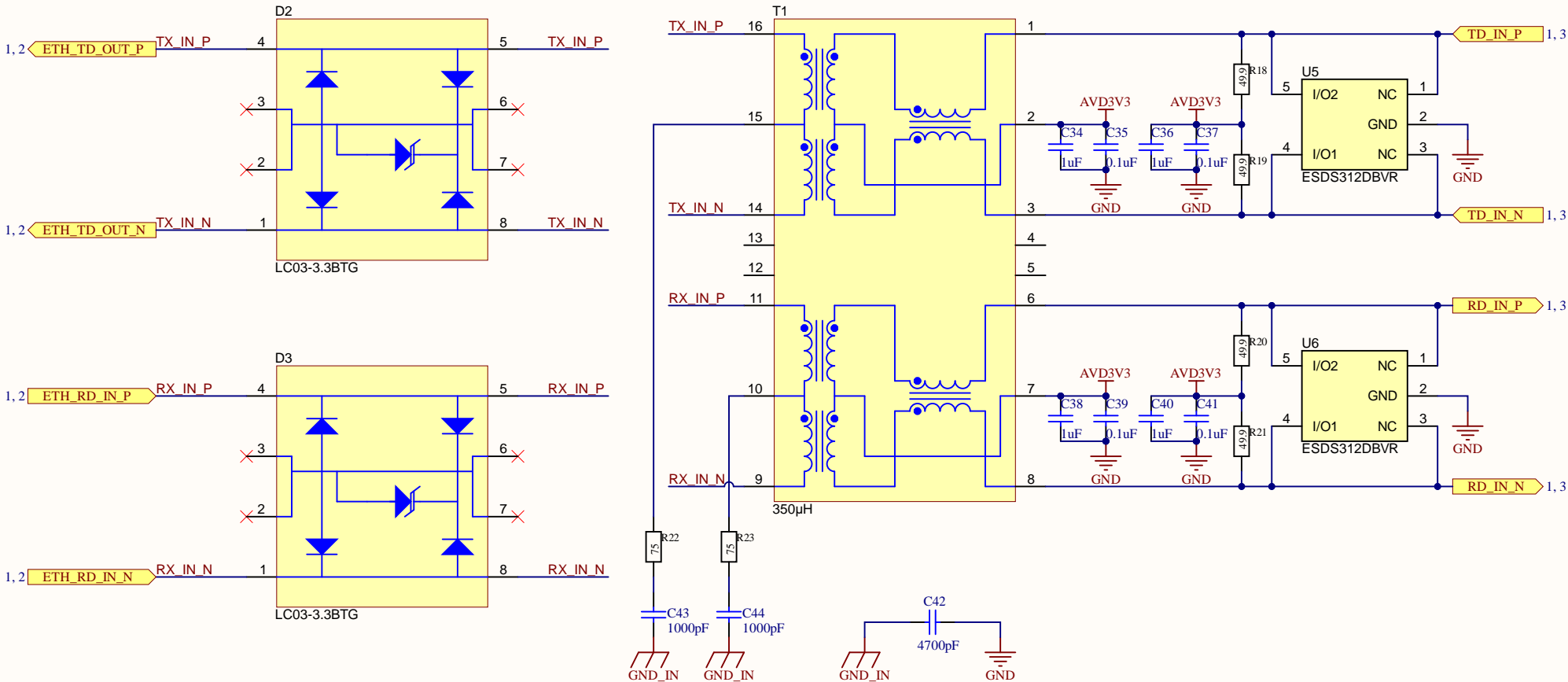


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				Power_Sensors.SchDoc				4 OF 5	

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Bob Smith Termination



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