Lab 8 Deliverables

## Average accuracy (with units in cm) = -.295

True positio	n Measured Position	Error Xti - Xmi	
Xti	<del>Xmi</del>		
0.5	0.562	-0.062	
1.0	1.079	079	
1.5	1.582	-0.082	
2.0	1.981	0.019	
1.75	1.806	-0.56	



Code:

```
int
delay
;
             SYSCTL_RCGC2_R |= 0x10; //enable port E clock
             while((SYSCTL_PRGPIO_R&0x10) == 0){};
             GPIO_PORTE_AMSEL_R |= 0x04;
                                                        //
             GPIO_PORTE_DEN_R &= 0xFB; //clear bits to enable analog
             SYSCTL_RCGCADC_R |= 0x01; //enable ADC clock
             delay = SYSCTL_RCGCADC_R; // extra time to stabilize
        delay = SYSCTL_RCGCADC_R; // extra time to stabilize
        delay = SYSCTL_RCGCADC_R;  // extra time to stabilize
        delay = SYSCTL_RCGCADC_R;
             ADC0_PC_R \mid= 0x01;
                                                              //Set ADC
       conversion speed to 125kHz
             ADCO_SSPRI_R = 0x0123; //set sequencer priority
             ADC0_ACTSS_R &= (~0x123);
                                                        //zero bit 3 to disable
       selected sequence
             ADCO_EMUX_R &= ~0xF000; //set software start trigger event
             ADC0_SSMUX3_R \mid= 0x01;
                                            //since we are using PE2
       (channel 1) write 1 to [3:0]
             ADC0 SSCTL3 R = 0 \times 0006;
                                                        //this line sets [3:0]
       to "0110"
             ADC0_IM_R &= (~0x0008);
                                                              //clear bit 3 to
       disable interrupts
```

//set bit 3 to enable selected

ADCO\_ACTSS\_R  $\mid$ = 0x0008;

sequencer 3

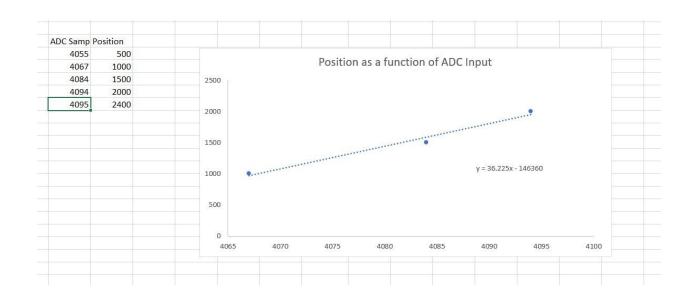
```
ADCO_SAC_R = 0x04; // 16) enable hardware oversampling; A N means 2^N (16 here) samples are averaged; 0 <= N <= 6
```

```
uint32
_t
ADC_In
(void)
{
                  //initiate capture
                  //RIS (SAR)
                  //read captured value from fifo buffer
                  //clear ris flag using icr reg
                  //return
                  //*****check this function output- data should be 12 bits.
          uint32_t local_data;
                  ADC0_PSSI_R = 0 \times 0008;
                  while ((ADC0_RIS_R & 0x08) == 0) {
                  };
                                 //wait for conversion
                  local_data = (ADC0_SSFIF03_R & 0xFFF);
                  //retrieve data from fifo buffer
                  ADC0_ISC_R = 0 \times 0008;
                  return (local_data);
          }
```

```
int
main(void){
```

```
DisableInterrupts();
       TExaS_Init();
       PortF_Init();
       SysTick_Init();
       ADC_Init();
       ST7735_InitR(INITR_REDTAB);
       EnableInterrupts();
       while (1){
              while (ADCStatus == 1){
              Data = ADCMail;
              ADCStatus = 0;
              Position = Convert(Data);
              ST7735_SetCursor(0,0);
              LCD_OutFix(Position);
              ST7735_OutString("cm");
              PF2 ^= 0x02;
              }
       }
}
```

//initialize the PLL, LCD



Position	Analog input	ADC sample	Correct Fixed-point	Measured Fixed-point Outpu
0.50 cm	0.584	538	50	49
1.00cm	1.024	1145	1000	470
1.50 cm	1.542	1534	1500	942
1.75 cm	1.742	3102	1750	1703
2.0 cm	2.102	3532	2000	1985

