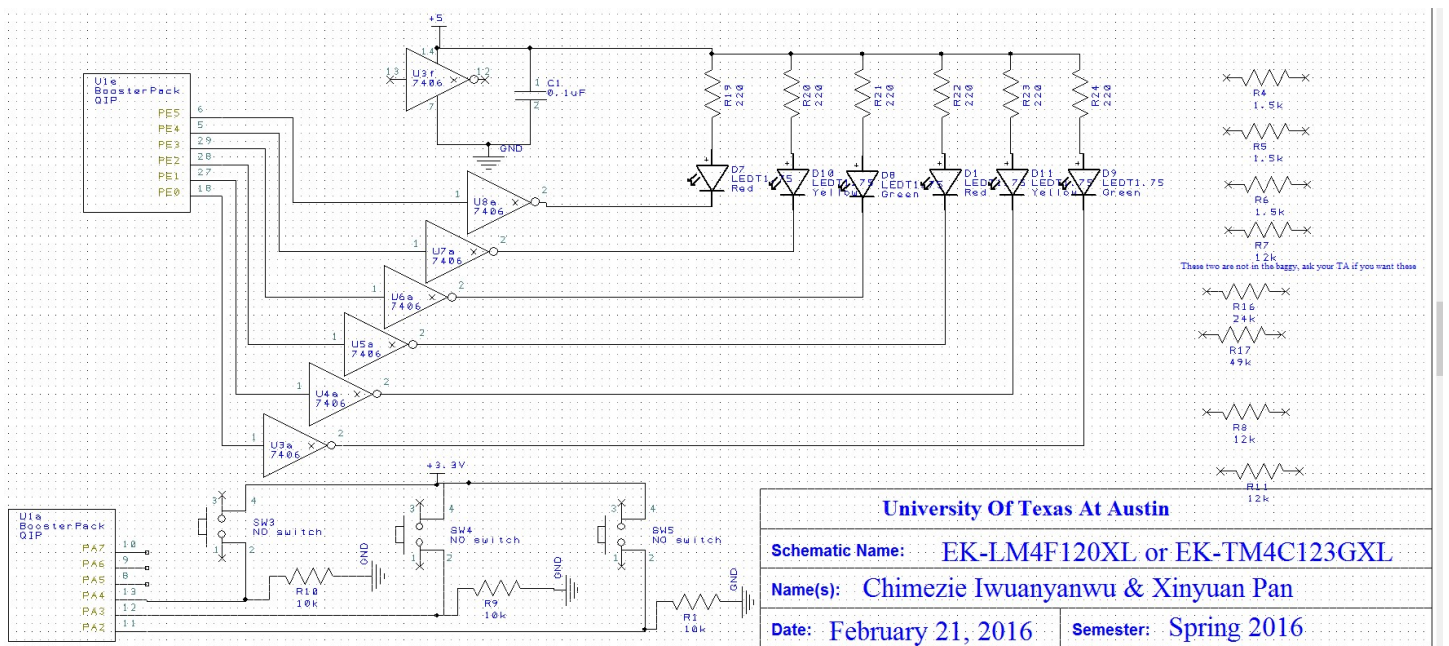


Logic Analyzer



Circuit Diagram



Circuit Diagram

