

# Sai Paresh Karyekar

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## EDUCATION

<b>Georgia Institute of Technology, Atlanta, GA</b> <i>Master of Science in Electrical and Computer Engineering (ML)</i>	<b>May 2025</b> GPA: 3.9/4
<b>Veermata Jijabai Technological Institute (VJTI), India</b> <i>Bachelor of Technology in Electronics &amp; Telecommunication Engineering</i>	<b>June 2023</b> CGPA: 9.62/10

## RELEVANT COURSEWORK

Generative and Geometric Deep Learning, Data and Visual Analytics, Machine Learning, Technology Entrepreneurship (**Teaching Assistant**)

## EXPERIENCE

<b>NVIDIA</b>   <i>Developer Marketing Intern (Product Marketing)</i>	June 2024 - Aug 2024
<ul style="list-style-type: none"><li>Engineered a Generative AI-powered RAG tool for technical blog generation using Llama3 model on NVIDIA NIMs, accelerating content creation and demonstrating expertise in deep learning and AI/ML techniques.</li><li>Optimized outputs through advanced prompt engineering, producing detailed technical blog outlines, comprehensive content, and effective social media posts while ensuring 90% adoption within the team.</li><li>Curated developer resources for the contest with LlamaIndex, thereby improving support for developers.</li></ul>	
<b>Georgia Institute of Technology, Atlanta</b>   <i>Student Researcher</i> 🔗	Aug 2023 - Dec 2023
<ul style="list-style-type: none"><li>Applied statistical techniques (ANOVA, OLS regression) and machine learning models to analyze a large-scale dataset of student performance in a microelectronics course.</li><li>Developed predictive models using scikit-learn and performed feature importance analysis, resulting in a 5% improvement in course success rates.</li></ul>	

## PROJECTS

<b>Enhancing Mathematical Reasoning in Small Language Models (SLMs)</b> 🔗	Sept 2024 - Dec 2024
<ul style="list-style-type: none"><li>Optimized the T5-small transformer model (60M parameters) for math reasoning on the GSM8K dataset using techniques such as Low-Rank Adaptation (LoRA), Chain-of-Thought (CoT) prompting, and full fine-tuning.</li><li>Compared CoT-based methods to a quantized T5-base model, demonstrating 5% lower memory usage while maintaining comparable performance.</li></ul>	
<b>ApplyCation: Automated Job Application Platform</b> 🔗	Oct 2024
<ul style="list-style-type: none"><li>Developed an automated job application system leveraging Selenium and dynamic script generation via Claude API, streamlining job applications on various portals and reducing the time per application by 50%.</li><li>Designed a responsive Streamlit-based frontend for managing user settings, resume uploads, and job tracking, improving user experience and accessibility.</li></ul>	

## SKILLS

<b>Languages</b>	Python, C++, SQL, R, MATLAB
<b>Tools</b>	Git, Linux, GCP, AWS, Docker
<b>Frameworks</b>	Tensorflow, PyTorch, Scikit-learn, OpenCV, LangChain, LlamaIndex, Selenium, Streamlit

## EXTRA-CURRICULARS

- As a member of the Data Science club at GT, contributed to the Workout of the Day prediction project by estimating workout durations and comparing performance with CrossFit enthusiasts.
- Served as the Social Chair of the Grad Society of Women Engineers (Grad SWE).