

ICS 233, Term 072

Computer Architecture & Assembly Language

HW# 1

- Q.1.** Exercises 1.1 through 1.28.
- Q.2.** Briefly describe the main functionality of the program counter register (PC), the instruction register (IR), and the fetch-execute process in a computer.
- Q.3.** Describe two advantages for programming in assembly and two advantages for programming in a high-level language.
- Q.4.** Given a magnetic disk with the following properties: Rotation speed = 7200 RPM (rotations per minute), Average seek = 8 ms, Sector = 512 bytes, Track = 200 sectors. Calculate the following:
- (i) Time of one rotation (in milliseconds).
 - (ii) Average time to access a block of 32 consecutive sectors.
- Q.5.** Assume you are in a company that will market a certain IC chip. The fixed costs, including R&D, fabrication and equipments, and so on, add up to \$500,000. The cost per wafer is \$6000, and each wafer can be diced into 1500 dies. The die yield is 50%. Finally, the dies are packaged and tested, with a cost of \$10 per chip. The test yield is 90%; only those that pass the test will be sold to customers. If the retail price is 40% more than the cost, at least how many chips have to be sold to break even?
- Q.6.** Represent the following numbers in binary and hexadecimal. Use as many bits as needed, and approximate the fraction up to 3 digits:
- (i) 250.375
 - (ii) 4444.4
- Q.7.** Express the following numbers in sign-magnitude, 1's complement, and 2's complement notations, assuming 8-bit representation:
- (i) -119
 - (ii) -55
- Q.8.** Show how the decimal integer -120 would be represented in 2's complement notation using:
- (i) 8 bits
 - (ii) 16 bits

Q.9. Perform the following operations assuming 8-bit 2's complement representation of numbers. Indicate in your answer when an overflow occurs:

(i) $01010011 + 11111111$

(ii) $10110000 - 01110110$

(iii) $AF + FF$

(iv) $AF - 70$

Q.10. A microcontroller uses 8-bit registers. Give the following in both binary and decimal:

(i) The maximum unsigned number that can be stored.

(ii) The smallest (negative) number and the largest (positive) number that can be stored using the sign-magnitude notation.

(iii) The smallest (negative) number and the largest (positive) number that can be stored using the 2's complement notation.

Q.11. If you type the phrase ICS233 on your keyboard, what is the binary sequence sent to the computer using 8-bit ASCII with the 8th bit being an even parity bit.

Q.12. Suppose that a byte contains the ASCII code of a decimal digit; that is '0' to '9'. What hex number should be subtracted from the byte to convert it to the numerical form of the characters?