JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

KAKINADA - 533 003, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT / GRADE SHEET

CMM. No.: K Serial No.: Name:

00368690

YALAKALA SAI KIRAN

Bachelor of Technology ELECTRICAL AND ELECTRONICS ENGINEERING 572996879736

Aadhar No.:

Name of the College : VISHNU INST OF TECHNOLOGY

Backwal

Name & Year of Final Exam: B.Tech May-2018

Firet Class

	Hall Ticket No. 14PA1A0290		Year of A	Year of Admission 2014 - 2015 Class Awarded : First Cla								
	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS		COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS	
I YEAR												
	1 MATHEMATICS-II(MM) 2 ENGG, DRAWING 3 PROFESS, ETHICS& HUMAN VAL. 4 ENGLISH-I 5 MATHEMATICS-I 6 ENGG,PHYSICS 7 ENGG,WORK SHOP & IT WORKSHOP 8 ENGLISH COMM.SKILLS LAB-I 9 ENGG, PHYSICS LABORATORY	23 28 25 25 17 14 20 22 16	42 45 42 56 24 38 39 39 39	65 73 67 81 41 52 59 61 54	3 3 3 3 3 3 2 2		1 MATHEMATICS-III 2 ENGLISH-II 3 ENGG. CHEMISTRY 4 C PROGRAMMING 5 ENGG. MECHANICS 6 ELECTRICAL CIRCUITS ANALYSIS-I 7 ENGINEERING CHEMISTRY LAB 8 C PROGRAMMING LAB 9 ENGLISH-COMM. SKILLS LAB-II	16 20 12 12 15 12 15 10 22	28 24 28 38 30 32 26 40 46	44 44 40 50 45 44 41 50 68	3 3 3 3 3 2 2 2	
II YEAR												
	1 THERMAL AND HYDRO PRIME MOVERS 2 ELECTRICAL CIRCUIT ANALYSIS-II 3 BASIC ELECTRONICS AND DEVICES 4 ELECTRO MAGNETIC FIELDS 5 ELECTRICAL MACHINES-I 6 COMPLEX VAR. &STAT. METHODS 7 THERMAL AND HYDRO LAB 8 ELECTRICAL CIRCUITS LAB	21 15 4 10 14 17 16	30 41 52 29 24 39	50 45 45 62 43 41 55 46	3 3 3 3 3 2 2	STREET, STREET,	1 ENVIRONMENTAL STUDIES 2 SWITCHING THEORY AND LOGIC DESIGN 3 PULSE & DIGITAL CIRCUITS 4 POWER SYSTEMS-I 5 ELECTRICAL MACHINES-II 6 CONTROL SYSTEMS 7 ELECTRICAL MACHINES -I LAB 8 ELECTRONIC DEVICES & CIRCUITS LAB	24 13 25 23 17 19 12 14	36 27 41 52 31 31 39 30	60 40 66 75 48 50 51 44	3 3 3 3 3 2 2 2	
-	III YEAR											
	1 MEFA 2 ELECTRICAL MEASUREMENTS 3 POWER SYSTEMS-II 4 ELECTRICAL MACHINES-III 5 POWER ELECTRONICS 6 LINEAR & DIGITAL IC APPLICATIONS 7 IPR & PATENTS 8 ELECTRICAL MACHINES-II LAB 9 CONTROL SYSTEMS LAB	22 22 25 24 18 25 21 23 20	34 44 28 25 32 38 41 36 42	56 66 53 49 50 63 62 59	3 3 3 3 3 2 2 2		1 SWITCHGEAR AND PROTECTION 2 UTILIZATION OF ELECTRICAL ENERGY 3 POWER SYSTEM ANALYSIS 4 MANAGEMENT SCIENCE 5 POWER SEMICONDUCTOR DRIVES 6 MICROPROCES.& MICROCONTROL. 7 POWER ELECTRONICS LAB 8 ELECTRICAL MEASUREMENTS LAB	17 26 20 22 14 16 20 19	26 24 43 47 53 24 40 47	43 50 63 69 67 40 60 66	3 3 3 3 3 2 2 2	
	IV YEAR											
	1 POWER SYSTEM OPERATION & CONTROL 2 RENEWABLE ENERGY SOURCES & SYS. 3 ELECTRICAL DISTRIBUTION SYSTEMS 4 ENERGY AUDIT CONSERVATION & MAN. 5 HVAC AND DC TRANSMISSION 6 ELECTRICAL SIMULATION LAB 7 POWER SYSTEMS LAB 8 MICROPROCESSORS & MICROCONT. LA	27 24 AG. 23 18 20 19	29 70 42 24 45 48 45 40	55 97 66 47 63 68 64 60	3 3 3 2 2 2 2		1 AI TECHNIQUES 2 DIGITAL CONTROL SYSTEMS 3 SPECIAL ELECTRICAL MACHINES 4 FLEXIBLE ALTER. CURRENT TRANS. SYS. 5 PROJECT WORK	22 20 21 23 56	38 39 33 37 131	60 59 54 60 187	3 3 3 9	
-	THE RESIDENCE OF THE PROPERTY	The state of the s	A STATE OF THE PARTY OF THE PAR									

180

Number of Credits registered for Aggregate Marks Secured for best

180 Credits

3718 out of 6075 (61.20 %)

Date of Declaration of Result: (See overleaf for Instructions)

June 2018

3/8/2018

(*Courses registered but not counted for calculation of aggregate)

* Medium of Instruction and Examinations in English

N. Hohan Ro

CONTROLLER OF EXAMINATIONS