|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
|  | Comparisons of Canonical Huffman Machine and Synthesized on Vertex - 5 FPGA | | | | | |
| Huffman Buffer | Frequency Count | Sorting | Bit Mapping | Cote Table | Top Module (Full Work) |
| Number of Slice Register | 40 | 385 | 421 | 159 | 223 | 1439 |
| Number of Slice LUTs | 120 | 554 | 2021 | 178 | 309 | 3217 |
| Number of Occupied Slice | 61 | 206 | 762 | 83 | 216 | 1243 |
| Delay (ns) | 3.701 | 4.690 | 8.026 | 3.879 | 5.192 | 8.016 |
| Power (W) | 3.294 | 3.298 | 3.301 | 3.298 | 3.298 | 3.316 |

**Comparisons of Canonical Huffman Machine**

