



KORADA SAIMADHAN
B.Tech. - Electronics & Communication Engineering
Ph: +91-9533107089
Email: koradasaimadhan@gmail.com
Srikakulam, Andhra Pradesh, India - 532421



EDUCATION

Andhra University College of Engineering, Visakhapatnam B.Tech. - Electronics & Communication Engineering CGPA: 7.65 / 10	2022 - 2026
Srichaitanya junior college, Srikakulam 12 th BIEAP Percentage: 74.80 / 100	2022
Kranthi Vidyaniketan school, Srikakulam 10 th BSEAP CGPA: 9.20 / 10	2020

INTERNSHIPS

Hippo cloud technologies PVT LTD IT / Computers - Software Software Trainee Key Skills: HTML CSS Javascript Node.js NodeJS MySQL As a Software Trainee in Web Development, I was trained and worked on full stack web technologies, contributing to the development of interactive web applications. I gained hands-on experience in both frontend and backend technologies, collaborating on real-time projects under the guidance of experienced developers.	01 May, 2025 - 27 Jul, 2025
SkillDzire IT / Computers - Software Key Skills: VLSI Verilog Completed a hands-on internship in VLSI (Very Large Scale Integration), focusing on digital circuit design, simulation, and verification. This internship provided practical exposure to the semiconductor design flow, logic design principles, and hardware description languages.	05 May, 2025 - 20 Jun, 2025
Bharat sanchar nigam limited Telecom Key Skills: Telecommunication Communications Completed an industrial training internship at BSNL, where I gained hands-on exposure to core telecommunications infrastructure and networking technologies. The training focused on understanding the working principles and architecture of various telecom systems used by BSNL for providing broadband and mobile services across India.	17 May, 2024 - 05 Jun, 2024

PROJECTS

PAM-using-verilog Key Skills: Verilog VLSI Project Link: https://saimadhan9533.github.io/PAM-using-verilog/ Implemented a Pulse Amplitude Modulation (PAM) system using Verilog HDL to digitally generate and simulate PAM signals. The design encodes input binary data into amplitude levels and was verified through waveform simulation.
Floating-point-unit Key Skills: VLSI Verilog Project Link: https://saimadhan9533.github.io/Floating-point-unit/ Designed a 32-bit IEEE 754-compliant Floating Point Unit (FPU) using Verilog HDL to perform addition, subtraction, multiplication, and division with support for normalization, rounding, and exception handling.
Portfolio Key Skills: HTML CSS Javascript Project Link: https://saimadhan9533.github.io/portfolio/ Designed and developed a responsive personal portfolio website using HTML, CSS, and JavaScript to showcase projects, skills, and achievements with an engaging user interface
Arthematic-Unit

Key Skills: Verilog VLSI

Project Link: <https://saimadhan9533.github.io/Arthematic-Unit/>

Designed an Arithmetic Unit using Verilog HDL to perform basic operations such as addition, subtraction, multiplication, and division on binary inputs. The unit was modular, efficient, and simulated successfully for various test cases

CO-CURRICULAR ACTIVITIES

- Editing

PERSONAL INTERESTS / HOBBIES

- Playing Cricket,Browsing Internet,Reading Mythology,Photography/Editing

IMs

- WhatsApp - 9533107089

PERSONAL DETAILS

Gender: Male

Marital Status: Single

Current Address: Kothapeta, Madapam, Srikakulam, Andhra Pradesh, India - 532421

Email: koradasaimadhan@gmail.com

Date of Birth: 21 Aug, 2005

Known Languages: Telugu,English,Hindi

Phone Number: +91-9533107089