

## Assignment - 1

1. What are Device driver?
2. Difference between general purpose system vs embedded systems
3. How can hardware understand the code we write in embedded systems (C file to .exe file)
4. Difference between RTOS & GPOS

① What are device driver?

Definition:-

Device drivers are software programs that act as intermediaries b/w hardware device and the operating system.

Importance of device drivers

① Hardware compatibility:-

Device drivers ensure that hardware devices are compatible with the operating system. Without the appropriate driver, a device may not function correctly (or) may not be recognized by the OS.

② Optimized performance:-

Drivers are designed to optimize the performance of hardware devices. They can unlock advanced features improve efficiency.

③ Stability and Reliability:-

Well designed drivers contribute to system stability by preventing conflicts and errors b/w different hardware components.

④ Security:-

Some drivers include security features protect against vulnerabilities.



## How device Drivers work?

- ① Initialization:- when the OS starts it loads essential drivers into memory. These drivers include those required for system components, such as the motherboard and storage devices.
- ② Dynamic loading:- Additional drivers are loaded dynamically as needed when new hardware devices are detected. This process is known as plug and play.
- ③ communication:- The OS communicates with hardware devices through the drivers when a user interacts with a device, the OS sends commands and data to the corresponding driver.
- ④ translation:- The driver translates these commands and data into a format that the hardware understands. It may involve converting high-level OS commands into low-level hardware instructions.
- ⑤ Data transfer:- Device drivers manage the flow of data between the OS and the hardware.
- ⑥ Error handling:- Device drivers handle errors and exceptions that may occur during device operation.
- ⑦ User interface:- Some drivers provide user interfaces (or) configuration tools that allow users to customize device settings and behaviour.

## Types of Device Drivers

- ① Kernel mode drivers
- ② User mode drivers
- ③ Filter drivers
- ④ Virtual device drivers.

Conclusion:- Device drivers are indispensable components of modern computing systems. They enable the seamless interaction b/w OS and a wide range of hardware devices, ensuring compatibility, performance optimization and system stability. Understanding the role of drivers is crucial for troubleshooting hardware issues and maintaining a smoothly functioning computer system.



② difference b/w General purpose slm and embedded slm?

① General purpose slm

Def:- General purpose slm like a typical personal computer is designed to perform a wide range of tasks and can run various software applications. It is flexible and adaptable with a general operating slm like windows, macos, or linux.

② Embedded slm

Def:- Embedded slm is designed for a specific function or set of functions. It's often a part of a large slm or product, such as a microwave oven, automotive control slm, or a medical device. Embedded slms have limited computing resources, run specialized software and are optimized for reliability and efficiency.

Key Differences

General purpose slm's	embedded system's
① General purpose slm's serve a broad range of applications	① embedded slm's have specific dedicated purpose.
② These slm's run a variety of software applications	② These slm's use specialized often proprietary software.
③ These slm's use standard hardware components	③ These slm's often have custom hardware tailored to their specific task.
④ These slms are flexible and can adapt to different tasks.	④ These slms are fixed in their function.
⑤ These slms typically run full-featured OS's.	⑤ These slm's may use real time operating slm's (RTOS) or custom firmware.
⑥ These slm's have ample computing resources.	⑥ These slm's have limited resources to meet their specific requirements.
⑦ These slm's often have a graphical user interface (GUI) for user interaction.	⑦ These slm's may have simple interfaces or none at all.



## Conclusion :-

General purpose sm's are versatile and capable of running various applications while embedded sm's are purpose-built for specific tasks and operates with constraints in terms of resources and functionality.

③ How can hardware understand the codes that we write in Embedded sm? (.c File to .exe File)

C File is first go through the pre-processor, then compiler compiles it to assembler and creates object file (main.o) then linker link the main.o with required header objects and libraries and creates a executable file (program.exe)

In embedded sm's hardware understands the codes you write through a combination of components and a microcontroller / microprocessor.

Here's a simplified overview of how this works:-

### ① Microcontroller / microprocessor

embedded sm's typically have a microcontroller (or) microprocessor at their core. These are specialized chips designed to execute instructions. They have a CPU and various peripherals.

### ② Machine code :-

When you write code for an embedded sm's it is usually in a high-level programming language like C (or) C++ you use a compiler to convert this high level code into machine code that the microcontroller can understand.

### ③ Memory :-

The machine code is stored in the memory of the ES. This memory can be divided into various sections, including program memory and data memory.

### ④ Instruction Fetch and Execution :-

The CPU of the microcontroller fetches instructions from the program memory one by one. These instructions are in the form of binary code which corresponds to specific operations.



### ⑤ Peripheral interfaces :-

The microcontroller has interfaces that allow it to interact with external hardware devices.

### ⑥ Interrupts :-

ES's often use interrupts to handle asynchronous events when an interrupt occurs, the microcontroller can pause its current task, execute a predefined interrupt service routine (ISR) and then return to the main program.

### ⑦ clock and timing :-

The microcontroller relies on an internal clock (or) oscillator to synchronize its operations and maintain precise timing.

### ⑧ compiler optimization :-

compilers For ES's often provide optimization options to generate efficient code, taking into account that limited resources available in these sim's.

### ⑨ Hardware Abstraction layers (HALs)

In some cases developers may use hardware abstraction layers (or) libraries provided by the microcontroller manufacturer to simplify interaction with hardware components. These libraries provide higher level functions to control hardware peripherals.

### ④ what is the difference b/w RTOS & CPPOS

RTOS	CPPOS
Real time operating system	General purpose operating system
① predictable behaviour	① there is no predictable behaviour
② works under worst case assumption	② optimizes for Avg case.
③ no large memory	③ large memory
④ priority inversion major issue	④ priority inversion unnoticed
⑤ dedicated to single network	⑤ multi user environment
⑥ scalable	⑥ non-scalable.



<p>⑦ Time sensitive</p> <p>⑧ Flat memory model</p> <p>⑨ unfair scheduling</p> <p>⑩ scheduling based on priority</p> <p>⑪ kernel is pre-emptive</p> <p>⑫ RTOS has got bounded latencies</p> <p>⑬ light weight and small in size</p> <p>⑭ RTOS is designed for applications that require precise and deterministic timing often in e-s's, robotics, and control s/m's. It ensures that tasks are executed within specified time constraints</p>	<p>① Time insensitive</p> <p>② protected memory model</p> <p>③ Fair scheduling</p> <p>④ adjusted dynamically</p> <p>⑤ kernel non-preemptive</p> <p>⑥ GPOS has unbounded latencies.</p> <p>⑦ heavy and large.</p> <p>⑧ GPOS also known as standard desktop (or) server operating s/m's are designed for general purpose computing tasks. They are versatile and cater to a wide range of applications.</p>
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### conclusion:-

The choice b/w RTOS and GPOS depends on the specific requirements of the s/m (or) application. If determinism and real-time constraints are essential, an RTOS is more suitable for general purpose computing tasks, a GPOS provides the versatility and feature set required.