

UNIT - III -

Q1)

Safe state & unsafe state

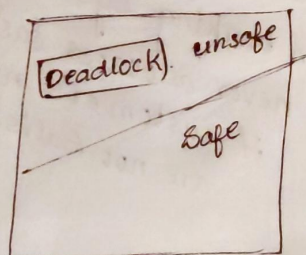
SAFE STATE :

- A state is said to be safe if system can allocate resources to each process (max) in some order.
- If there is a safe sequence then the system said to be in safe state. for processes $\{P_1, P_2, \dots, P_n\}$
- Safe state is not a deadlock state and deadlock state is an unsafe state.

UNSAFE STATE :

- It may lead to deadlock, this OS cannot prevent process from requesting a resources.

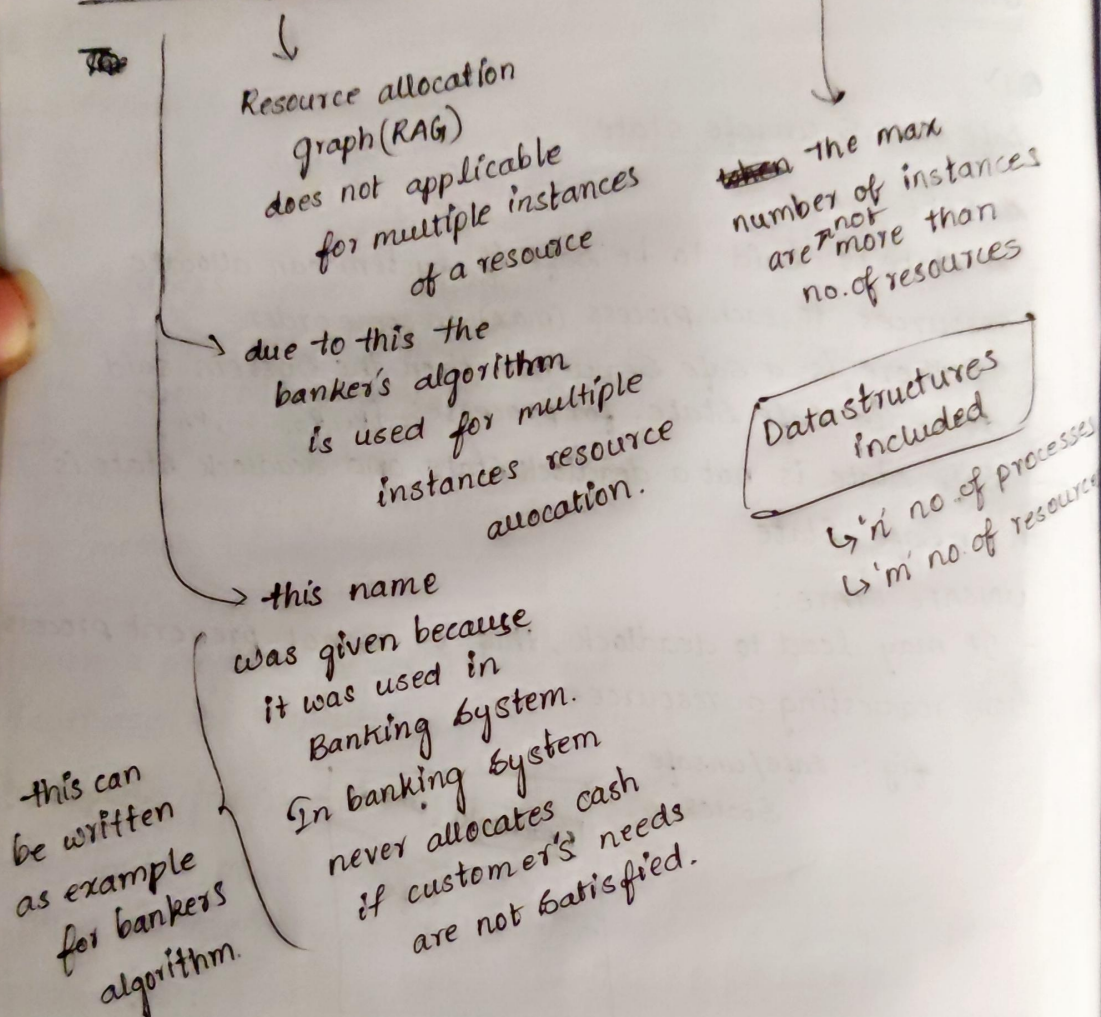
fig :- Safe/unsafe States.



Avoidance \Rightarrow ensure that the system will never enter Safe state.

Q2)

Banker's Algorithm



① Available:-

~~a vector length m indicates the~~

→ m - available no. of resources

if $available[j] = \{k\}$ then

'k' instances of R_j are available.

② Max:-

$m \times n \rightarrow$ determines the maximum demand of each processes.

if $max[i][j]$ equals k then P_i request for k instance of resource type R_j .

③ Allocation:-

$n \times m \rightarrow$ defines
the no. of resources
currently allocated
to each process.

if allocation $[i][j]$ equals to 'k'
then process P_i currently
allocated to Resource R_j .

④ Need:-

$n \times m$ -matrix defines
remaining resource need of
each process.

$$\text{Need}[i][j] \Rightarrow \text{max}[i] - \text{allocation}[i][j].$$

Q3) Deadlock detection.

If a system doesn't use any deadlock prevention or deadlock avoidance then deadlock may occur.

In this environment the system provides:

- an algorithm that examines whether deadlock occurred.
- an algorithm to rescue from the deadlock.

Deadlock Detection Algorithm.

work and finish are the vectors of length $m \times n$ respectively.

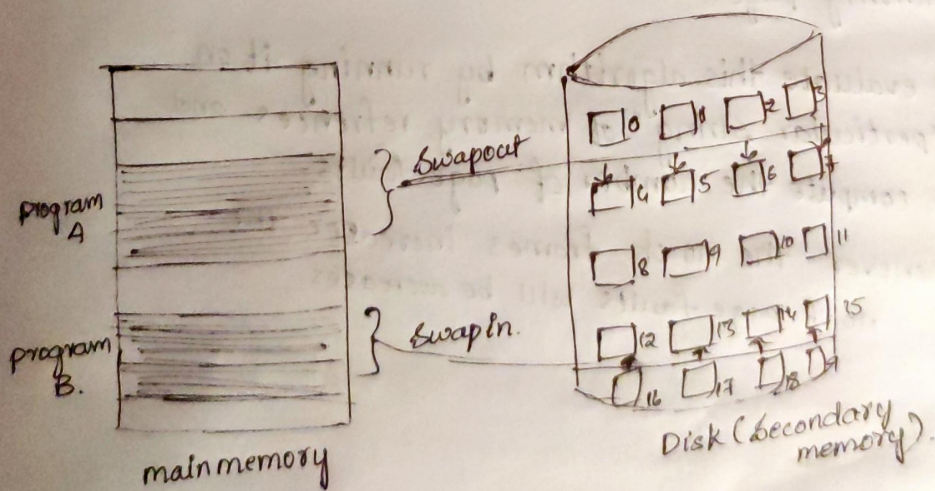
- ① Initialize work = available
for $i = 0, 1, 2, \dots, n-1$ if allocation $\neq 0$, then
finish[i] = false,
otherwise finish[i] = True.
- ② Find Index 'i' such that both
a. finish[i] == false
b. Request[i] \leq work
If no 'i' exists then go to step 4.
- ③ work = work + allocation[i]
finish[i] = True
Go to Step ②.
- ④ If finish[i] == false, for some i, $1 \leq i \leq n$
then system is deadlock state.
if finish[i] == True \rightarrow Safe state.

UNIT - IV :-

Q4) Demand Paging.

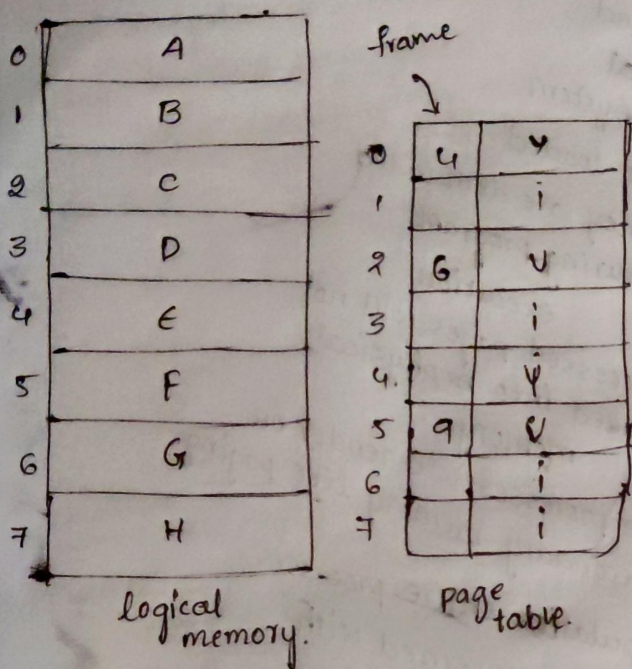
- commonly used in the virtual memory system.
- pages are loaded when they are demanded during program execution.
- not accessed pages will not be loaded into physical memory.
- the processes will reside on secondary memory like paging.
- Swapping manipulates entire processes, whereas paging is concerned with individual pages.

Fig:- demand paging. (transferring ~~disk~~ paged memory to contiguous disk space)



$$EAT = (1-P) \times ma + P \times \text{page fault} \times \text{time}.$$

fig ②. page table when some pages are not in the main memory.



Q5) page replacement algorithms:-

→ This algorithm helps to decide which pages are swapped out from the main memory for every incoming page.

→ we evaluate this algorithm by running it on a "particular string" of memory references and then compute the number of page faults.

→ whenever the no. of frames increases the no. of page faults will be decreases.

① FIFO Replacement algorithm

- It is one of the simplest page replacement algorithm.
- associates with each page when the page is brought into the main memory.
- In this type of algorithm the oldest page is replaced which is present in main memory for longest period of time.

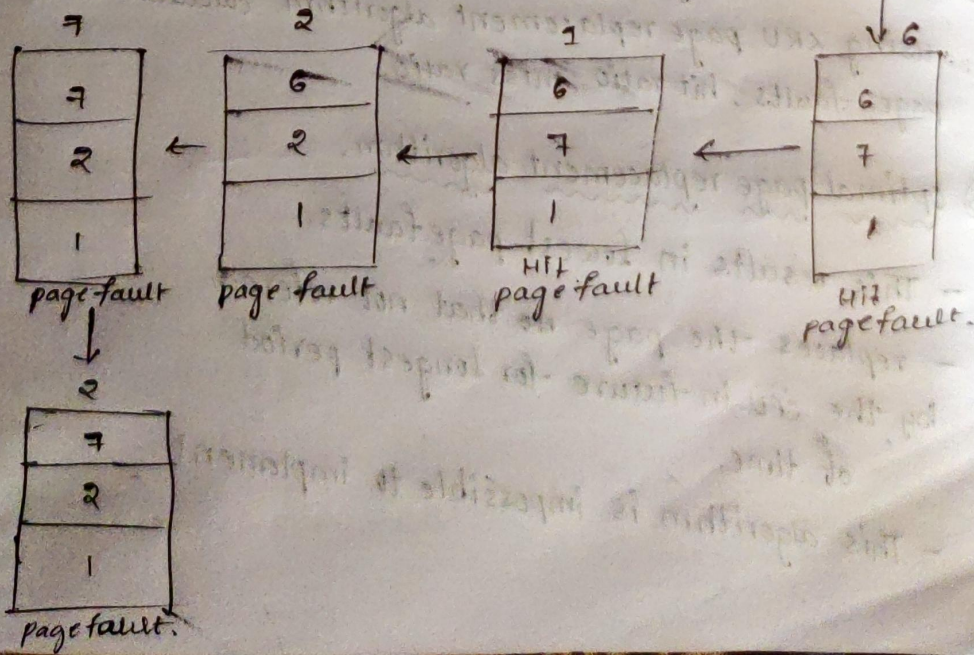
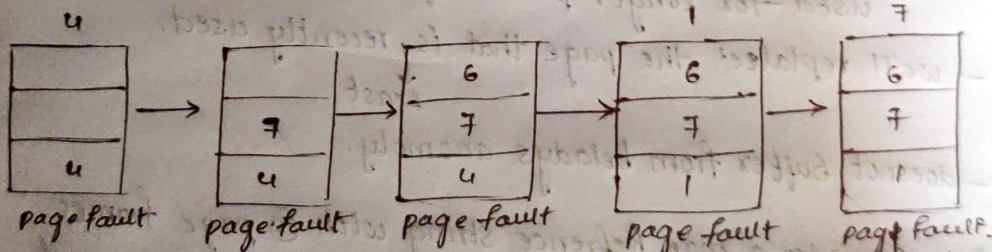
Ex:- consider a page reference string with ③ page frames using FIFO page replacement algorithm calculate, hit ratio, miss ratio.

Reference string

4, 7, 6, 1, 7, 6, 1, 2, 7, 2

601 assume that all page frames empty ^{main} memory divided ③ slots.

4, 7, 6, 1, 7, 6, 1, 2, 7, 2



total no. of page fault = 6.

total no. of page references = 10.

Calculate Hit Ratio.

Total no. of pages hits

$$= \text{total no. of page ref} - \text{total no. of page fault.}$$

$$= 10 - 6.$$

$$= 4.$$

$$\text{Hit ratio} \rightarrow \frac{\text{total no. of hits.}}{\text{total no. of page ref.}}$$

$$= \frac{4}{10} = 0.4 = 40\%.$$

② LRU page Replacement algorithm :-

- mainly works on the least recently used.
- In this the page referenced by CPU have not been used for longer period of time.
- we'll replace the page that is ^{least} recently used.
- doesnot suffer from belady's anomaly.

Ex:- Consider a page Reference string with 3 page frames using LRU page replacement algorithm. calculate no. of page faults, hit ratio miss ratio.

③ optimal page replacement algorithm.

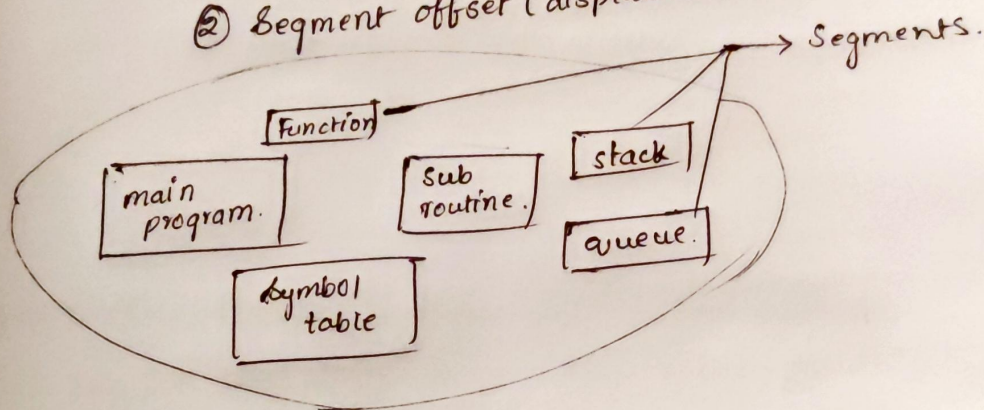
- This results in lowest page faults.
- replaces the page ~~no~~ that not referred by the CPU in future for longest period of time.
- This algorithm is impossible to implement.

Q67 Segmentation:-

- memory management technique that ~~used to~~ supports the programmers memory view.
 - each ~~big~~ logical address may have the collection of segments.
 - each segment contain segment name and segment length.
- logical address is divided into 2 parts.

① Segment number (s)

② Segment offset (displacement) (d)



Segment Hardware :-

- Segment are represented by tables.
- entry in the segment table consists of 2 types

1. Segment base:

Hold the starting physical address.

2. Segment limits:

Specifies the range length of the segment.