Saipraveen Vabbilisetty

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Work Authorization: F-1

Objective: Securing a Summer Internship -2017.

Education

Masters in Computer Science at University of Texas at Dallas   **GPA: 3.85/4.0**

Coursework: Advanced Computer Networks, Advanced Computer Architecture, Database Design, Big Data Management and Analytics,Algorithms analysis and Data Structures, Operating Systems..

Bachelor of Technology, Electrical and Electronics,   **May, 2015.**
Amrita Vishwa Vidyapeetham, Karnataka, India.   **GPA:3.63/4.0**

- Winner of Academic Excellence award for the years 2012-13&2013-14.
- Winner of "Karthik Kalaichelvan Memorial award" for academic excellence from 2011-2015.

Technical Skillss

Programming Languages: C, Java
Applications/Tools: Solid edge, P-Spice, MPLAB IDE, MATLAB, ETAS, ASCET.
Operating Systems: Windows, LINUX.
Databases and Tools: My SQL, HDFS, Hive, Pig and Hadoop Streaming, Map Reduce.
IDEs and Web Editors: JavaScript, CSS, JSON, API, Bootstrap, HTML, PHP.
Simulator: Gem5
Framework: MVC
Designing tools: UML

Work Experience

Associate Software Engineer, Robert Bosch Engineering Solutions, India.   **July 2015-July 2016.**

- Application Software platform based projects on Value added Functions for ESP.
- Developing and Testing Automatic Emergency Brake and Hill Descent Control Software.
- Mastery over Automotive Embedded Software such as ETAS, ASCET, ATT.
- Worked on reducing the high beam during night travel using Control Engineering, Optics, Solid state physics.

Implementing "***Fog Computing***" using the concept of Multi-threading. The main goals of this project is to exchange periodic updates which gives information about their queuing delays, responding to the requests from clients, fog to fog request offloading.   -**Advanced Computer Networks, December 2016.**

*"**Grace Degradation techniques**"* to handle component failures gracefully and continue to operate in reduced performance mode, by using multiple processors. A decision making mechanism to decide which processor should handle execution on the availability in times of failure.   - **Advanced Computer Architecture, November 2016.**

*"**Cache Design and Optimization**"* The objective of the project is to fine tune the cache according to different specifications and configurations like cache size, block size and associativity in order to obtain an optimized CPI.   - **Advanced Computer Architecture, November 2016.**

"Estimating Battery Reserve using Weather Forecasting and Optimization". A neural network predicated algorithm for sooth saying the wind velocity is presented. The tools used for this project are NNTOOL (Neural Network tool) and OPTIM TOOL (Optimization tool) in MATLAB.   - **Undergraduate Academic Project**, **May-2015.**

*"**The Design and Implementation of an E-Commerce Site**"* An E-Commerce Website was designed for online services which is compatible with both MY SQL and No SQL.   -**Database Design, Ongoing Project, January-2017.**