Assign4 Key A 0123456289 (lw \$t1, \$(\$51); AI4I→t1 (lw \$t2, 36(\$51); AI9I→t2 (Sw \$t1, 36(\$51); t1→AI9I (Sw t2, 16(\$51); t2→AI4I (Sar \$t1, \$51, 3 S1: A1 B2 C3 D4 C 3 D 7A1B223D #3. | SW \$51, 4(\$zero); D SW \$52, 8(\$zero); D &W \$51, 6(\$zero); D => \$51: C3 D4 5A 6B => 4 memory accesses. , add \$t1,\$52,\$51; R - register addressing mode (a) lw \$to, 4(\$ti); — I — base/displacement "
bue \$to, \$55, End; — I — pc-relative "
addi \$51, \$51, 2; — I — immediate "

subi \$51, \$51, 1; — I — immediate " J pseudo-direct " 4 1000110100101000 0000 0000 0000 0100 4 1 2 8 9 9 9 4 ボケ・ >> pc: 0000 (28> byte add opcode

28 (Byteaddr.)

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