(#1) See zoom class video
'0111'-for Subtraction and setupat.

(#2) MD AC Ma 1110 - init 0101 0000 0111-) >>1 0000 0000 +0101 0111 + HUD 1011 ->>1 0101 0010 40101 1011+ + MD 0111 1101/21 0011 (+0101 00 0110 ->1 1000 0100

(b) MD AC MQ MQ-1
1010 0000 0101 0 - mit

2'5 0110 0100 0101 0 - MD

0011 0010 1 >>1

+1010

1101 0010 1 - MD

1110 10010 - MD

1000 10010 - MD

1100 0100 1 >>1

+1010

1110 00100 1 + MD

1110 00100 1 >>1

product (30)

(#6) Instead of doing a separate vestore operation in each iteration, it performs either +/-MD in each iteration for that...

(6) x5 = -30

(#4) restoring 1011/0100 = (2:0010 R:0011 0100. 0000 216 0001 0111-1100 +1100 1101 1101 01101-3 (+0100 0110-4 0010 1100-1 (+1100 1110 110 [] -2 110/01-3 1110 (t 0100 1100 -4 100 -1 0101 +1100 100 [ -2 000 100 1 -3 000 1 0011-1 0011 +1100 0010-2 1111 00/10-3 1111 0010-4 0011 Q R (#5) non-restoring AC MP 1011 - unit 0000 0100 0001 01111-1 +1100 1100 0110 -2' 1101 0110 -3 1101 1010 1100-1 (+0100 1100-2 1110 1100-3 1110 100 -1 1101 +0100 100 -2 0001 100 1 -3 0001 001D-17 0011 +1100 0011-2 1111 001回一3 1111 + 0100 0010 4 06/16