

Assignment 3 Key

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|-----|---|-------|---|
| #1. | $M_1 - 900 \text{ MHz}$ | V_S | $M_2 - 800 \text{ MHz}$ |
| | prog1 - $\begin{cases} \text{CPU}_{\text{exe}} = 20 \text{ sec} \\ \text{IC} = 500 \text{ M} \end{cases}$ | | prog1 - $\begin{cases} \text{CPU}_{\text{exe}} = 15 \text{ sec} \\ \text{IC} = 400 \text{ M} \end{cases}$ |

Compute CPI

$$\text{CPU}_{\text{exe}} = \text{IC} \times \text{CPI} \times \text{CCT}$$

$$\Rightarrow \text{CPI} = \frac{\text{CPU}_{\text{exe}}}{\text{IC} \times \text{CCT}} = \frac{\text{CPU}_{\text{exe}} \times \text{rate}}{\text{IC}}$$

for prog1, $\begin{cases} \text{CPI on } M_1 = \frac{20 \times 900 \text{ M}}{500 \text{ M}} = 36 \\ \text{CPI on } M_2 = \frac{15 \times 800 \text{ M}}{400 \text{ M}} = 30 \end{cases}$

| | | |
|-----|--|---|
| #2. | given M_1 | M_2 |
| | prog2 - $\begin{cases} \text{CPI} = 36 \\ \text{CPU}_{\text{exe}} = 8 \text{ sec} \end{cases}$ | prog2 - $\begin{cases} \text{CPI} = 30 \\ \text{CPU}_{\text{exe}} = 10 \text{ sec} \end{cases}$ |

Compute IC

$$\text{IC} = \frac{\text{CPU}_{\text{exe}} \times \text{rate}}{\text{CPI}}$$

for prog2, $\begin{cases} \text{IC on } M_1 = \frac{8 \text{ sec} \times 900 \text{ M}}{36} = 200 \text{ M} \\ \text{IC on } M_2 = \frac{10 \text{ sec} \times 800 \text{ M}}{30} = 266.66 \dots \text{ M} \end{cases}$

#3. $\text{MZPS} = \frac{\text{IC}}{\text{CPU}_{\text{exe}} \times M_i} = \frac{\text{rate}}{\text{CPI} \times M_i}$

for the peak (max) MZPS rate, CPI should be minimal.

| | |
|--|---------------------------------|
| $M_1: \frac{800 \text{ M}}{1 \times M} = 800 \text{ MZPS}$ | — all instr's in IC are type A. |
| $M_2: \frac{900 \text{ M}}{2 \times M} = 450 \text{ MZPS}$ | — all instr's in IC are type B. |

#4.

| | | | CPI(M1) | CPI(M2) |
|------|---|------|---------|---------|
| type | A | 25%. | 1 | 3 |
| | B | 25%. | 2 | 2 |
| | C | 25%. | 3 | 4 |
| | D | 25%. | 4 | 2 |

Compute C_{plex} and Compare

$$\begin{aligned}
 C_{pu-M1} &= IC * CPI * CCT \\
 &= \frac{IC * [(0.25 * 1) + (0.25 * 2) + (0.25 * 3) + (0.25 * 4)]}{800M} \\
 &= \frac{IC * 2.5}{800M}
 \end{aligned}$$

vs.

$$\begin{aligned}
 C_{pu-M2} &= \frac{IC * [(0.25 * 3) + (0.25 * 2) + (0.25 * 4) + (0.25 * 2)]}{900M} \\
 &= \frac{IC * 2.75}{900M}
 \end{aligned}$$

$$\Rightarrow M2 \text{ is } \frac{\left(\frac{IC * 2.5}{800M} \right)}{\left(\frac{IC * 2.75}{900M} \right)} = \frac{2.5}{800} \cdot \frac{900}{2.75} = \frac{1.0227... \times}{\text{faster.}}$$

#5. from #4, $\frac{IC * 2.5}{(X)} = \frac{IC * 2.75}{900M}$

Solve for X. $\Rightarrow X = \frac{(IC * 2.5) * 900M}{IC * 2.75} = \underline{818.1818... MHz.}$

#6. $rate_C2 = \frac{clock_cycles_C2}{C_{plex_C2}} = \frac{1.5 * clock_cycles_C1}{7.5 \text{ sec}}$

$$clock_cycles_C1 = C_{plex_C1} * rate_C1$$

$$= 15 \text{ sec} * 2.5 \text{ GHz} = 37.5 * 10^9$$

double performance
from 15 sec.

$$\Rightarrow rate_C2 = \frac{1.5 * 37.5 * 10^9}{7.5 \text{ sec}} = \underline{7.5 \text{ GHz}}$$