Verification of UDP Protocol
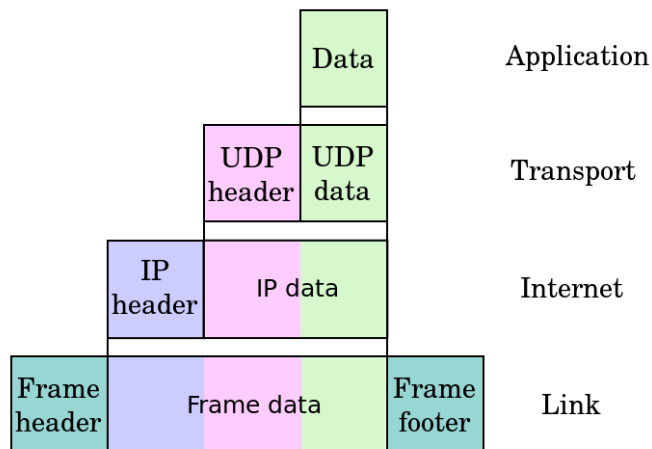
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In a Nutshell

We are proposing to verify an UDP protocol leveraged from opencores.org using UVM environment

About the Design

- The design has the complete UDP stack written in VHDL. (Transmission Layer (UDP – Soft IP), Internet (IP4 – Soft IP), Link (MAC – Encrypted).

Workflow

- Transmission Layer (UDP)
    - o UDP – TX - Sai
    - o UDP – RX - Vadi
    - o UDP – Loopback Test - Manisha

Verification Environment

A reusable UVM testbench would be used to verify the UDP protocol.