**San Jose State University**

**Department of Computer Engineering**

**CMPE 200 Report**

**Assignment 8 Report**

**Title** Pipelined MIPS Processor & I/O Interface

**Semester** \_\_\_\_\_\_\_\_\_\_\_\_ **Date** \_\_\_\_\_\_\_\_\_\_\_\_\_\_

**By**

**Name** \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ **SID** \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

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**Name** \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ **SID** \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

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