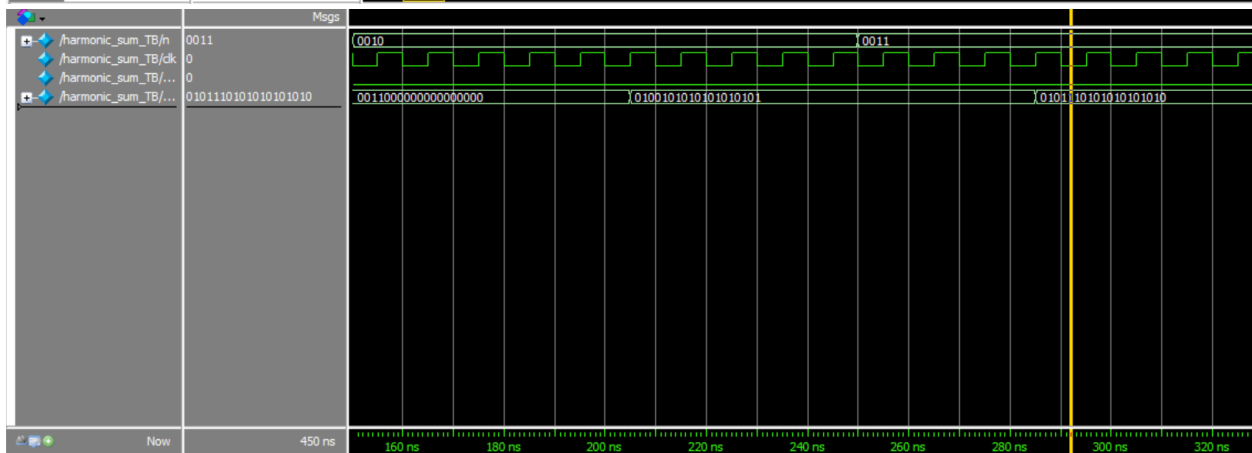
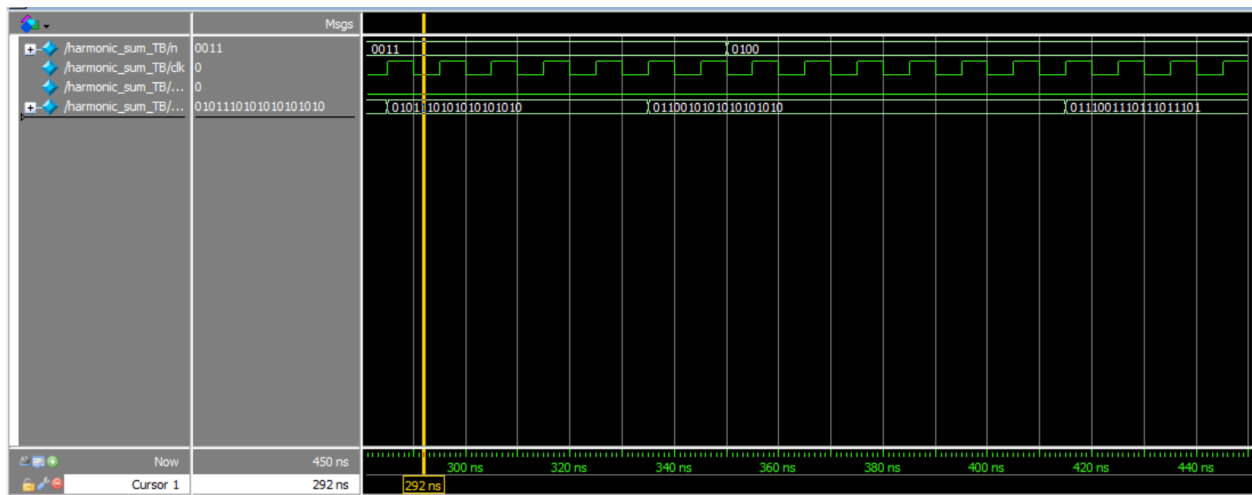
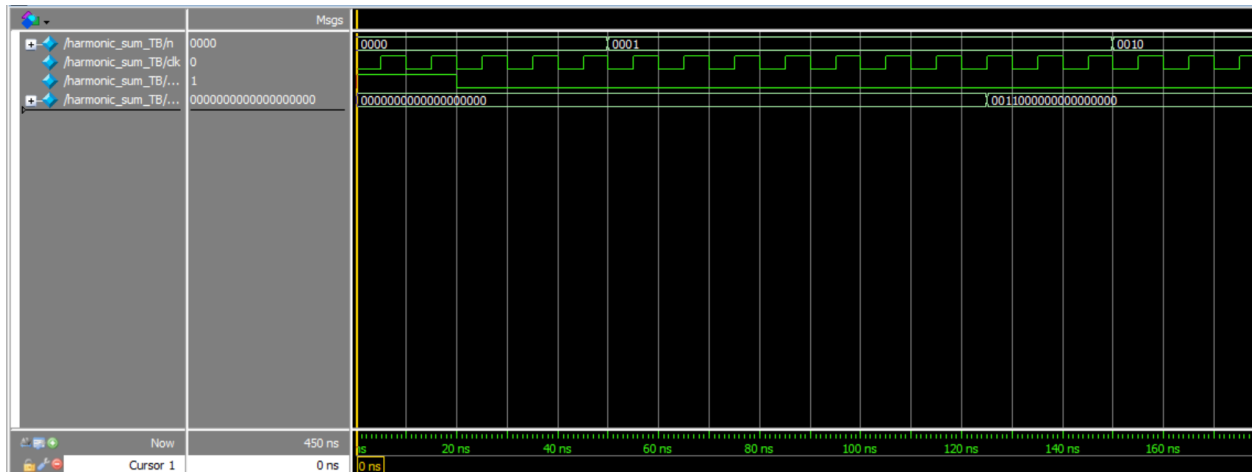
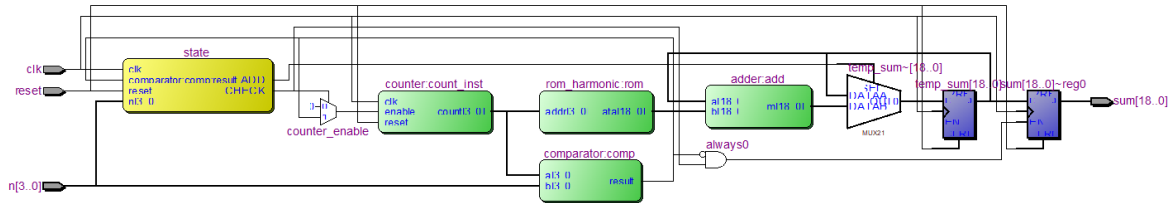


### Harmonic Sum waveform in part 2:

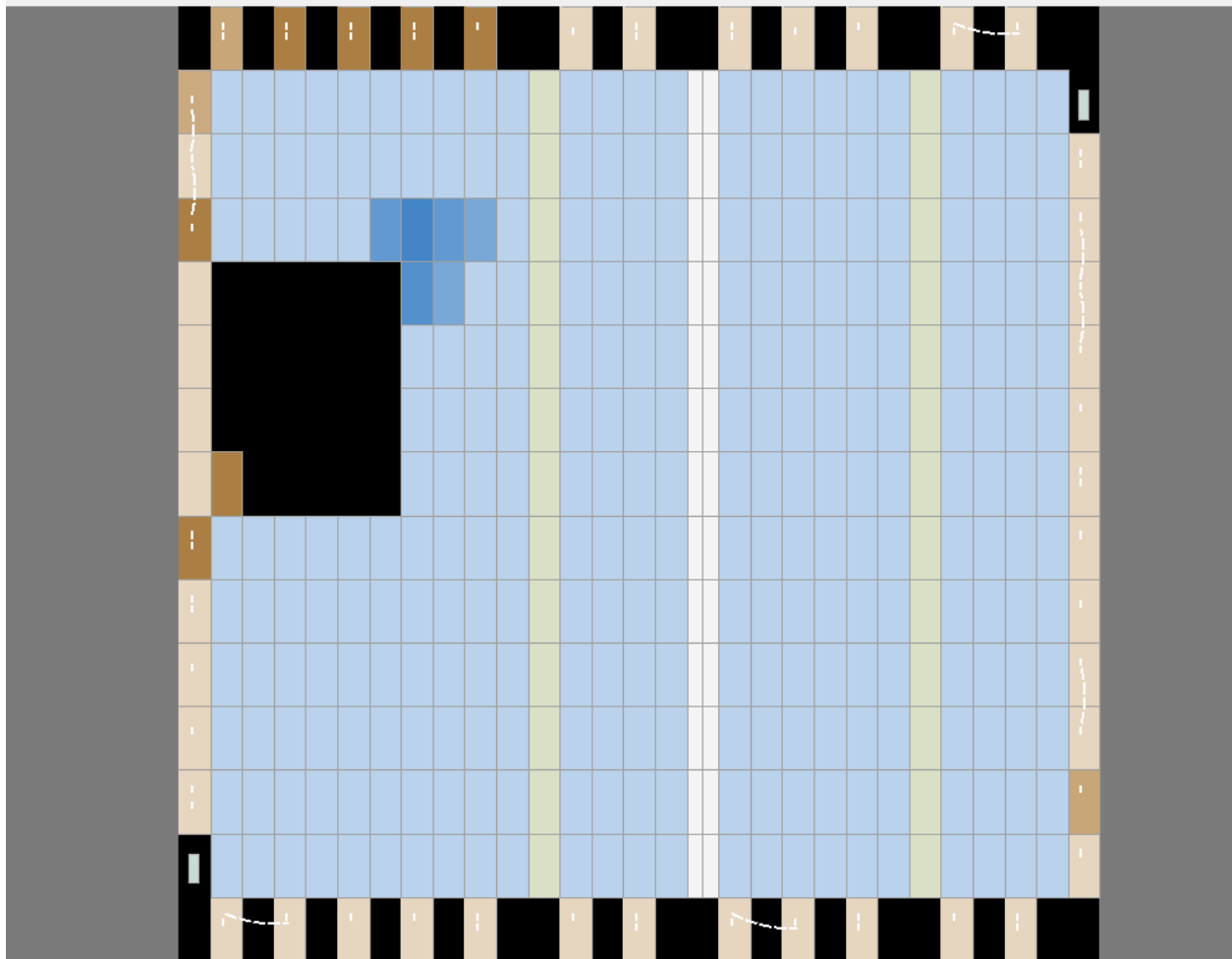


Part C:

RTL viewer:



floor-plan:



Timing:

**TimeQuest Timing Analyzer Summary**

Quartus II Version	Version 13.0.1 Build 232 06/12/2013 Service Pack 1 SJ Web Edition
Revision Name	CA4
Device Family	Cyclone II
Device Name	EP2C5AF256A7
Timing Models	Final
Delay Model	Combined
Rise/Fall Delays	Unavailable

**Flow Summary**

Flow Status	Successful - Fri Dec 20 06:10:28 2024
Quartus II 64-Bit Version	13.0.1 Build 232 06/12/2013 SP 1 SJ Web Edition
Revision Name	CA4
Top-level Entity Name	CA4
Family	Cyclone II
Device	EP2C5AF256A7
Timing Models	Final
Total logic elements	68 / 4,608 ( 1 % )
Total combinational functions	50 / 4,608 ( 1 % )
Dedicated logic registers	47 / 4,608 ( 1 % )
Total registers	47
Total pins	25 / 158 ( 16 % )
Total virtual pins	0
Total memory bits	0 / 119,808 ( 0 % )
Embedded Multiplier 9-bit elements	0 / 26 ( 0 % )
Total PLLs	0 / 2 ( 0 % )