CONCEPT OF REGISTER

Register using D Latches

D_3

D_2

D_1

D_0

Enable

Precise Control is not Possible for the Enable
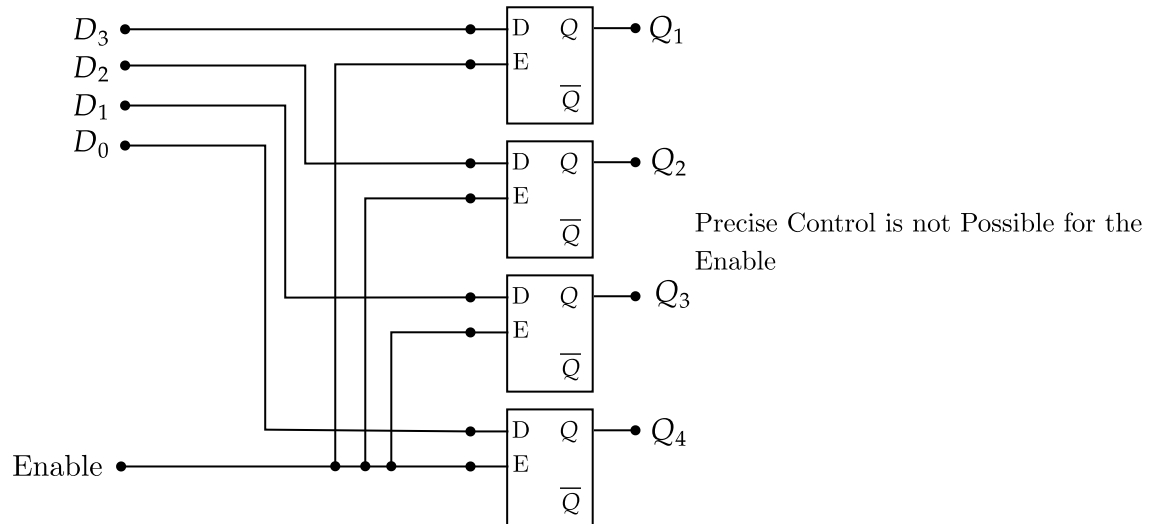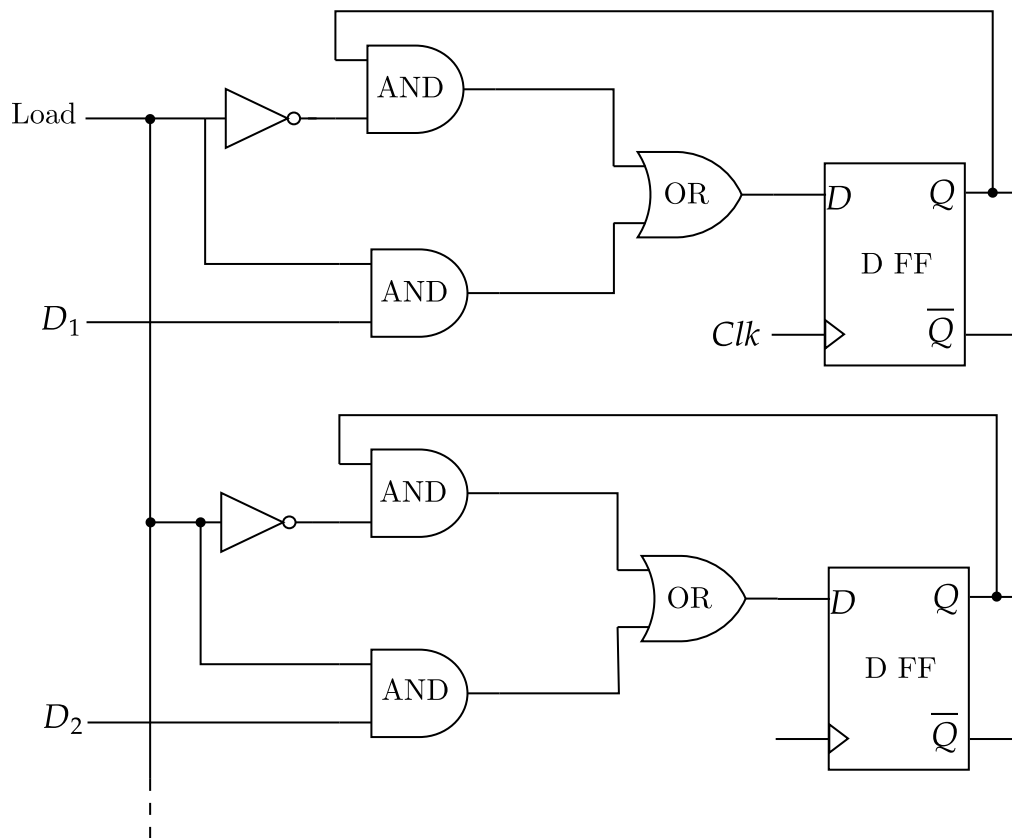
1 bit Register using D Flip Flop

Precise Control is Possible for the with the help of Clock

Load

Data
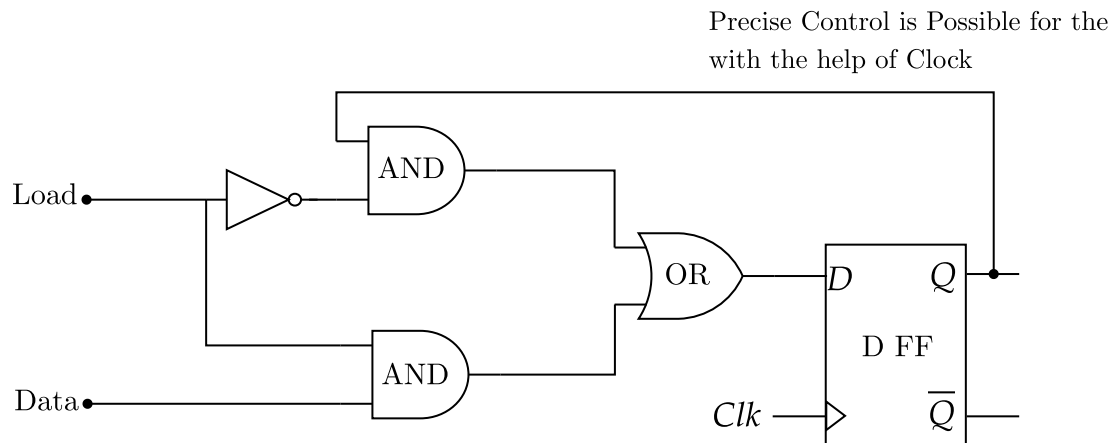
Logic Diagram of Register

Precise Control is Possible for the
Enable When Register designed using D FF

Load

D_1

AND

AND

OR

D   Q

D FF

Clk

$\overline{Q}$

AND

AND

OR

D   Q

D FF

D_2

$\overline{Q}$

8 bit Computer Project
Dr. Sajan P Philip/BIT Sathy

Circuit Diagram of 8 bit Register

8 bit Computer Project
Dr. Sajan P Philip/BIT Sathy