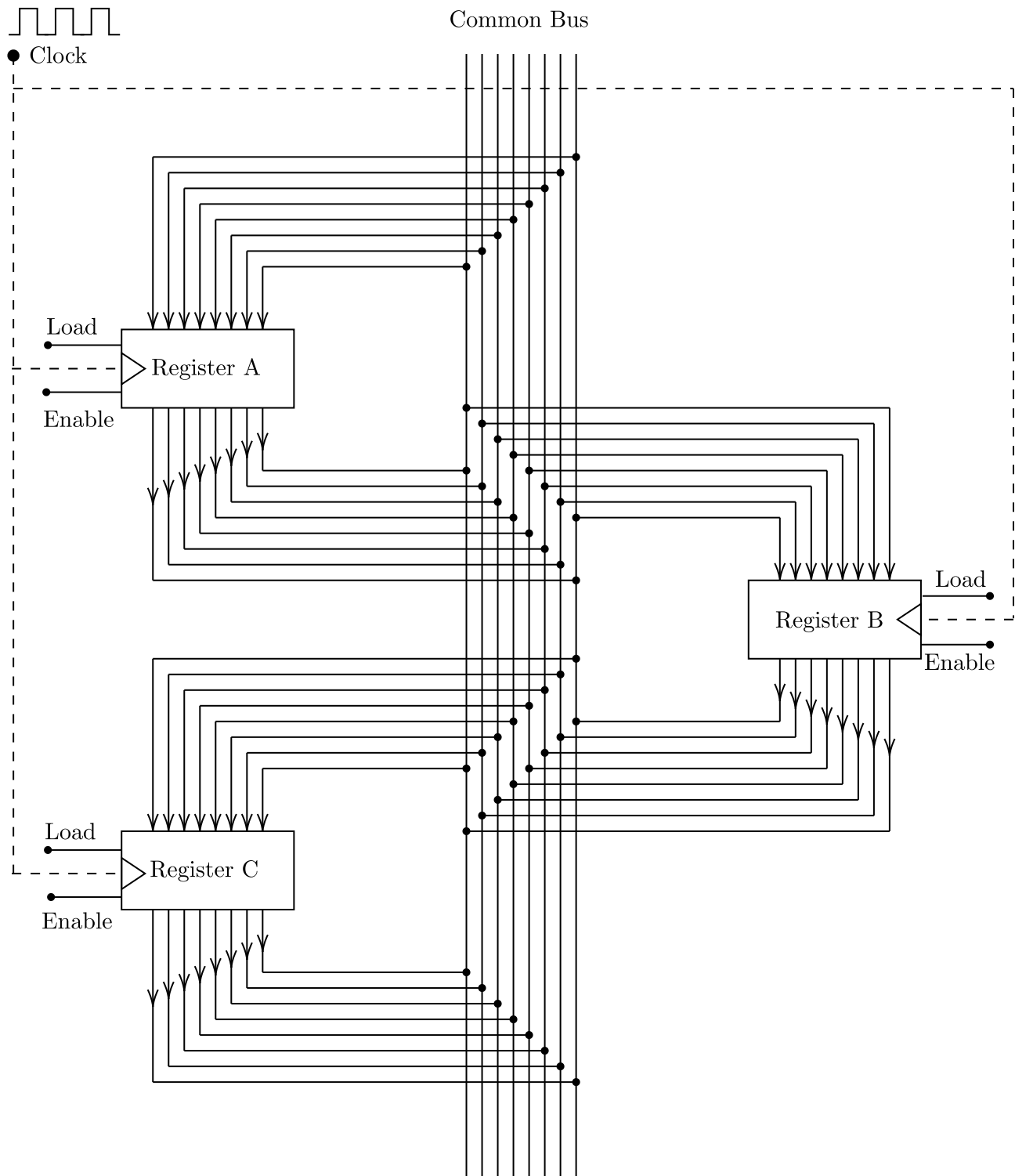
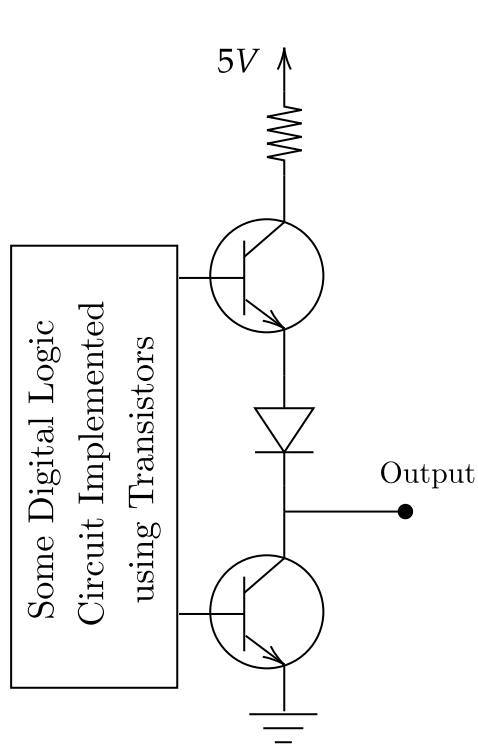


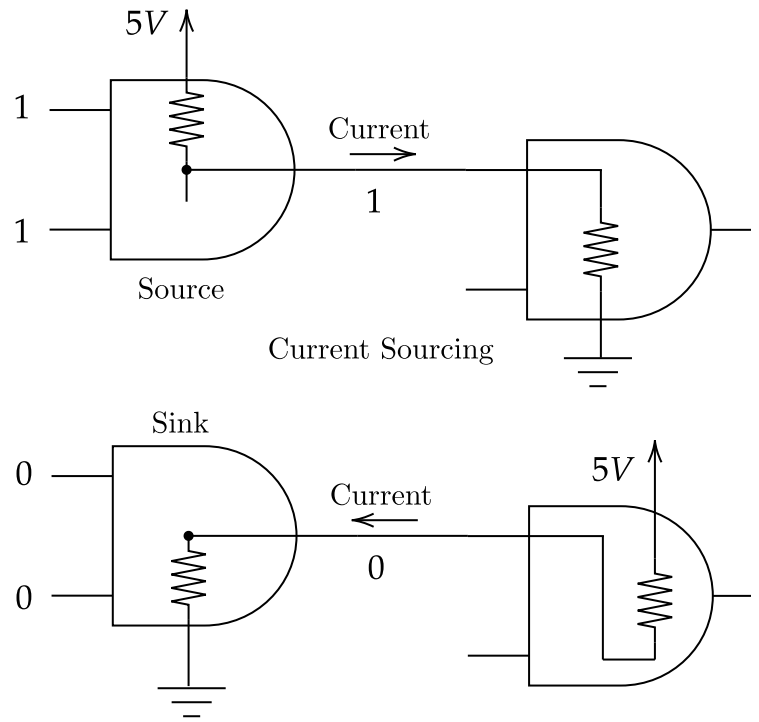
CONCEPT OF BUS AND TRI STATE BUFFER



Current Sourcing and Current Sinking in Logic Gates

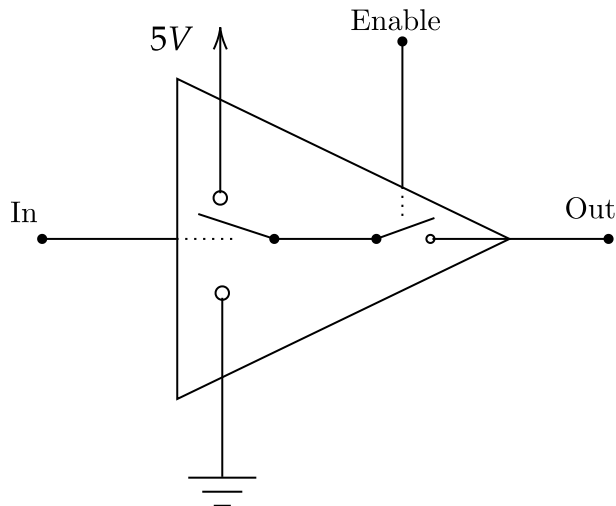


Implementation of Digital Logic

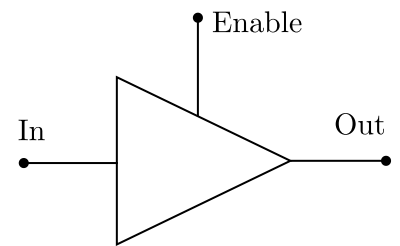


Current Sinking

Tristate Buffer



Implementation of Tri-state Buffer



Symbol of Tri-state Buffer

Example Implementation of Tri State Logic

