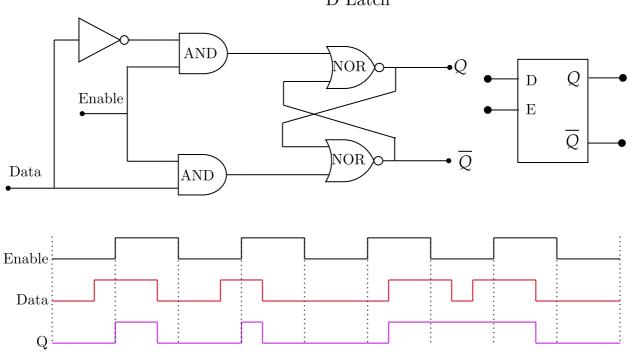
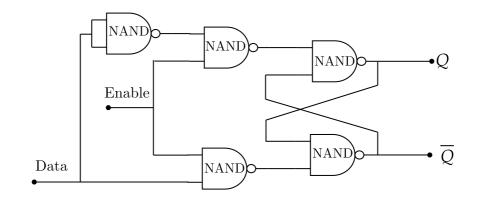
D LATCH & FLIP FLOP

D Latch



D Latch using NAND Gates



D FLIP FLOP

