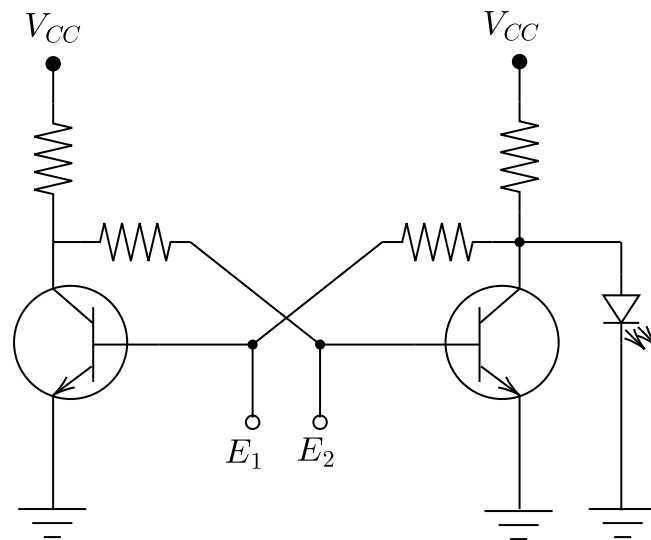
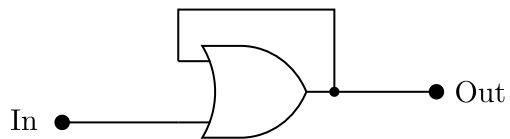


SET RESET LATCH

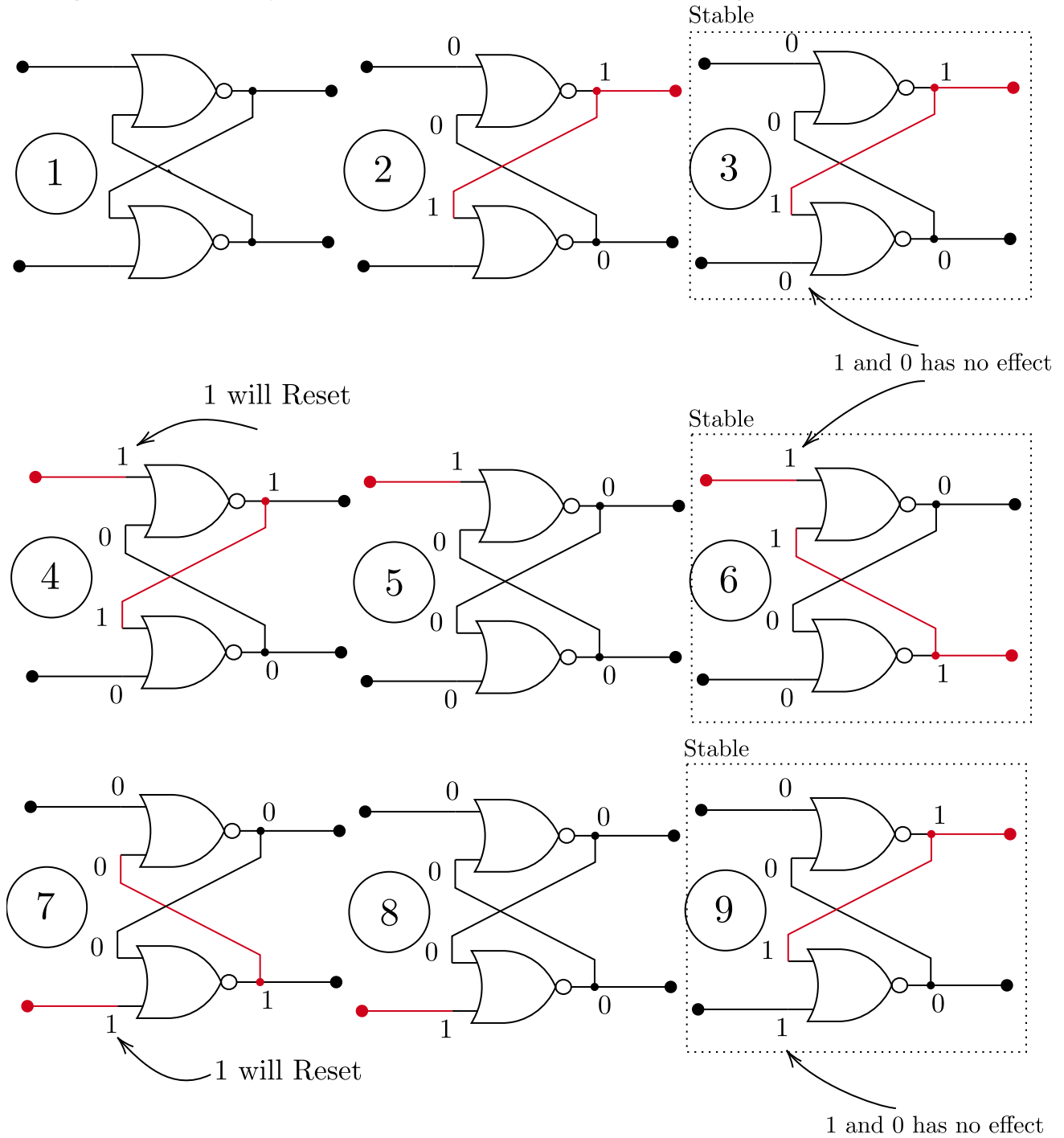
Transparent Latch using BJT



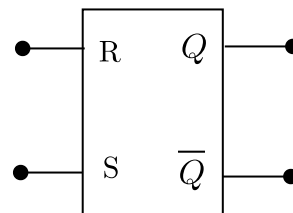
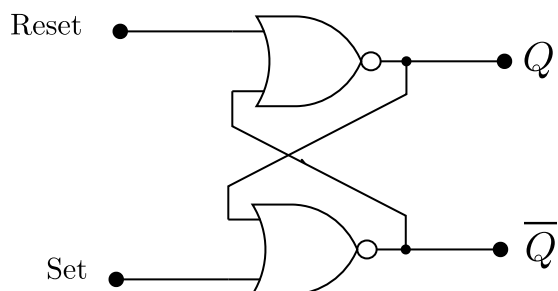
Basic Single Bit Memory without Reset



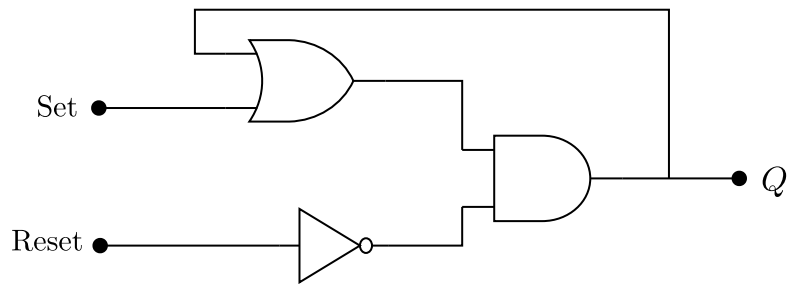
Single Bit Memory with Reset - SR Latch - Working



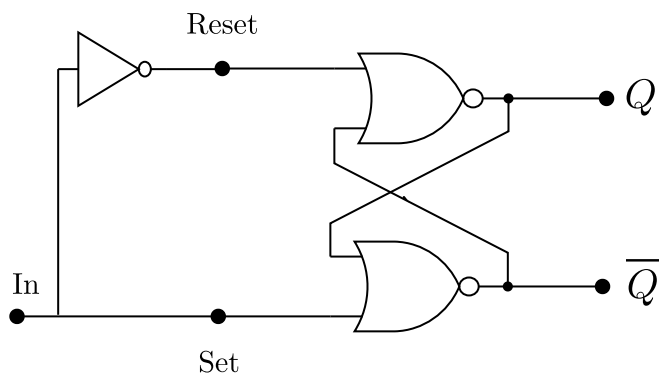
SR Latch - Representation



SR Latch - Variation



SR Latch to D Latch Intermediate Step



This Circuit does not Latch the Value

SR Latch with Enable

