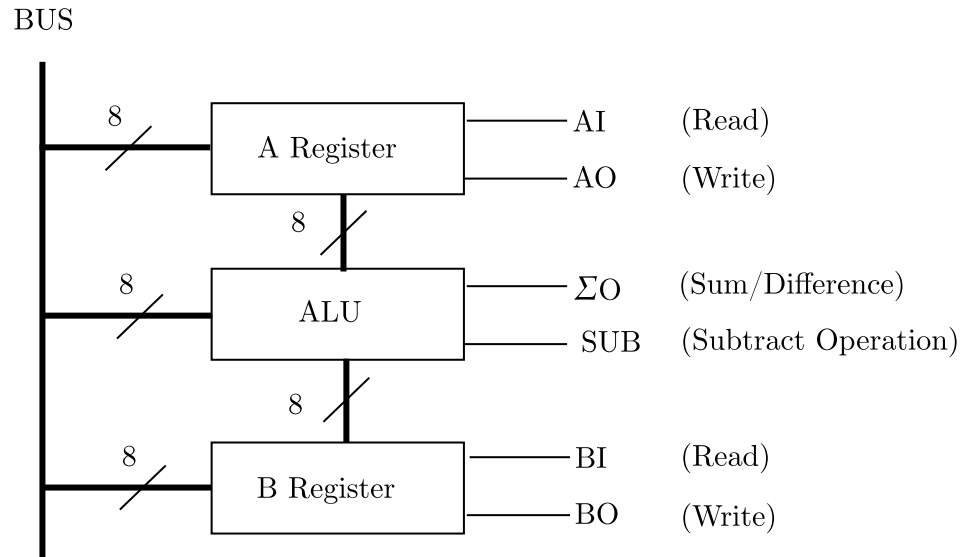


# ARITHMETIC LOGIC UNIT DESIGN

## Block Diagram



# Functional Circuit Diagram

