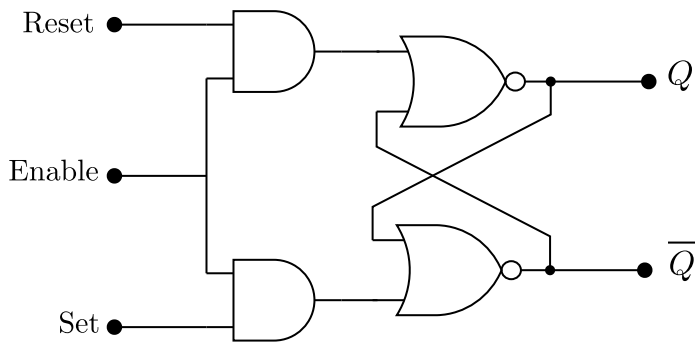
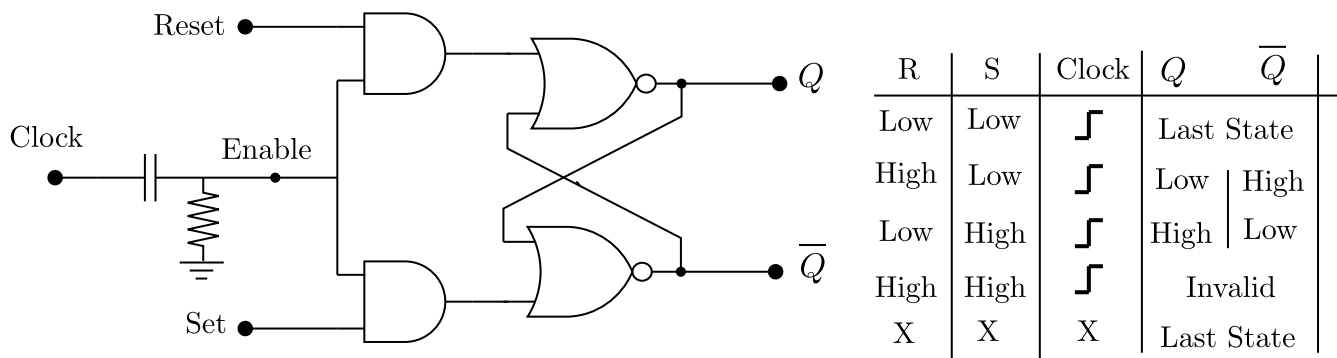


J K FLIP FLOP

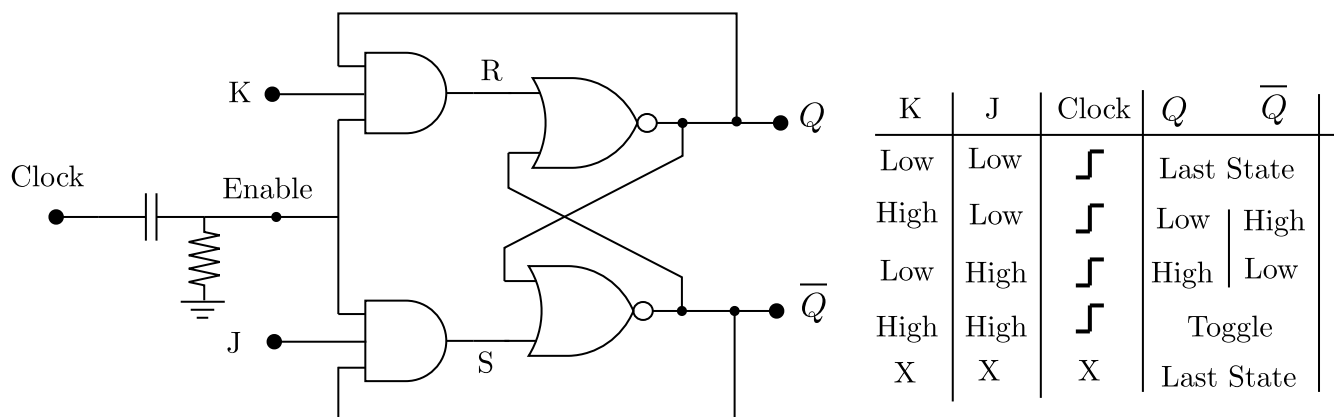
S R Latch with Enable



S R Flip Flop with Clock



S R Flip Flop with Clock



The disadvantage of the Circuit is that it has Race Around Problem

Master Slave J K Flip Flop

