

Inputs
1@8x1

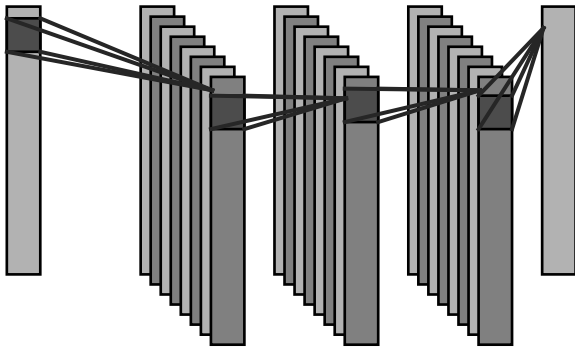
Rec
16@8x1

Rec
16@8x1

Rec
16@8x1

Rec
16@8x1

Rec
1@8x1



Conv
ReLU
1x1
kernel

Conv
ReLU
1x1
kernel

Conv
ReLU
1x1
kernel

Conv
ReLU
1x1
kernel

Conv
Linear
1x1
kernel

Sum
1@1x1

Rec
16@1x1

Rec
16@1x1

Rec
16@1x1

Rec
16@1x1

Outputs
1@1x1



Pooling
Sum
1x1
kernel

Conv
ReLU
1x1
kernel

Conv
ReLU
1x1
kernel

Conv
ReLU
1x1
kernel

Conv
Linear
1x1
kernel