
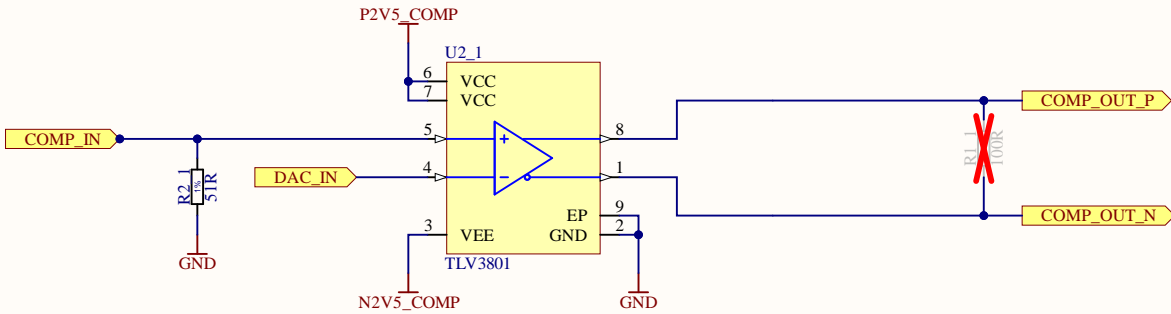

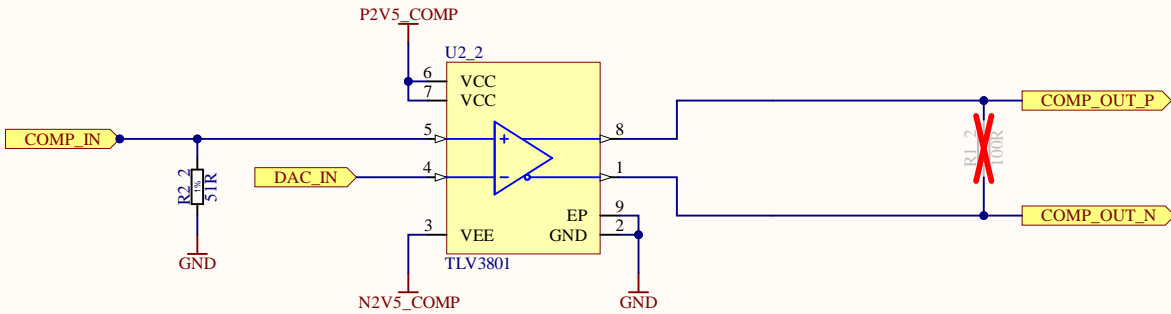



Project:	Time_Sampling_Oscilloscope.PrjPcb	Engineer:	Szymon Jedliński	Revision:	1.0	<i>WEITI ISE</i> <i>ul. Nowowiejska 15/19</i> <i>00-665 Warszawa</i>	
Title:	*	Drawn by:	Szymon Jedliński	Sheet:	1		
File:	TOP.SchDoc	Checked by:	*	Total Sheet:	7		
Variant:	Basic Assembly	Approved by:	*	Date:	29.07.2023		



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Title	*	Drawn by:	Szymon Jedliński	Sheet:	6		
File:	Comparator_circuit.SchDoc	Checked by:*		Total Sheet:	7		
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Title	*	Drawn by:	Szymon Jedliński	Sheet:	6		
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