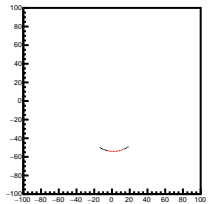
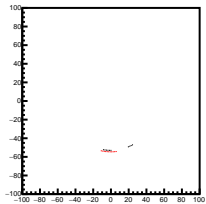


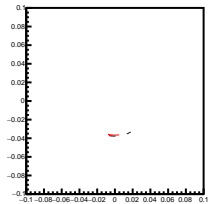
iev 0 ALL - MC XY



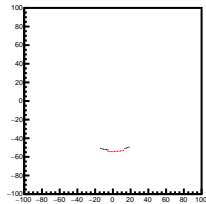
iev 0 ALL - Wire XY@endplate



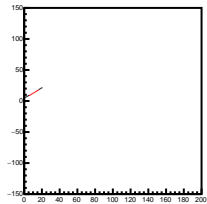
lev 0 ALL - Conformal XY@endplate



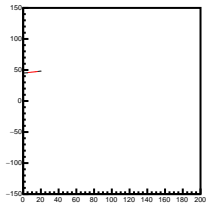
iev 0 ALL - Wire XY @hitz



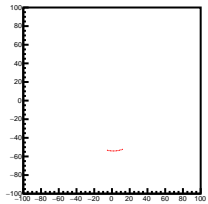
iev 0 ALL - MC Z vs ihit XY



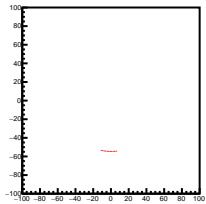
iev 0 ALL - MC PZ vs ihit



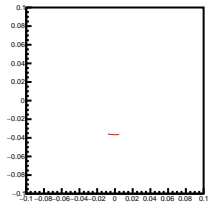
iev 0 EVEN - MC XY



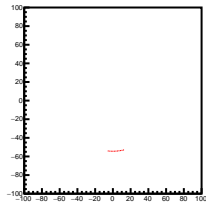
iev 0 EVEN - Wire XY@endplate



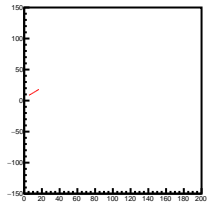
iev 0 EVEN - Conformal XY@endplate



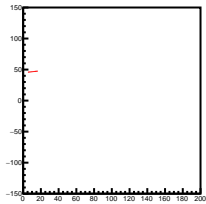
iev 0 EVEN - Wire XY@hitz



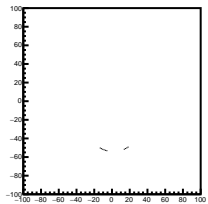
iev 0 EVEN - MC Z vs ihit XY



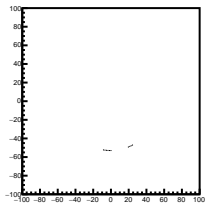
iev 0 EVEN - MC PZ vs ihit



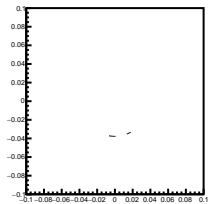
iev 0 EVEN - MC XY



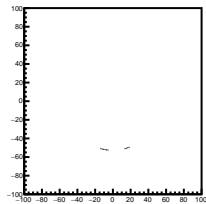
iev 0 EVEN - Wire XY@endplate



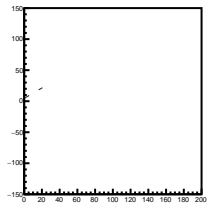
iev 0 EVEN - Conformal XY@endplate



iev 0 EVEN - Wire XY@hitz



iev 0 EVEN - MC Z vs ihit XY



iev 0 EVEN - MC PZ vs ihit

