

# Early evaluation of the hybrid cluster with torus interconnect aimed at cost-effective molecular-dynamics simulations

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**Abstract.** In this paper, we describe the Desmos cluster that consists of 32 hybrid nodes connected by a low-latency high-bandwidth torus interconnect. The interconnect is based on the Angara NIC that supports 3D and 4D torus network topology. We describe the corresponding ASIC structure and the software stack (shmem and MPI including). Firstly, this cluster is aimed at cost-effective classical molecular dynamics calculations. We present strong and weak scaling benchmarks for GROMACS and LAMMPS. Secondly, the cluster serves as a test bed for the Angara interconnect and verifies its ability to unite large MPP systems and to speed-up effectively MPI-based applications.

## 1 Introduction

## 2 Related work

### 2.1 Scalability of classical MD on supercomputers

A lot of work on redesigning software for HPC with GPUs and testing new hardware is done by this moment. MD codes are also become rewritten for running on CPU+coprocessor machines which allow to investigate real size and timescales in MD models. Authors of [1] show how to double the performance of running MD biomolecular model in NAMD [2] with GPUs on Jaguar Cray XK6 machine with Gemini interconnect. In [3] it is described how to achieve an excellent efficiency using cluster with GPUs by using suitable memory access patterns and mechanisms like CUDA streams and profiling tools. The errors propagation in GPU systems is studied in [4].

The Mont-Blanc project is aimed to developing of the supercomputer based on ARM cores [5]. The main advantages of this project are flexibility in designing an application-specific system-on-chip, in turn providing the possibility in balancing performance, energy-efficiency, and cost.

The topology of connections between supercomputer nodes also influences on the scaling of the current simulation. The comparison of torus and fat tree topologies is done in [6] based on SIESTA electronic structure code [7] for *ab initio* MD on six large-scale supercomputers. Torus topologies are showed to demonstrate a better scalability to large system sizes than those implementing fat tree topologies. Torus topology is implemented in 512-node Anton 2 MD supercomputer which allows to achieve simulation rates of multiple microseconds of physical time per day for systems with millions of atoms [8].

High MD code optimization for concrete supercomputer can provide better large-scaling performance than use of wide spread MD packages such as NAMD [2], GROMACS [9], CHARMM [10], LAMMPS and AMBER [11] which can be installed almost on any machine. In [12], authors demonstrate novel code-level and algorithmic improvements to Sandia’s miniMD benchmark [13] and show that the usage of Intel Xeon Phi coprocessor provides up to 2x performance increase. The optimizations of MD code ls1mardyn are done for Intel Sandy Bridge processor including vectorization and shared-memory parallelization which allow to simulate liquid of multi-trillion Lennard-Jones particles on 146016 Cores of SuperMUC. Special MD code is developed in [14] called MODYLAS for machines like K-computer with torus topology. MODYLAS allows to investigate 100 ns-long MD calculation of 10 million-atom systems within 3 days (5 ms/step) by using machines as K-computer. The optimized for Intel Xeon Phi vectorization scheme is developed and applied to speed up LAMMPS calculations in [15]. The accuracy of MD with single precision is also studied.

### 3 Cluster “Desmos”

Table 1: Desmos cluster system configuration.

<b>Server</b>	Supermicro SuperServer 1018GR-T
<b>Processor</b>	Xeon E5-1650 v3 (6 cores, 3.0 GHz)
<b>GPU</b>	Nvidia GeForce GTX 1070 (1920C, 8 GB GDDR5)
<b>Memory</b>	DDR4 8 GB
<b>Number of nodes</b>	32
<b>Interconnect</b>	Angara 4D-torus $4 \times 2 \times 2 \times 2$
<b>Operating system</b>	SLES 11 SP4
<b>Compiler</b>	Intel Parallel Studio XE 2017
<b>MPI</b>	Angara MPI (based on MPICH 3.0.4)

### 3.1 GPU Testing

The Nvidia GTX 1070 cards have no memory errors correction (ECC) in contrast to the professional accelerators. For this reason it was necessary to make sure that there is no hardware memory errors in each GPU.

Testing of each GPU was performed using MemtestG80 [16]. Testing was performed on the available memory of the accelerator (8 GB) and lasted at least 4 hours (depending on the number of test iterations). Testing time was chosen on the basis of the results obtained in the paper [17] and restrictions imposed by the number of cards and the total time of the test the entire batch. There are no errors detected during the testing process.

### 3.2 Angara interconnect

Angara interconnect is a Russian-designed communication network with torus topology. Inteconnect chip was developed by JSC NICEVT and manufactured by TSMC with 65 nm process. The chip supports deadlock-free adaptive routing based on bubble flow control [18], direction ordered routing [19], [20] and initial and final hops [20] for fault tolerance.

Each node has a dedicated memory region available for remote access (read, write, atomic operations) from other nodes to support the OpenSHMEM and PGAS languages. Multiple parallel programming models are supported, including MPI, OpenMP, OpenSHMEM.

The network adapter is a PCI Express extension card that is connected to the adjacent nodes by up to 6 cables (or up to 8 with an extension card). The following topologies are supported: ring, 2D, 3D and 4D torus (or grid).

Fig. 1: The scheme of the “Angara” chip.

Description  
Early HPL performance  
Energy consumption

Fig. 2: The photo of one “Desmos” node and the photo of the rear side of the rack with cabling.

## 4 Experimental Results

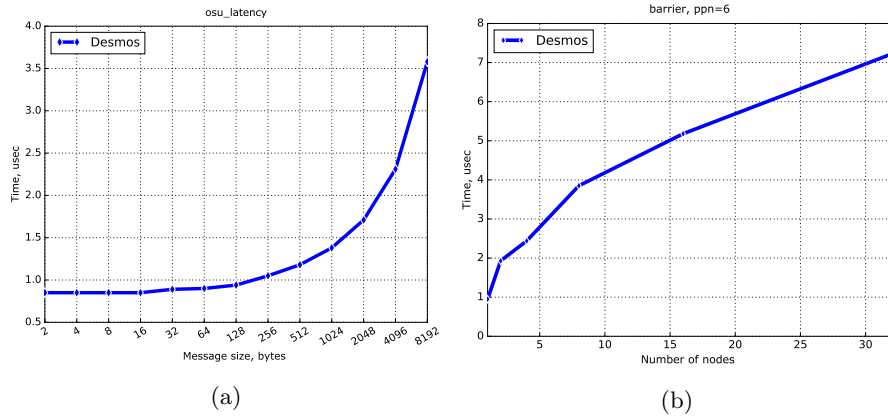


Fig. 3: (a) Communication latency between two adjacent Desmos nodes (OSU Micro-Benchmarks 5.3.2). (b) Intel MPI Benchmarks 2017 MPI\_Barrier, processes per node (ppn) = 6.

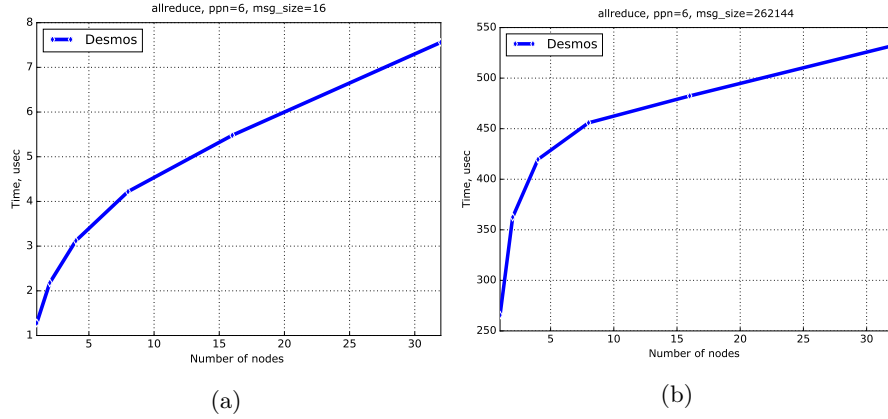


Fig. 4: FIXME: REMOVE?? Intel MPI Benchmarks 2017 MPI\_Allreduce, ppn = 6 (a) message size = 16 bytes (b) message size = 256 KB.

#### 4.1 ApoA1 benchmark

The Apolipoprotein A1 in water ( $\sim 100k$  atoms) system (ApoA1) is used to compare Desmos cluster with other supercomputers. The performances of different clusters based on ApoA1 test are shown in Fig. 5 in terms of seconds per 1 atom for 1 MD step and declared peak performance (3.236 TFlops per node consist of 6 core Xeon performance which is 0.336 TFlops and GPU performance which is estimated as 2.9 TFlops). The dotted line shows ideal scalability with performance 0.1 MFlops/atom/step.

Table 2: Info about different clusters.

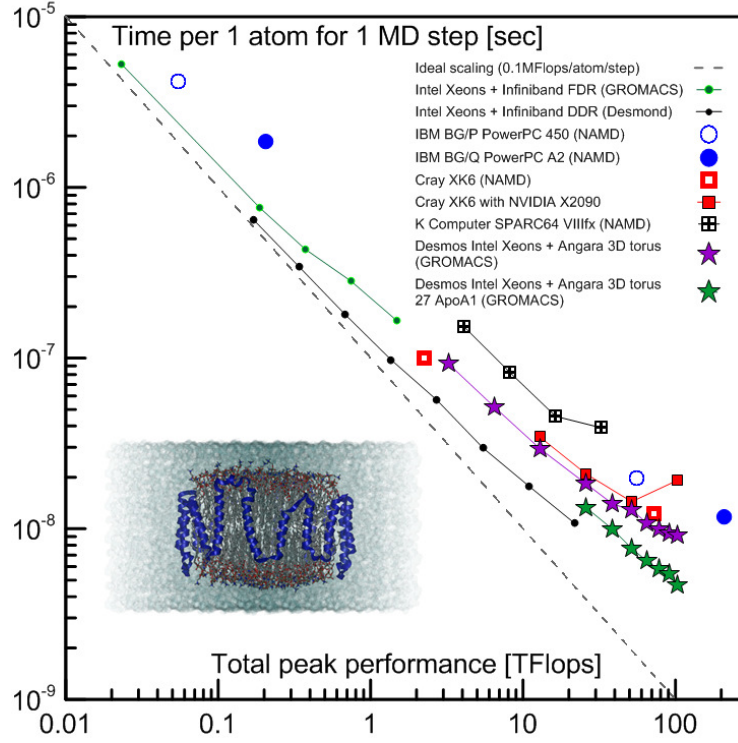


Fig. 5: ApoA1 benchmark. Timestep per 1 atom per 1 MD step vs  $R_{peak}$ . The comparison of different systems.

Despite different architectures and MD software, this data can be used to analyze the applicability of some supercomputer systems for biomolecular MD system. Black and green lines with dots show the performance of CPU cluster with Intel Xeon nodes that consist of 28 cores [21]. New Desmos machine (purple filled stars) with GROMACS shows better scalability than CRAY XK6 with NVIDIA X2090 [1] (red filled squares) and K Computer SPARC64 VIII fx with NAMD [?]. It also outgo benchmarks ran on IBM BlueGene/P and BlueGene/Q [22] (blue open and filled points correspondingly).

Green stars are 27 times replicated ApoA1 benchmarks on Desmos machine. This result shows the continuous scaling with larger number atoms per node. It indicates the possibility for adding new nodes to Desmos without lack of performance.

## 4.2 MEM and RIB benchmarks

The work [23] gives a good guidelines for achieving the best performance for a minimal price in 2015. Authors compare different configurations of supercomputers using two wide spread biological benchmarks (are available at [24]): membrane channel protein embedded in a lipid bilayer surrounded by water (MEM,  $\sim 100k$  atoms) and ions and bacterial ribosome in water with ions (RIB,  $\sim 2M$  atoms). GROMACS package is used for all tests. The longer simulations than in [23] are performed to obtain the best performance.

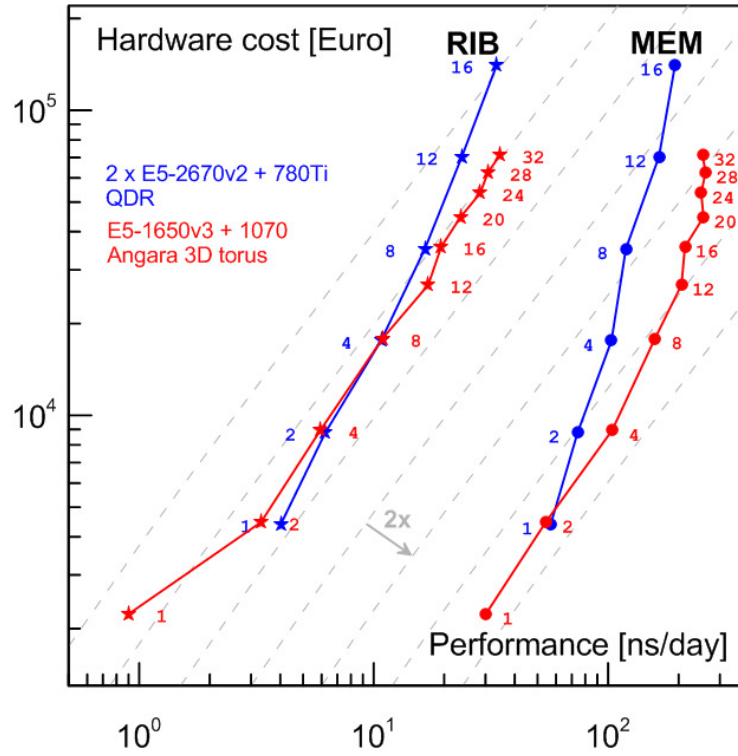


Fig. 6: The cost of hardware vs achieved performance for MEM and RIB benchmarks. Dashed lines show ideal scaling. Comparison with the published results for GROMACS in [23].

We compare the results obtained on Desmos cluster with the best choice of [23]: the nodes consist of 2 sockets Xeon E5-2670v2 with 780Ti and connected via InfiniBand QDR. The costs of hardware in euros are displayed on Y axis and the performance in ns/day is shown on X axis in Fig. 6. The numbers show the amount of nodes used. Grey lines indicate ideal scaling.

Desmos (red color) shows better weak scaling for MEM benchmark than system configuration provided by [23] (blue color). The saturation is achieved after 20 nodes which corresponds to the small amount of atoms per node. In the case of RIB benchmark, Desmos demonstrates ideal scaling after 16 nodes which shows that the productivity can be increased with more number of nodes.

## 5 Conclusions

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