B.E. (Information Technology) Seventh Semester (CBS)

Elective-I : Compiler Design

AHK/KW/19/2352 P. Pages: 2 Time: Three Hours Max. Marks: 80 Notes: All questions carry marks as indicated. 1. 2. Solve Question 1 OR Questions No. 2. 3. Solve Question 3 OR Questions No. 4. Solve Question 5 OR Questions No. 6, 4. Solve Question 7 OR Questions No. 8. 5. 6. Solve Question 9 OR Questions No. 10. Solve Question 11 OR Questions No. 12. 7. 8. Assume suitable data whenever necessary. 9. Illustrate your answers whenever necessary with the help of neat sketches. Explain the structure of realistic compiler with block diagram. Explain different types of compiler. OR Explain Bootstrapping & cross-compiler. What are the different types of compiler writing tools? Explain each. b) 3. Construct LALR parsing table for the given grammar a) $S \rightarrow AA$; $A \rightarrow aA/b$ b) Is the following grammar LR(10) or not? $S \rightarrow wAz/xBz/wBy/zAy$; $A \rightarrow r$; $B \rightarrow r$ OR Compare SLR, CLR & LALR parser. a) Check whether the given grammar is LR (0) or not? b) $S \rightarrow Aa/bAc/Bc/bBa$; $A \rightarrow d$; $B \rightarrow d$ 13 5. Draw parse tree, generate TAC and write translation scheme for : if (A > B and not B > C) or (C > D))then while (C > D) do D = D + 1else D = D -

OR

13

Draw labeled parse tree & translate the following statement to intermediate code using array reference. Array a & b have size 30 x 40 while array c & d have size 20. BPW = 4. c[a[i, j] + c[a[i, j]] + d[i+j]

AHK/KW/19/2352 1 P.T.O

- 7. What is symbol table? Explain data structure for symbol table management.

Explain different storage allocation strategies.

OR

Explain with block diagram, the basic structure of activation record. a)

- b) Explain different types of errors in compilation process. Also explain error detection & recovery for LL & LR parser.

Draw program flow graph for following code.

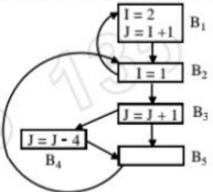
```
fact (x)
int f = 1:
for (i = 2; i < x; i++)
```

f = f * i: return (f);

Explain the procedure to eliminate induction variable with suitable example,

OR

10. Compute IN and OUT computations & u-d chain for following data flow graph. 14



- 11. Consider the following code:
 - $t_1 = a + b$; $t_2 = c + d$; $t_3 = e t_2$; $t_4 = t_1 t_3$

13

- Generate DAG and implement labeling algorithm.
- Write gencode algorithm & generate code using gencode() function. ii)

Explain peephole optimization.

- Explain machine model for code generation. Also explain the problems that hinder good code generation.