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## B.E. (Computer Science Engineering) Fourth Semester (C.B.S.)

## **System Programming**

P. Pages: 3 NRT/KS/19/3382

Time : Three Hours

\*\*0 6 4 4 \*

Max. Marks : 80

Notes: 1. All questions carry marks as indicated.

- 2. Solve Question 1 OR Questions No. 2.
- 3. Solve Question 3 OR Questions No. 4.
- 4. Solve Question 5 OR Questions No. 6.
- 5. Solve Question 7 OR Questions No. 8.
- 6. Solve Question 9 OR Questions No. 10.
- 7. Solve Question 11 OR Questions No. 12.
- 8. Due credit will be given to neatness and adequate dimensions.
- 9. Assume suitable data whenever necessary.
- 10. Illustrate your answers whenever necessary with the help of neat sketches.
- 1. a) Explain Pass 1 of Assembler in detail with the help of flowchart and databases.
  - b) Explain following terms with help of examples.
    - i) USING

ii) BALR

iii) DS

iv) DC

v) LTORG

vi) DROP

OR

1

2. For the given AL code get Base Table Literal Table, Symbol Table, Machine opcode
Table along with machine code

Tuble diong with machine code						
PROG	START	0				
	BALR	15, 0				
	USING	*, 15				
	LR	5, 15				
	LH	1, Data 1				
	USING	*, 10				
	BR	14				
Data 2	DC	F '11'				
Data 1		H '22'				
PQR		H '23'				
BCK		F				
MN	EQU	1				
XY	EQU	2				
	BALR	2, 0				
	USING	* + MN, PQR				
	LA	7, = A (BCK)				
	BR	6				
	DC	H '64'				
	DROP	XY				
	L	9, = A (Data 1)				
	A	9, PQR				
	LTORG	,				
	ST	9, = F'100'				
	END	,				

8

6



- **3.** a) List the names of databases used in pass 1 and pass 2 of microprocessor.
  - b) Explain conditional macro expansion and Macro Instruction defining Macro facilities Brief your answer when, how and which MACRO facilities are implemented for a particular AL code.

OR

5

8

13

5

8

**4.** Get MDT, MNT, ALA and expanded code for the following code fragment -

MACRO XYZ & A

MACRO

& A & B

MACRO

& B & C

ST 15, A (& C)

BALR 14, 15

L 1, = F'3'

A 1, & C

ST 1, & C

**MEND** 

**MEND** 

MEND

PROG START

BALR

XYZ MIT HELLO

HELLO DI

DI DC F'10'

**END** 

- 5. a) How relocating loader performs the function of loader? Explain in detail.
  - b) Why 2 passes are required by direct linking loader? Draw and explain flowchart of pass 2 of loader.

OR



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6.		Show the entries in ESE JOHN START ENTRN EXTRY BALR USING SR L A ST BR FIVE DC PGC DC PGD DC DC DC DC DC END	PGA, PGB PGC, PGD 15, 0 *, 15 4, 14 1, FIVE 2, FIVE 2, FIVE 14 F '4' A (PGA + 4) A (PGC-PGB) A (PGD-PGC) A (PGD)	13	
7.	a)	Explain the COFF symbol table and string table.		8	
	b)	Write a short note on Link editor.		5	
			OR		
8.	a)	Explain Source Code Co	ontrol System (SCCS).	7	
	b)	Explain the GNU debugger. Enlist the findings concred by GNU debugger.			
9.	a)	Explain the different types of Unix Device Drivers.		8	
	b)	Explain Gross anatomy of Device Drivers.			
			OR		
10.	a)	Explain the process of Driver installation on Unix System in detail.		8	
	b)	Compare the device drivers for Unix and Windows Operating System.			
11.	a)	Explain database and algorithm for lexical phase of compiler.		7	
	b)	Explain YACC in detail	1.	7	
	OR				
12.	a)	What is cross compiler	? How does it use bootstrapping concept ?	7	
	b)	Describe a tool for study	y of Lex.	7	

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The best time to plant a tree was 20 years ago. The second best time is now.

~ Chinese Proverb

